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DS 073009

Memorandum of Understanding

This Memorandum of Understanding is entered into at Thiruvananthapuram on this 30th of June 2022.

BETWEEN

St. Joseph's College of Engineering and Technology, hereinafter referred to as SJCET, which expression shall, unless it be repugnant to the context or meaning thereof to be deemed to mean a legal person, represented by The Principal, St. Joseph's College of Engineering and Technology, Choondacherry P.O,Palai, Kottayam, 686 579 of the ONE PART,

AND

Centre for Development of Advanced Computing, Vellayambalam, Thiruvananthapuram a Constituent Unit of C-DAC, A Scientific Society under the Ministry of Electronics and Information Technology(MeitY), Government of India, registered under the Societies Registration Act of 1860and Bombay Public Trusts Act of 1950, having its Registered office at Savitribhai Phule, Pune University Campus, Ganesh Khind, Pune-411007 hereinafter referred to as C-DAC(T) which expression shall, unless it be repugnant to or inconsistent with subject or context thereof, include and be deemed to mean a legal person, represented by The Director, C-DAC(T) of the OTHER PART.





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DS 073010

JUN 2022

Either one will be referred as "party" and both will be referred as "parties" unless it be repugnant to or inconsistent with subject or context thereof.

1. AREA AND SCOPE OF COOPERATION

The purpose of this Memorandum of Understanding(herein after referred to as MoU) is to formally express the mutual wish of SJCET and C-DAC(T) to associate in the studies and research in the subject areas of Cyber Security and Forensics, Cyber Law, Software Technologies, Cloud Computing, GIS Technology,5G Networks, Power Electronics, Control Systems and Instrumentation, VEGA Processor based Microcontrollers and System-on-Chipsand in the efforts of Collaborative Teaching, Research and Development. This is a legally non-binding agreement, which outlines only an intention to promote collaboration between the two parties for a socially productive cause. This document would be followed by an Agreement, in future, specifying the areas of collaboration and sharing, which will have the legal binding.

Generally speaking, and within the limits of the financial means of each party, the cooperation will take the following form, as per the mutually agreed terms & conditions, from time to time:

1. Expert Lectures: C-DAC (T) nominate its scientists/engineers to deliver expert lectures in the areas of Cyber Security and Forensics, Cyber Law, Cloud Computing, Information Security, Power Electronics, VEGA Processors, Renewable Energyand allied fields and any emerging topics suited to the requirements of B.Tech/M.Tech/Ph.D programmes conducted by SJCET, at the discretion of C-DAC (T).

Collaborating in R&D Projects: As SJCET has expertise in the different basic disciplines of Engineering viz, Computer Engineering, Electronics Engineering, Electrical Engineering, US TA

Mechanical Engineering, and, Civil Engineering;it's faculty and students may approach C-DAC (T)to take up project modules of C-DAC (T).

- C-DAC (T) and SJCET may also prepare joint project proposals and approach funding agencies for sponsoring the research and development projects.
- SJCET may request C-DAC(T) for setting up facilities in advanced areason its campus as per the needs of the institution and the cost of these may be worked out on mutually agreeable terms
- Joint Programmes: take efforts in organising joint conferences, methodology workshops, professional development programmes, training programmes, Hackathons etc. for the benefit of the research community
- Student Projects: C-DAC (T) may offer the required technical guidance and required facilities for undertaking the approved projects of M.Techstudents.
- Activities: Both the parties to mutually consult and take approval from each other, before publishing any information derived as part of the joint activities.
- Development: CDAC (T) to support SJCET by providing evaluation boards and training in the development of technological solutions using the indigenous processor families.
- Industrial training: C-DAC (T) may make offersfor the industrial training (internships) for the students of SJCETfor enhancing their professional skills; or as part of curriculum requirement of the B.Tech/ M.TechProgrammes.
- Pre-placement trainings: The students of B.Tech programme may be given pre-placement training on relevant domains of technical skills under a programme custom designed as per market requirement.
- 11. Training: C-DAC (T) may offer training programs to the students and faculty of SJCET
- Faculty Exchange: Wherever possible, parties to support each other in teaching, research, development, and professional training activities.
- 13. Collaboration: Both parties will extend invitation to the teachers/researchers/ developers of the other party to participate in seminars, conferences, courses and meetings on research themes of common interest.
- Joint Guidance: Subject to relevant academic regulations, parties will promote co-direction or co-supervision of master's dissertation works and doctoral researches.
- 15. Any other project of common interest suggested by either of the two parties.

2. PERIOD

The present Memorandum of Understanding takes effect as from the date of signature of this Memorandum of Understanding by both parties; and remains valid for a period of 3 years. It can be renewed, on expiry of this term, for one or several terms, if agreeable to both the parties.

If one party plans to withdraw from the agreement, it must give notice in writing 3 months in advance to the other party, on the understanding that any ongoing actions should be carried through to a successful conclusion.

Notwithstanding anything given in this MoU, both parties can together decide to close the MoU if they arrive at conclusions.







3. DISPUTE RESOLUTION AND ARBITRATION

In the event of dispute or a difference of any nature whatsoever, between the parties during the course of performance of the respective obligations arising out of the agreements made pursuant to this MoU, the parties will agree to refer the matter to the Heads of the Institutions to resolve the disputes, keeping in view the best interest of the parties and to maintain the spirit of performance of this agreement.

If any dispute still persists, it shall be governed by the provisions of alternate dispute resolution and settled in accordance with the relevant provisions of Arbitration and Reconciliation Act1996, as amended from time to time.

If any judicial proceedings arise, it shall be conducted at the appropriate courts in Thiruvananthapuram, Kerala.

4. FORCE MAJEURE

Without prejudice to accrued liabilities and rights, no party shall have any liability, whatsoever, to the other party or be deemed to be in default by reason of delay or failure in performance under this MoU, to the extent that such delay or failure is caused by or arises from acts or circumstance or events beyond the reasonable control of that party, including but not limited to Acts of God, Acts or Regulations of any governmental authority, war or national emergency, accident, flood, fire, riot, strikes, lock-outs, industrial disputes, natural catastrophes or epidemics/pandemics, Court Orders etc. Each Party shall bear its own losses arising from such force majeure event(s), if any.

5. SEVERABILITY

If any part of this Agreement is found by a court of competent jurisdiction or other competent authority to be invalid, unlawful or unenforceable, then such part will be severed from the remainder of this Agreement, and the remaining part will continue to be valid and enforceable to the fullest extent permitted by law.

6. INTELLECTUAL PROPERTY

All prior information, Intellectual Properties(IP) such as patents, design and data existing with either party before the signing of this MoU (pre-existing IP) shall be the sole property of the party concerned. All IP including, design information, designs, copyrights, software architecture, framework, source codes and data generated through the collaboration or patents under this MoU shall be held as per the mutually agreed terms, which shall be arrived at before approaching the registering authorities for protection of the IP. This agreement shall be specifically made in writing and shall alsocontainthe terms of joint ownership of the IP as per the guidelines of the funding agency, if such an agency is involved in the implementation of the specific project from which the IP has been generated.

The terms and conditions of this MoU shall not be disclosed to any third parties by any party of this MoU without the prior written consent of the other party. Either party shall, however, allow, without prejudice, other party to cite the existence of the MoU in their official reports.



(Ino)

This Memorandum of Understanding shall be governed by the laws of Union of India and State of Kerala and shall be under the superintendence of the parent bodies of the parties.

IN WITNESS WHEREOF, the parties hereto have caused this Memorandum of Understanding(MoU) to be executed in duplicate, through their representatives at Thiruvananthapuram in the day and year first above written:

Now the Memorandum of Understanding witnesses as follows कले सेलवन ए./ Kalai Selvan A.

वैज्ञानिक जी / Scientist G निदेशक / Director **Authorized Signatories:** प्रगत संगणन विकास केन्द्र Centre for Deformand on behalf of C-DAC(T) For and on behalf of SJCET Vellayambalam, (Signature) (Signature) Name: Dr. V P Devassia Name: Mr. Kalai Selvan A Designation: Director, C-DAC(T) Designation: Principal, SJCET Place: Thiruvananthapuram Place: Pala Witnesses: Witnesses: (Signature) (Signature) Name: . HAND E GRIDO E Designation: Designation:



प्रगत संगणन विकास केन्द्र CENTRE FOR DEVELOPMENT OF ADVANCED COMPUTING

लंघर एवं सूचना प्रौरांगिकी लंबातय की वैद्यानिक संस्था, भारत सरकार A Scientific Society of Ministry of Communication and Information Technology, Government of India

> सोफ्टवयर प्रशिक्षण एवं विकास केन्द्र Software Training And Development Centre



40/8147 ती, भारतकातारा रोड, कोच्यी - 682 035, इंडिया, फोश: +91 484-2372422, 2369972 फेश्म : +91 484-2372427 40/8147 C, Nerakathara Road, Kochi-682 035, India. Ph: +91 484-2372422, 2369972 Fax: +91 484-2372422 e-mail ID; stdekochi@cdac.in

66/A180

CDACK/PROJECT TRAINEE/20-21/01

24 June 2020

CERTIFICATE

This is to certify that Ms. Anju Thomas has been working in the Live Project "Academic Solution for University" for CDAC's Client, as a Project Intern, during the period from 20th January 2020 to 20th May 2020.

During the above period she has been working in Spring boot 2.2.1, Jquery 3.5.1, Bootstrap CSS 3.3.7, Jquery 3.3.1, Postgres 11.7 and CDAC's Fibre root framework.

Mrs. Sundha Co Centre Head CDAC – KRC Kochi



ST.JOSEPH'S COLLEGE OF ENGINEERING

COLLEGE OF ENGINEERING AND TECHNOLOGY, - P A L A I -

Technical Talk on Vega Processors and Ecosystem

Overview

Event Name	Technical Talk on Vega Processors and Ecosystem
Date	17.05.2022
Venue	MTB Seminar Hall
Focus of the event	Research Methodology
Organizing Body	Department of Electronics and Communication Engineering
Resource Person	Mr. Libin T T, Joint Director, CDAC
Total Number of Attendees	137



PRINCIPAL ST. JOSEPH'S COLLEGE OF ENGG. & TECHNOLOGY, PALAI

Managed by the Catholic Diocese of Palai • Approved by AICTE • Affiliated to APJ Abdul Kalam Technological University, Kerala



Department of Electronics and Communication Engineering





cosystem

Mr. LIBIN TT GUEST SPEAKER

Scientist 'F' at CDAC

May 17th 2022

Venue: Mother Therese Seminar Hall - Mother Theresa Block

Time: 9:00 AM to 12:30 PM



(@ecsa.sjcet



Vega Processors And Ecosystem

DATE: 17 May 2022

VENUE: Mother Theresa Block Seminar Hall

RESOURCE PERSON: Mr. Libin T (CDAC)

COORDINATOR: Alen Tom Vincent

ELECTRONICS AND COMMUNICATION STUDENT'S ASSOCIATION.

Sayana Rasheed

Secretary of ECSA

SJCET Palai

16/05/2022

Dr V P Devasia

Principal

SJCET Palai

Subject: Permission letter to conduct a hands-on on the topic VEGA Processors and ecosystem.

Respected sir.

I am Sayana Rasheed, Secretary of ECSA Sjcet , palai. I am writing this letter on behalf of ECSA to seek permission to conduct a hands-on on 17/05/2022 on the topic VEGA Processors and ecosystem. I kindly request you to grant permission for this.

DETAILS ABOUT THE EVENT:

Topic: VEGA Processors and ecosystem

Syllabus: introduction about C-DAC, VEGA Series of processors, VEGA processor based socs,

ARIES development board, Ecosystem.

Resource person: Libin T.T., Joint director at C-DAC, Kottayam

Date: 17/05/2022

venue: Mother Therese seminars hall

Time: 9:00-12:30pm

yana Raheed

ours obediently

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3) Name of the Diogram: seminar (hords-on) - processor Date: 17/05/22 Time: 10.00 am to 1.00 pm

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Vega Processor and Ecosystem 17/05/2022

Feedback form

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This form was created inside of St. Joseph's College of Engineering and Technology, Palai.

Google Forms



HEAD OF THE DEPARTMENT
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGG.
ST. JOSEPH'S COLLEGE OF ENGG. & TECHNOLOGY
CHOONDACHERRY P. O., PALAI-686 579



国人の国ニス国の OF PARTICIPATION

We hereby present this certificate to

Aleena Anil

for your participation in the half day seminar - Vega Processors and Ecosystem conducted at St. Joseph's College, of Engineering and Technology, Palai on 17th May 2022.



Prof. Anto Manuel Faculty

> Dr. Arun P H.O.D.

in charge

Rinu Mathew President,



Jonartmont of Electronice and Communication Engineering