



ST. JOSEPH'S

COLLEGE OF ENGINEERING
AND TECHNOLOGY,
- PALAI -

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Cycle 1 - NAAC Accreditation 2023

Criterion - 1

Curricular Aspects

1.1 Curriculum planning and Implementation

1.1.1 The Institution ensures effective curriculum planning and delivery through a well-planned and documented process including Academic calendar and conduct of continuous internal Assessment

Submitted to:



National Assessment and Accreditation Council

Criterion - 1

Curricular Aspects

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Academic Calendar

SJCET strictly adheres to the Academic Calendar stipulated by the APJAKTU based on which an Institute Academic calendar is generated and activities in the college are scheduled accordingly. A sample of the Academic Calendar issued by the APJAKTU is shown herein.

		APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY										
		Academic Calendar - January 2023 to June 2023										
		MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8										
		Jan-23			Feb-23			Mar-23				
Days	Date	Description	Class	Days	Date	Description	Class	Days	Date	Description	Class	
Sun	1			Wed	1	Commencement of classes for MBA S4	9	Wed	1		29	
Mon	2	Mannam Jayanthi		Thu	2		10	Thu	2		30	
Tue	3			Fri	3	Course Selection and Mapping Begins for MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8	11	Fri	3		31	
Wed	4			Sat	4			Sat	4			
Thu	5			Sun	5			Sun	5			
Fri	6			Mon	6	Commencement of classes for B.Tech S4/S6, BHMCT S6/S4	12	Mon	6		32	
Sat	7			Tue	7		13	Tue	7		33	
Sun	8			Wed	8		14	Wed	8		34	
Mon	9			Thu	9		15	Thu	9	First Series test to be completed for INT MCA S4/S6, B.Arch S8 Project Evaluation-I MBA S4	35	
Tue	10			Fri	10	First CC Meeting for MBA S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6	16	Fri	10		36	
Wed	11			Sat	11			Sat	11			
Thu	12			Sun	12			Sun	12			
Fri	13			Mon	13		17	Mon	13		37	
Sat	14			Tue	14		18	Tue	14		38	
Sun	15			Wed	15	Course Selection and Mapping Ends for MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8	19	Wed	15		39	
Mon	16			Thu	16		20	Thu	16	Second CC meeting for MCA S4, INT MCA S4/S6	40	
Tue	17			Fri	17		21	Fri	17		41	
Wed	18			Sat	18	Shivaratri		Sat	18			
Thu	19	Commencement of classes for B.Des S8, MCA S4, INT MCA S4/S6	1	Sun	19			Sun	19			
Fri	20		2	Mon	20		22	Mon	20		42	
Sat	21			Tue	21		23	Tue	21		43	
Sun	22			Wed	22		24	Wed	22	First Series test to be completed for B.Arch S4/S6, MBA S4, B.Tech S4/S6/S8, BHMCT S6/S4	44	
Mon	23	Commencement of classes for B.Arch S8	3	Thu	23		25	Thu	23		45	
Tue	24		4	Fri	24		26	Fri	24		46	
Wed	25		5	Sat	25			Sat	25			
Thu	26	Republic Day		Sun	26			Sun	26			
Fri	27		6	Mon	27		27	Mon	27		47	
Sat	28			Tue	28	First Interim evaluation for MCA S4 Jury Evaluation-I for M.Plan S4, M.Arch S4	28	Tue	28		48	

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Sun	29		
Mon	30	Commencement of classes for B.Tech S8, B.Arch S4/S6, M.Arch S4, M.Plan S4	7
Tue	31		8

Wed	29	Second CC meeting for MBA S4, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6	49
Thu	30		50
Fri	31	Second Interim evaluation for MCA S4 Jury Evaluation -II for M.Plan S4, M.Arch S4	51

		APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY										
		Academic Calendar - January 2023 to June 2023										
		MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8										
Apr-23				May-23				Jun-23				
Days	Date	Description	Class	Days	Date	Description	Class	Days	Date	Description	Class	
Sat	1			Mon	1	May Day		Thu	1	Publish IA Marks for B.Tech S4/S6,BHMCT S4/S6	90	
Sun	2			Tue	2		68	Fri	2	Class Ends Publish Attendance for B.Tech S4/S6,BHMCT S4/S6	91	
Mon	3		52	Wed	3		69	Sat	3			
Tue	4		53	Thu	4	Second Series test to be completed for INT MCA S4/S6, B.Arch S8	70	Sun	4			
Wed	5		54	Fri	5		71	Mon	5	Commencement of End Semester Examination for B.Tech S8, B.Arch S4/S6, MBA S4	92	
Thu	6	Maundy Thursday		Sat	6			Tue	6		93	
Fri	7	Good Friday		Sun	7			Wed	7	External Jury Evaluation and Viva Voice for M.Arch S4, M.Plan S4	94	
Sat	8			Mon	8		72	Thu	8		95	
Sun	9	Easter		Tue	9		73	Fri	9		96	
Mon	10		55	Wed	10		74	Sat	10			
Tue	11		56	Thu	11	Publish IA Marks for INT MCA S4/S6	75	Sun	11			
Wed	12		57	Fri	12	Class Ends Publish Attendance for INT MCA S4/S6 Third CC meeting for MBA S4, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6	76	Mon	12	Commencement of End Semester Examination for B.Tech S4/S6,BHMCT S6/S4	97	
Thu	13		58	Sat	13			Tue	13		98	
Fri	14	Dr.B.R. Ambedkar Jayanthi		Sun	14			Wed	14		99	
Sat	15	Vishu		Mon	15	Third interim evaluation for MCA S4	77	Thu	15		100	
Sun	16			Tue	16		78	Fri	16		101	
Mon	17	Exam Registration begins for MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8	59	Wed	17	Publish IA Marks for MCA S4, B.Des S8,B.Arch S8	79	Sat	17			
Tue	18		60	Thu	18	Class Ends Publish Attendance for MCA S4, B.Des S8,B.Arch S8	80	Sun	18			
Wed	19		61	Fri	19	Second Series test to be completed for B.Tech S4/S6/S8, B.Arch S4/S6,MBA S4,BHMCT S6/S4 Project Evaluation-II MBA S4 B.Arch S8 Jury begins	81	Mon	19		102	
Thu	20		62	Sat	20			Tue	20		103	
Fri	21	Eid-Ul-Fitr		Sun	21			Wed	21		104	
Sat	22			Mon	22	Commencement of End Semester Examination for INT MCA S4/S6 External Project evaluation for B.Des S8 begins	82	Thu	22		105	
Sun	23			Tue	23		83	Fri	23		106	
Mon	24		63	Wed	24		84	Sat	24			
Tue	25		64	Thu	25	Publish IA Marks for B.Tech S8,B.Arch S4/S6,MBA S4	85	Sun	25			

Sample APJAKTU Academic Calendar Page 2



APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

Academic Calendar - January 2023 to June 2023

MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8

Even Semester(2022-2023)

Sl.No	Important Events	Important Dates
1	Commencement of classes for B.Des S8, MCA S4, INT MCA S4/S6	19-01-2023
2	Commencement of classes for B.Arch S8	23-01-2023
3	Commencement of classes for B.Tech S8, B.Arch S4/S6, M.Arch S4, M.Plan S4	30-01-2023
4	Commencement of classes for MBA S4	01-02-2023
5	Commencement of classes for B.Tech S4/S6, BHMCT S6/S4	06-02-2023
6	Course Selection and Mapping Begins for MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8	03-02-2023
7	Course Selection and Mapping Ends for MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8	15-02-2023
8	First CC Meeting for MBA S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6	10-02-2023
9	Second CC meeting for MCA S4, INT MCA S4/S6	16-03-2023
10	Second CC meeting for MBA S4, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6	29-03-2023
11	Third CC meeting for MCA S4, INT MCA S4/S6,	28-04-2023
12	Third CC meeting for MBA S4, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6	12-05-2023
13	First Series test to be completed for INT MCA S4/S6, B.Arch S8	09-03-2023
14	First Series test to be completed for B.Arch S4/S6, MBA S4, B.Tech S4/S6/S8, BHMCT S6/S4	22-03-2023
15	Exam Registration begins for MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8	17-04-2023
16	Exam Registration ends for MBA S4, M.Arch S4, M.Plan S4, MCA S4, INT MCA S4/S6, B.Arch S4/S6/S8, B.Tech S4/S6/S8, BHMCT S4/S6, B.Des S8	27-04-2023
17	Second Series test to be completed for INT MCA S4/S6, B.Arch S8	04-05-2023
18	Second Series test to be completed for B.Tech S4/S6/S8, B.Arch S4/S6, MBA S4, BHMCT S6/S4	19-05-2023
19	First interim evaluation for MCA S4	28-02-2023
20	Second interim evaluation for MCA S4	31-03-2023
21	Third interim evaluation for MCA S4	15-05-2023
22	Jury Evaluation-I for M.Plan S4, M.Arch S4	28-02-2023
23	Jury Evaluation -II for M.Plan S4, M.Arch S4	31-03-2023
24	Jury Evaluation -III for M.Plan S4, M.Arch S4	28-04-2023
25	Jury Evaluation -IV for M.Plan S4, M.Arch S4	26-05-2023
26	Project Evaluation-I MBA S4	09-03-2023
27	Project Evaluation-II MBA S4	19-05-2023
28	Publish IA Marks for INT MCA S4/S6	11-05-2023
29	Class Ends Publish Attendance for INT MCA S4/S6	12-05-2023
30	Publish IA Marks for MCA S4, B.Des S8, B.Arch S8	17-05-2023
31	Class Ends Publish Attendance for MCA S4, B.Des S8, B.Arch S8	18-05-2023
32	B.Arch S8 Jury	19-05-2023, 20-05-2023
33	Publish IA Marks for B.Tech S8, B.Arch S4/S6, MBA S4	25-05-2023

Subject Allotment

To start with academic activities in the beginning of every semester, the Head of the Department consolidates and circulates a list of courses to be handled by the concerned departments (including papers to be handled in other Departments). Based on the circulated list, individual faculty are asked to report courses of their choice to be handled during the forthcoming semester. Depending on various metrics like previous academic results, courses a faculty have attended in a particular domain, e.t.c, the HoD allots courses to individual faculties and informs them in advance to start with preparations and course planning. A sample of these activities are added herein.

St. Joseph's College of Engineering and TEchnology, Palai
Department of Electronics and Communication Engineering

List of subjects - Even semester 2022-23

Semester & Branch	Name of the subject	Subject Code
S2 EC	Basics of Electrical & Electronics Engineering - Part B	EST130
	Electronics Engineering Workshop - Part B	ESL130
	Professional Communication	HUN 102
	Programming in C	EST 102
S2 ECS	Basics of Electrical & Electronics Engineering - Part B	EST130
	Electronics Engineering Workshop - Part B	ESL130
S2 CS A&B	Basics of Electrical & Electronics Engineering - Part B	EST130
	Electronics Engineering Workshop - Part B	ESL130
S2 EE	Basics of Electrical & Electronics Engineering - Part B	EST130
	Electronics Engineering Workshop - Part B	ESL130
S4 EC (Including minor & honours)	Analog Circuits	ECT 202
	Signals and Systems	ECT 204
	Computer Architecture & Microcontrollers	ECT 206
	Design & Engineering	EST200
	Constitution of India	MCN202
	Nano Electronics (Honours)	ECT292
	Microcontroller (Minor) (VLSI & ES Basket)	ECT282
	Analog Circuits & Simulation Lab	ECL 202
	Microcontroller Lab	ECL204
	Placement	
S4-CS A&B	Digital Lab	CSL203
S6-ECE A&B	Electromagnetics	ECT302
	VLSI Circuit Design	ECT304
	Information Theory and Coding	ECT306
	PROGRAM ELECTIVE I # Power Electronics/Embedded Systems / Introduction to MEMS	ECT 322/ECT 342/ECT362
	Management for Engineers	HUT 310
	Comprehensive Course Work	ECT308

(1/2)

Sample page : consolidated list of Subjects in a semester Page 1

	Mechatronics Minor - Advanced Automation System	MRT 384
	(Minor VLSI & ES basket) - VLSI Circuits	ECT382
	(Honours) ELECTRONIC DESIGN AUTOMATION	ECT394
	Communication Lab	ECL332
	MINI PROJECT	ECL334
	Placement	
S8-ECE	Wireless Communication	ECT402
	Program Elective III # (Biomedical Engg/ Satellite Communication/ Secure Communication)	ECT 414/ ECT 424/ ECT 434/
	Program Elective IV # (Modern communication Systems/ RTOS/Analog CMOS Design)	ECT 416/ ECT 426/ ECT 466/ ECT 476
	Program Elective V# (Mechatronics/ Low power VLSI /Renewable Energy Systems)	ECT 418/ ECT 438/ECT 448/ ECT 458/ECT 468
	Comprehensive Viva Voce	ECT 404
	PROJECT PHASE II	ECD 416
	MINIPROJECT (S8 Honours)	ECD 496

PG Program (VLSI & ES)

MTech S2-VLSI	FOUNDATIONS OF DATA SCIENCE	222TEC100
	ANALOG VLSI DESIGN	222TEC004
	PROGRAM ELECTIVE 3* (Embedded System Design/SoC Design/Embedded networking)	22EEC 038 /22EEC043/22EEC035
	PROGRAM ELECTIVE 4* (VLSI SYSTEM TESTING/Low power VLSI)	22EEC041/ 22EEC040
	INDUSTRY/ INTERDISCIPLINARY ELECTIVE (MEMS AND Sensors/Automotive Electronics)	22EEC084/22EEC083
	Mini Project	222PEC100
	Design LaB II	222LEC003
MTech S4-VLSI	Project Phase-II	04EC7594

Maximum two subjects (awaiting final confirmation)

* Any one subject (awaiting final confirmation)


12.01.23
HoD ECE

Sample page : consolidated list of Subjects in a semester Page 2



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COLLEGE OF ENGINEERING
AND TECHNOLOGY,
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administered by Diocese of Palai | Approved by AICTE | Affiliated to KTU/MQ University | An ISO 9001:2015 certified college

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

File No:

Subject Preference Form

Name of the faculty
Designation
Employee Code

: SABARINATH.G
: ASST PROFESSOR
: 03ECE008

Theory Courses

Sl No	UG /PG	Semester	Program	Course code	Name of the Course	Reason for Preference
1	UG	S8	EC	ECT434	Secure Communication	
2	UG	S2	EC	EST130	BASICS OF ELECTRONICS	
3	UG	S2	ECS	EST130	BASICS OF ELECTRONICS	
4	UG	S6	EC	ECT302	ELECTROMAGNETICS	

Laboratory Courses

Sl No	UG /PG	Semester	Program	Course code	Name of the Course	Reason for Preference
1	UG	S2	EC	ESL130	ELECTRONIC WORKSHOP	
2						
3						
4						

Date: 16/01/2023

Signature of the faculty:

Sample Subject Preference Form



For Office Use Only

Theory Courses Allotted

Sl No	UG /PG	Semester	Program	Course code	Name of the Course	Remarks of HoD (If any)
1	UG	S6 EC043	B-tech	ECT 302	Electromagnetics	
2						
3						

Laboratory Courses Allotted

Sl No	UG /PG	Semester	Program	Course code	Name of the Course	Remarks of HoD (If any)
1	UG	S2	B-tech EC & ECS	ES1 130	Electronics Engineering Workshop	
2						
3						

Suggestions/ Comments by HoD (if any)


19.01.23

Signature of HoD

Sample subject allocation to individual faculty

DEPARTMENT OF E.E., SICT PALAI			
SUBJECT ALLOCATION FINAL - EVEN Semester 2023-24			
Sl.No.	NAME	THEORY COURSE	LAB COURSE
1	DR. MADHUKUMAR S (Vice Principal)		PROJECT PHASE-II - 54 MTECH- VLSI & ES MINI PROJECT 52 MTECH- VLSI & ES
2	Dr ARUN P Head ICT	EMBEDDED NETWORKING - 52 MTECH- VLSI & ES	PROJECT PHASE - II - 58 ECE PROJECT PHASE - II - MTECH 54 VLSI & ES
3	Mr.SABARINATH G	ELECTROMAGNETICS - 56 ECE A&B	ELECTRONICS ENGINEERING WORKSHOP- 52 ECE & EES
4	Ms.SUNTHA S PILLAI	COMPUTER ARCHITECTURE & MICROCONTROLLERS - 54 ECE BASICS OF ELECTRICAL & ELECTRONICS ENGINEERING - PART II - 52 ECE	BASIC ELECTRONICS ENGINEERING WORKSHOP - 52 ECE A
5	Dr.TOMSON DEVIS	MICROCELLS COMMUNICATION - 58 ECE COMPREHENSIVE VIVA VOCE - 58 ECE	DIGITAL LAB - 54 ECE BASIC ELECTRONICS ENGINEERING WORKSHOP - 52 ECE COMMUNICATION LAB - 56 ECE A & B
6	Mr.KRISHN P R	MEMS AND SENSORS - 52 M.ECE (INTERDISCIPLINARY) DESIGN AND ENGINEERING - 54 ECE & EES	MINI PROJECT - 52 ECE A MINI PROJECT - 52 MTECH- VLSI & ES
7	Ms.JUNEY M GEORGE	PROGRAMMING IN C - 52 ECE RTOC 58 ECE	C PROGRAMMING LAB - 52 ECE ELECTRONICS ENGINEERING WORKSHOP - 52 ECE B MICROCONTROLLER LAB - 54 ECE
8	Mr.HINCY MERRIN VARKKEY	VLSI CIRCUIT DESIGN - 56 ECE A & B COMPREHENSIVE COURSE WORK - 56 ECE A&B NANO ELECTRONICS (54 HONOURS)	
9	Ms NITHA M BDU	ANALOG VLSI DESIGN - 52 MTECH EMBEDDED SYSTEMS - 56 ELECTIVE COMPREHENSIVE COURSE WORK - 56 ECE A&B	MINI PROJECT - 56 ECE B
10	Ms.SUYA THESSA JINI	VLSI CIRCUITS - MINOR VLSI 56 ECE ANALOG CIRCUITS - 54 ECE	DIGITAL LAB - 54 ECE B ANALOG CIRCUITS AND SIMULATION LAB - 54 ECE
11	Ms.SANJU SEBASTIAN	INFORMATION THEORY AND CODING - 56 ECE A & B FOUNDATIONS OF DATA SCIENCE - 52 MTECH COMPREHENSIVE VIVA VOCE - 58 ECE	MINI PROJECT - 58 HONOURS
12	Mr.ANTO MANUEL	INTRODUCTION TO MEMS - 56 ECE VLSI SYSTEM DESIGN - 52 MTECH- VLSI	PROJECT PHASE - II - 58 ECE DESIGN LAB - 52 MTECH
13	Ms ANU JYOTHI	BASICS OF ELECTRICAL & ELECTRONICS ENGINEERING - PART B - 52 ECE & EES MICROCONTROLLER - MINOR - VLSI - 54 B-TECH	ANALOG CIRCUITS AND SIMULATION LAB - 54 ECE COMMUNICATION LAB - 56 ECE B
14	Ms VIJAYALESHMI G	SIGNALS AND SYSTEMS - 54 ECE BASICS OF ELECTRICAL & ELECTRONICS ENGINEERING - PART B 52 ECE A & B	COMMUNICATION LAB - 56 ECE A DIGITAL LAB - 54 ECE B
15	Ms.SUMITHA JOSEPH	BIOMEDICAL ENGINEERING - PROGRAM ELECTIVE - III 58 ECE ELECTRONIC DESIGN AND AUTOMATION - 56 HONOURS PLACEMENT - 54 ECE	MINI PROJECT - 56 ECE A MICRO CONTROLLER LAB - 54 ECE
16	Ms.AMALA MATHEW	RENEWABLE ENERGY SYSTEMS - PROGRAM ELECTIVE - V 58 ECE CONSTITUTION OF INDIA - 54 ECE PLACEMENT - 56 ECE A & B	MINI PROJECT - 56 ECE B DIGITAL LAB - 54 ECE A C PROGRAMMING LAB - 52 ECE

19/01/2023



Ho/DECE

Sample of consolidation of subject allocation


Time Table

After subjects are allocated, the Department timetable in charge, in close association with the Institute Time Table Committee, prepares and publishes the Semester Time Table in adherence to the Institute and University Academic Calendars.

A sample timetable is shown herein.

STUDENTS TIME TABLE 58 B.TECH. (2019 BATCH)										
(WITH EFFECT FROM 30/1/2023)										
	TIME	A (ME-A)	B (ME-B)	C (CE)	D (CS-A)	E (CS-B)	F (EE)	G (EC-A)	H (EE)	
MONDAY	09.00-09.55	EL-4-CM(AR),TM(TZ),CE(SKV)		AQM(MRC)	ELECTIVE1(ASD,SMU)		ESDE(PVV)	RTOS(JMG)	CI(RMJ)	
	09.55-10.50			SC(TLB)	CCV(BT)	DC(JBP)	ESDE(PVV)	PPR(AP,ATM)	CCV(MS)	
	11.00-11.55	EL-3-QM(CPE),MNM(PPV)		QSV(BK)	ELECTIVE3(ASD,SMU)		HA(LMR)	RTOS(JMG)	VLS(GTV)	
	11.55-12.45	MTS(AR)	MTS(DNS)	QSV(BK)	ELECTIVE5(AG,BT)		HA(LMR)	RES(AMM)	ANN(RAG)	
	NOON INTERVAL									
TUESDAY	01.35-02.30	EL-5-AEE(ALB),PPE(ALX)		PROJ (MAM)	PROJ (BT,JKK)	PROJ (PVM,MY)	PROJ (LMR*,GAG)	PROJ (AP,ATM)	PROJ (ALS,RAG)	
	02.30-03.25	EL-3-QM(CPE),MNM(PPV)								
	03.35-04.30	MTS(AR)	CCV(PPV)	HONOURS MINI PROJECT						
	04.30-05.30	HONOURS MINI PROJECT								
	09.00-09.55	EL-4-CM(AR),TM(TZ),CE(SKV)		SC(TLB)	ELECTIVE3(ASD,SMU)		ESDE(PVV)	RTOS(JMG)	VLS(GTV)	
09.55-10.50	EL-3-QM(CPE),MNM(PPV)		RRB(TKM)	DC(KS)	CCV(JBP)	ESDE(PVV)	WC(TDM)	ANN(RAG)		
11.00-11.55	MTS(AR)	MTS(DNS)	RRB(TKM)	ELECTIVE4(DS,DMC)		EM(TT)	IME(SMU)	PPJ(JMK)		
11.55-12.45	CCV(BS)	CCV(PPV)	CCV(AAK)	DC(KS)	PPR(PVM)	EM(TT)	WC(TDM)	CI(RMJ)		
NOON INTERVAL										
WEDNESDAY	01.35-02.30	PROJ (JS,TZ)	PROJ (LP,RB)	PROJ (DDR)	PROJ (BT,JKK)	PROJ (PVM,MY)	PROJ (LMR*,GAG)	PROJ (AP,ATM)	PROJ (HMI,JMK)	
	02.30-03.25	PPR(JS,TZ)	PPR(LP,RB)							
	03.35-04.30	HONOURS MINI PROJECT								
	04.30-05.30	HONOURS MINI PROJECT								
	09.00-09.55	PROJ (JS,TZ)	PROJ (LP,RB)	AQM(MRC)	ELECTIVE4(DS,DMC)		ESD(PVV)	IME(SMU)	PPJ(JMK)	
09.55-10.50	AQM(MRC)			ELECTIVE5(AG,BT)		SEM(DCT)	WC(TDM)	ANN(RAG)		
11.00-11.55	RRB(TKM)			CCV(SMU)	CCV(BT)	EM(TT)	RES(AMM)	CI(RMJ)		
11.55-12.45	RRB(TKM)			ELECTIVE4(DS,DMC)		SEM(DCT)	RES(AMM)	VLS(GTV)		
NOON INTERVAL										
THURSDAY	01.35-02.30	PROJ (JS,TZ)	PROJ (LP,RB)	QSV(BK)	PPR(BT)	DC(JBP)	HA(LMR)	RTOS(JMG)	CI(RMJ)	
	02.30-03.25			QSV(BK)	ELECTIVE3(ASD,SMU)		HA(LMR)	RES(AMM)	VLS(GTV)	
	03.35-04.30	HONOURS MINI PROJECT								
	04.30-05.30	HONOURS MINI PROJECT								
	09.00-09.55	MTS(AR)	MTS(DNS)	AQM(MRC)	ELECTIVE5(AG,BT)		EM(TT)	CCV (SAS,TDM)	ANN(RAG)	
09.55-10.50			PPR(MAM)	DC(KS)	DC(JBP)	EM(TT)	BME(SMU)	CI(RMJ)		
11.00-11.55	EL-5-AEE(ALB),PPE(ALX)		RRB(TKM)	DC(KS)	DC(JBP)	SEM(DCT)	BME(SMU)	PPJ(JMK)		
11.55-12.45			QSV(BK)	ELECTIVE3(ASD,SMU)		SEM(DCT)	RTOS(JMG)	VLS(GTV)		
NOON INTERVAL										
FRIDAY	01.35-02.30	PROJ (JS,TZ)	PROJ (LP,RB)	PROJ (JS)	PROJ (BT,JKK)	PROJ (PVM,MY)	PROJ (LMR*,GAG)	PROJ (AP,ATM)	PROJ (ALS,RAG)	
	02.30-03.25			HONOURS MINI PROJECT						
	03.35-04.30	HONOURS MINI PROJECT								
	04.30-05.30	HONOURS MINI PROJECT								
	09.00-09.55	EL-5-AEE(ALB),PPE(ALX)		SC(TLB)	DC(KS)	DC(JBP)	CCV(TT)	RES(AMM)	PPJ(JMK)	
09.55-10.50	CCV(BS)	MTS(DNS)	SC(TLB)	ELECTIVE4(DS,DMC)		CCV(TT)	WC(TDM)	CCV(RMJ)		
11.00-11.55	EL-4-CM(AR),TM(TZ),CE(SKV)		CCV(AAK)	ELECTIVE5(AG,BT)		PPR(TT)	BME(SMU)	ANN(RAG)		
11.55-12.45			AQM(MRC)	ELECTIVE3(ASD,SMU)		SEM(DCT)	WC(TDM)	PPJ(JMK)		
NOON INTERVAL										
FRIDAY	01.35-02.30	EL-3-QM(CPE),MNM(PPV)		PROJ (MAM)	PROJ (BT,JKK)	PROJ (PVM,MY)	PROJ (LMR*,GAG)	PROJ (AP,ATM)	PROJ (HMI,JMK)	
	02.30-03.25	EL-5-AEE(ALB),PPE(ALX)								
	03.35-04.30	HONOURS MINI PROJECT								
	04.30-05.00	HONOURS MINI PROJECT								


 DEAN(UG)
 Dr. George Tom Varghese


 PRINCIPAL
 Dr. V.P. Devassia

Sample Department Wise Time Table

Course Diary

Every faculty maintains an academic course diary, which contains all information pertaining to the course planning, delivery, implementation, assessments and impacts (course objective attainments and contribution to program objective attainments). A sample of selected pages of the course diary is attached here with.

ECE 2020-2024 A (S5)

ECT301 - LINEAR INTEGRATED CIRCUITS

ST. JOSEPH'S COLLEGE OF ENGINEERING AND TECHNOLOGY, PALAI

CHOONDACHERRY P.O, PALAI, KOTTAYAM 686 579, KERALA,INDIA.

PH: 04822239700



COURSE DIARY

Subject	ECT301 - LINEAR INTEGRATED CIRCUITS
Batch	ECE 2020-2024 A (S5)
Academic Year	2022-23
Total hours taken	62
Name of Teacher	NITHA M BIJU
Designation	Assistant Professor
Department	Electronics and Communication Engineering

Front page sample

SLNo.	Content
1	GENERAL INSTRUCTIONS
2	VISION & MISION OF COLLEGE
3	PROGRAM OUTCOME
4	VISION & MISSION OF DEPARTMENT
5	PROGRAM EDUCATIONAL OBJECTIVES
6	PROGRAM SPECIFIC OUTCOMES
7	COURSE OUTCOMES
8	ACADEMIC CALENDAR
9	TIMETABLE
10	SYLLABUS
11	SUBJECT PLAN
12	SUBJECT COVERAGE
13	ASSIGNMENTS
14	ASSIGNMENT MARKS
15	ASSIGNMENT CO SPLIT-UP MARKS
16	SERIES EXAMS
17	SERIES EXAM MARKS
18	SERIES EXAM CO SPLIT-UP MARKS
19	INTERNALMARK
20	QUIZZES
21	OUTCOME BASED EXAMS
22	CO PO ATTAINMENT
23	INDIVIDUAL ATTAINMENTS
24	OVERALL INDIVIDUAL ATTAINMENT
25	QUESTION PAPER
26	QUESTION PAPER WITH ANSWER SCHEME
27	COURSE EXIT SURVEY
28	ATTENDANCE SUMMARY
29	SPECIAL CLASS
30	STUDENT LIST

Contents page of Course diary

Sep 2022				Oct 2022			
Days	Date	Description	Class	Days	Date	Description	Class
				Sat	01		
				Sun	02		
				Mon	03	Declared Holiday (Pooja)- Government	
				Tue	04	Mahanavami	
				Wed	05	Vijayadhashami	
				Thu	06		7
				Fri	07		
				Sat	08		
				Sun	09		
				Mon	10		
				Tue	11		
				Wed	12		
				Thu	13		
				Fri	14		9
				Sat	15		
				Sun	16		
				Mon	17		10
				Tue	18		11
Mon	19		1	Wed	19		
Tue	20		2	Thu	20		12
Wed	21	Sree Narayana guru samadhi		Fri	21		14
Thu	22			Sat	22		
Fri	23		3	Sun	23		
Sat	24			Mon	24	Deepavali	
Sun	25			Tue	25		
Mon	26			Wed	26		
Tue	27			Thu	27		
Wed	28			Fri	28		17
Thu	29		5	Sat	29		
Fri	30		6	Sun	30		
				Mon	31		

Sample page of institute academic calendar

ECE 2020-2024 A (S5)
TIMETABLE

ECT301 - LINEAR INTEGRATED CIRCUITS

Day	Period 1	Period 2	Period 3	Period 4	Period 5	Period 6	Period 7	Period 8
Monday	--	--	--	--	--	--	ECT301 - LINEAR INTEGRATED CIRCUITS	--
Tuesday	ECT301 - LINEAR INTEGRATED CIRCUITS	--	--	--	--	--	--	--
Wednesday	--	--	--	--	--	--	--	--
Thursday	--	--	--	ECT301 - LINEAR INTEGRATED CIRCUITS	--	--	--	--
Friday	--	--	--	--	--	ECT301 - LINEAR INTEGRATED CIRCUITS	--	--
Saturday	--	--	--	--	--	--	--	--

Subject-wise timetable for faculty

Module	Topic
1	<p>1.1. Operational amplifiers</p> <p>1.1.1. The 741 Op Amp, Block diagram, Ideal op-amp parameters, typical parameter values for 741</p> <p>1.1.2. Equivalent circuit, Open loop configurations, Voltage transfer curve, Frequency response curve.</p> <p>1.1.3. Differential amplifier configurations using BJT, DC Analysis- transfer characteristics</p> <p>1.1.4. AC analysis- differential and common mode gains, CMRR, input and output resistance, Voltage gain</p> <p>1.1.5. Constant current bias and constant current source</p> <p>1.1.6. Concept of current mirror, the two transistor current mirror Wilson and Widlar current mirrors.</p> <p>1.1.7. Tutorial on Differential Amplifiers</p> <p>1.1.8. Tutorial on Operational Amplifiers</p>
2	<p>2.1. Op-amp with negative feedback and Op-amp applications</p> <p>2.1.1. General concept of Voltage Series, Voltage Shunt, current series and current shunt negative feedback</p> <p>2.1.2. Op Amp circuits with voltage series and voltage shunt feedback, Virtual ground Concept</p> <p>2.1.3. Analysis of practical inverting and non-inverting amplifier</p> <p>2.1.4. Summer, Voltage Follower-loading effect</p> <p>2.1.5. Differential and Instrumentation Amplifiers</p> <p>2.1.6. Voltage to current and Current to voltage converters</p> <p>2.1.7. Integrator, Differentiator</p> <p>2.1.8. Precision rectifiers-half wave and full wave</p> <p>2.1.9. Comparators, Schmitt Triggers</p> <p>2.1.10. Log and antilog amplifier</p> <p>2.1.11. Problems based on op-amp circuits</p>
3	<p>3.1. Op-amp Oscillators and Multivibrators</p> <p>3.1.1. Phase Shift and Wien-bridge Oscillators,</p> <p>3.1.2. Triangular and Sawtooth waveform generators, Astable and monostable multivibrators</p> <p>3.1.3. Comparison, design of First and second order low pass and High pass active filters</p> <p>3.1.4. Design of Second Order Band pass and band reject filters</p> <p>3.1.5. State variable filters</p> <p>3.1.6. Tutorial on Op-amp Oscillators and Multivibrators</p> <p>3.1.7. Tutorial on filters</p>
4	<p>4.1. Timer, VCO and PLL</p> <p>4.1.1. Timer IC 555- Functional diagram, Astable and monostable operations.</p> <p>4.1.2. Basic concepts of Voltage Controlled Oscillator</p> <p>4.1.3. Application of VCO IC LM566</p> <p>4.1.4. PLL Operation, Closed loop analysis Lock and capture range</p> <p>4.1.5. Basic building blocks, PLL IC 565, Applications of PLL</p> <p>4.1.6. Tutorial on 555 timer, VCO & PLL</p>
5	<p>5.1. Voltage regulators and Data converters</p> <p>5.1.1. Fixed and Adjustable voltage regulators</p> <p>5.1.2. IC 723 – Low voltage and high voltage configurations,</p> <p>5.1.3. Current boosting, Current limiting, Short circuit and Fold-back protection.</p> <p>5.1.4. Digital to Analog converters, Specifications, Weighted resistor type and R-2R Ladder type.</p> <p>5.1.5. Analog to Digital Converters: Specifications, Flash type and Successive approximation type.</p> <p>5.1.6. Tutorial on ADC & DAC</p>

Sample page of course syllabus in course diary

Sl No	Date	Hour	Topic	Module	Method Of Delivery
1	2022-09-19	Hour 7	The 741 Op Amp, Block diagram, Ideal op-amp parameters, typical parameter values for 741	1	Lecture
2	2022-09-20	Hour 1	The 741 Op Amp, Block diagram, Ideal op-amp parameters, typical parameter values for 741	1	Lecture
3	2022-09-23	Hour 6	Equivalent circuit, Open loop configurations, Voltage transfer curve, Frequency response curve.	1	Lecture
4	2022-09-24	Hour 1	Differential amplifier configurations using BJT, DC Analysis- transfer characteristics	1	Lecture
5	2022-09-29	Hour 4	Differential amplifier configurations using BJT, DC Analysis- transfer characteristics	1	Lecture
		Hour 7	Tutorial on Operational Amplifiers	1	Tutorial
6	2022-09-30	Hour 6	AC analysis- differential and common mode gains, CMRR, input and output resistance, Voltage gain	1	Lecture
7	2022-10-06	Hour 4	Constant current bias and constant current source	1	Lecture
8	2022-10-07	Hour 6	Concept of current mirror, the two transistor current mirror Wilson and Widlar current mirrors.	1	Lecture
9	2022-10-10	Hour 7	Concept of current mirror, the two transistor current mirror Wilson and Widlar current mirrors.	1	Lecture
10	2022-10-11	Hour 1	Tutorial on Differential Amplifiers	1	Tutorial
11	2022-10-13	Hour 4	Tutorial on Operational Amplifiers	1	Tutorial
12	2022-10-14	Hour 3	General concept of Voltage Series, Voltage Shunt, current series and current shunt negative feedback	2	Lecture
		Hour 6	Op Amp circuits with voltage series and voltage shunt feedback, Virtual ground Concept	2	Lecture
13	2022-10-17	Hour 7	Analysis of practical inverting and non-inverting amplifier	2	Lecture
14	2022-10-18	Hour 1	Analysis of practical inverting and non-inverting amplifier	2	Lecture
15	2022-10-20	Hour 4	Analysis of practical inverting and non-inverting amplifier	2	Tutorial
16	2022-10-21	Hour 1	Summer, Voltage Follower-loading effect	2	Lecture
		Hour 6	Differential and Instrumentation Amplifiers	2	Lecture
17	2022-10-22	Hour 6	Voltage to current and Current to voltage converters	2	Lecture
18	2022-10-25	Hour 1	Integrator, Differentiator	2	Lecture
19	2022-10-28	Hour 2	Precision rectifiers-half wave and full wave	2	Lecture
		Hour 4	Comparators, Schmitt Triggers	2	Lecture
		Hour 6	Log and antilog amplifier	2	Lecture
20	2022-10-29	Hour 7	Problems based on op-amp circuits	2	Tutorial
21	2022-11-03	Hour 4	Problems based on op-amp circuits	2	Tutorial
22	2022-11-04	Hour 1	Phase Shift and Wien-bridge Oscillators,	3	Lecture
		Hour 6	Phase Shift and Wien-bridge Oscillators,	3	Lecture
23	2022-11-05	Hour 1	Triangular and Sawtooth waveform generators, Astable and monostable multivibrators	3	Lecture
24	2022-11-07	Hour 7	Triangular and Sawtooth waveform generators, Astable and monostable multivibrators	3	Lecture
25	2022-11-08	Hour 1	Tutorial on Op-amp Oscillators and Multivibrators	3	Tutorial
26	2022-11-10	Hour 4	Tutorial on Op-amp Oscillators and Multivibrators	3	Tutorial
27	2022-11-11	Hour 6	Comparison, design of First and second order low pass and High pass active filters	3	Lecture
28	2022-11-14	Hour 7	Comparison, design of First and second order low pass and High pass active filters	3	Lecture
29	2022-11-15	Hour 1	Design of Second Order Band pass and band reject filters	3	Lecture
		Hour 7	Design of Second Order Band pass and band reject filters	3	Lecture
30	2022-11-19	Hour 1	State variable filters	3	Lecture
		Hour 4	Tutorial on filters	3	Tutorial
31	2022-11-21	Hour 7	Timer IC 555- Functional diagram, Astable and monostable operations.	4	Lecture
32	2022-11-22	Hour 1	Timer IC 555- Functional diagram, Astable and monostable operations.	4	Lecture
33	2022-11-24	Hour 4	Basic concepts of Voltage Controlled Oscillator	4	Lecture
34	2022-11-25	Hour 4	Application of VCO IC LM566	4	Lecture
		Hour 6	PLL Operation, Closed loop analysis Lock and capture range	4	Lecture
35	2022-11-26	Hour 3	Basic building blocks, PLL IC 565, Applications of PLL.	4	Lecture
		Hour 4	Basic building blocks, PLL IC 565, Applications of PLL.	4	Lecture
36	2022-11-28	Hour 7	Tutorial on 555 timer,VCO & PLL.	4	Tutorial

Sl No	Date	Hour	Topic	Module	Method Of Delivery
37	2022-11-29	Hour 1	Tutorial on 555 timer,VCO & PLL.	4	Tutorial
38	2022-12-01	Hour 4	Fixed and Adjustable voltage regulators	5	Lecture
39	2022-12-02	Hour 6	IC 723 – Low voltage and high voltage configurations,	5	Lecture
40	2022-12-03	Hour 6	IC 723 – Low voltage and high voltage configurations,	5	Lecture
41	2022-12-05	Hour 7	Current boosting, Current limiting, Short circuit and Fold-back protection.	5	Lecture
42	2022-12-06	Hour 1	Current boosting, Current limiting, Short circuit and Fold-back protection.	5	Lecture
		Hour 6	Digital to Analog converters, Specifications, Weighted resistor type and R-2R Ladder type.	5	Lecture
43	2022-12-08	Hour 4	Digital to Analog converters, Specifications, Weighted resistor type and R-2R Ladder type.	5	Lecture
44	2022-12-09	Hour 2	Analog to Digital Converters: Specifications, Flash type and Successive approximation type.	5	Lecture
		Hour 3	Analog to Digital Converters: Specifications, Flash type and Successive approximation type.	5	Lecture
45	2022-12-12	Hour 7	Tutorial on ADC & DAC	5	Tutorial

Sl No	Date	Hour	Topic	Module	Remarks
1	2022-09-19	Hour 7	The 741 Op Amp, Block diagram, Ideal op-amp parameters, typical parameter values for 741	1	
2	2022-09-20	Hour 1	The 741 Op Amp, Block diagram, Ideal op-amp parameters, typical parameter values for 741	1	
3	2022-09-23	Hour 6	Equivalent circuit, Open loop configurations, Voltage transfer curve, Frequency response curve.	1	
4	2022-09-24	Hour 1	Differential amplifier configurations using BJT, DC Analysis- transfer characteristics	1	
5	2022-09-29	Hour 4	Differential amplifier configurations using BJT, DC Analysis- transfer characteristics	1	
		Hour 7	Tutorial on Differential Amplifiers	1	
6	2022-09-30	Hour 6	AC analysis- differential and common mode gains, CMRR, input and output resistance, Voltage gain	1	
7	2022-10-06	Hour 4	Constant current bias and constant current source	1	
8	2022-10-14	Hour 3	Concept of current mirror, the two transistor current mirror Wilson and Widlar current mirrors.	1	
		Hour 6	Concept of current mirror, the two transistor current mirror Wilson and Widlar current mirrors.	1	
9	2022-10-15	Hour 4	Tutorial on Operational Amplifiers	1	
10	2022-10-17	Hour 7	Tutorial on Operational Amplifiers	1	
11	2022-10-18	Hour 1	General concept of Voltage Series, Voltage Shunt, current series and current shunt negative feedback	2	
12	2022-10-20	Hour 4	Op Amp circuits with voltage series and voltage shunt feedback, Virtual ground Concept	2	
13	2022-10-21	Hour 1	Analysis of practical inverting and non-inverting amplifier	2	
			Problems based on op-amp circuits	2	
		Hour 6	Analysis of practical inverting and non-inverting amplifier	2	
			Problems based on op-amp circuits	2	
14	2022-10-22	Hour 6	Analysis of practical inverting and non-inverting amplifier	2	
15	2022-10-28	Hour 2	Summer, Voltage Follower-loading effect	2	
		Hour 4	Problems based on op-amp circuits	2	
		Hour 6	Differential and Instrumentation Amplifiers	2	
16	2022-11-03	Hour 4	Voltage to current and Current to voltage converters	2	
17	2022-11-04	Hour 1	Integrator, Differentiator	2	
		Hour 6	Problems based on op-amp circuits	2	
18	2022-11-05	Hour 1	Precision rectifiers-half wave and full wave	2	
19	2022-11-07	Hour 7	Comparators, Schmitt Triggers	2	
20	2022-11-08	Hour 1	Log and antilog amplifier	2	
21	2022-11-14	Hour 1	Problems based on op-amp circuits	2	
		Hour 2	Problems based on op-amp circuits	2	
		Hour 3	Problems based on op-amp circuits	2	
22	2022-11-15	Hour 7	Phase Shift and Wien-bridge Oscillators,	3	
23	2022-11-19	Hour 1	Phase Shift and Wien-bridge Oscillators,	3	
		Hour 4	Triangular and Sawtooth waveform generators, Astable and monostable multivibrators	3	
24	2022-11-21	Hour 7	Triangular and Sawtooth waveform generators, Astable and monostable multivibrators	3	
25	2022-11-24	Hour 4	Triangular and Sawtooth waveform generators, Astable and monostable multivibrators	3	
26	2022-11-25	Hour 4	Comparison, design of First and second order low pass and High pass active filters	3	
		Hour 6	Design of Second Order Band pass and band reject filters	3	
		Hour 3	Design of Second Order Band pass and band reject filters	3	

Subject coverage – page 1

ECE 2020-2024 A (S5)

ECT301 - LINEAR INTEGRATED CIRCUITS

S.No	Date	Hour	Topic	Module	Remarks
		Hour 4	State variable filters	3	
28	2022-11-28	Hour 7	Tutorial on Op-amp Oscillators and Multivibrators	3	
29	2022-11-29	Hour 1	Tutorial on Op-amp Oscillators and Multivibrators	3	
30	2022-12-05	Hour 7	Tutorial on filters	3	
31	2022-12-06	Hour 1	Timer IC 555- Functional diagram, Astable and monostable operations.	4	
		Hour 6	Timer IC 555- Functional diagram, Astable and monostable operations.	4	
32	2022-12-08	Hour 4	Timer IC 555- Functional diagram, Astable and monostable operations.	4	
33	2022-12-09	Hour 2	Basic concepts of Voltage Controlled Oscillator	4	
		Hour 3	Application of VCO IC LM566	4	
34	2022-12-12	Hour 7	PLL Operation, Closed loop analysis Lock and capture range	4	
35	2022-12-14	Hour 5	Basic building blocks, PLL IC 565, Applications of PLL	4	
		Hour 6	Basic building blocks, PLL IC 565, Applications of PLL	4	
		Hour 7	Tutorial on 555 timer,VCO & PLL	4	
36	2022-12-22	Hour 3	Tutorial on 555 timer,VCO & PLL	4	
		Hour 4	Fixed and Adjustable voltage regulators	5	
37	2023-01-04	Hour 3	IC 723 – Low voltage and high voltage configurations,	5	
		Hour 4	Current boosting, Current limiting, Short circuit and Fold-back protection.	5	
		Hour 7	Digital to Analog converters, Specifications, Weighted resistor type and R-2R Ladder type.	5	
38	2023-01-05	Hour 1	Digital to Analog converters, Specifications, Weighted resistor type and R-2R Ladder type.	5	
		Hour 2	Digital to Analog converters, Specifications, Weighted resistor type and R-2R Ladder type.	5	
39	2023-01-08	Hour 3	Analog to Digital Converters: Specifications, Flash type and Successive approximation type.	5	
		Hour 4	Analog to Digital Converters: Specifications, Flash type and Successive approximation type.	5	
40	2023-01-10	Hour 5	Analog to Digital Converters: Specifications, Flash type and Successive approximation type.	5	
		Hour 6	Tutorial on ADC & DAC	5	
		Hour 7	Tutorial on ADC & DAC	5	

Subject coverage – page 2

ECE 2020-2024 A (S5)
ASSIGNMENTS

ECT301 - LINEAR INTEGRATED CIRCUITS

S.No	Title	Details	Issued On	Last Date	Max Mark	Question
1	Assignment 1	Assignment 1	26/09/2022	05/10/2022	15	
2	Assignment 2	Assignment 2	22/12/2022	03/01/2023	15	

Sample Assignments

ECE 2020-2024 A (S5)
ASSIGNMENT MARKS

ECT301 - LINEAR INTEGRATED CIRCUITS

Roll No	Name	Assignment 1 (15)	Assignment 2 (15)
1	AARYA SARA ALEX	15	12
2	ABHISREE B	15	14
3	ADITHYAN S PHILIP	15	15
4	AISWARYA JOSE	15	14.5
5	AJIL PAVITHRAN	15	15
6	AKSHAI P S	15	15
7	ALAN GEORGE	15	14.5
8	ALAN THOMAS	15	15
9	ALBIN JOSE	15	15
10	ALEENA MARGARET	15	15
11	ALEX BABU JOSEPH	15	15
12	ANET SUNNY	15	15
13	ANISIA S PULICKAN	15	14.5
14	ANJALI RACHEL ANISH	15	14.5
15	ANN MARIA JOHNEY	15	15
16	ANN MARIYA SHAJI	15	15
17	ANU SOMARAJAN K	15	15
18	ARJUN KRISHNA C V	15	14.5
19	ARJUN T S	15	14.5
20	ASHLEY ELEZABETH	15	15
21	ATHUL KRISHNA SURESH	15	14.5
22	DIYA SEBASTIAN	15	15
23	ELVIN ALOSIOUS	15	14.5
24	GLADWIN BIJU	15	14.5
25	ISAAC GEORGE	15	15
25	GOKUL V	15	12
26	JACOB MONCY	15	15
27	JEEVANJOSEPH P B	15	15
28	JISS JOSEPH	15	14.5
29	JOEL MATHEW	15	15
30	JOHNS PATTARA	15	15
31	JOSE ABRAHAM	15	14.5
32	JOSIL N D	15	15
33	JOYAL SIBY	15	15
34	JULIT SHAJI	15	15
35	KARTHIKA N	15	15
36	MELWIN SHAJU	15	15
37	NAYANA MANOJ	15	15
38	NAYANA MOL JOSEPH	15	15
39	PRITHVIN A NAIR	15	10
40	SACHIN JOSHY	15	13
41	SARATH P V	15	15
42	SHARON JUDY	15	15
43	SHARON MERIN SABU	15	15
44	SONU JACOB	15	15
45	THEJAS KRISHNA.J	15	14.5

Sample page assignment Mark entry

ASSIGNMENT CO SPLIT-UP MARKS

Name of evaluation: Assignment Assignment 1		Total Mark: 15		Assignment name: Assignment 1
Roll No	Name	1 (7.50) CO1	2 (7.50) CO2	Total
1	AARYA SARA ALEX	7.5	7.5	15
2	ABHISREE B	7.5	7.5	15
3	ADITHYAN S PHILIP	7.5	7.5	15
4	AISWARYA JOSE	7.5	7.5	15
5	AJIL PAVITHRAN	7.5	7.5	15
6	AKSHAI P S	7.5	7.5	15
7	ALAN GEORGE	7.5	7.5	15
8	ALAN THOMAS	7.5	7.5	15
9	ALBIN JOSE	7.5	7.5	15
10	ALEENA MARGARET	7.5	7.5	15
11	ALEX BABU JOSEPH	7.5	7.5	15
12	ANET SUNNY	7.5	7.5	15
13	ANISIA S PULICKAN	7.5	7.5	15
14	ANJALI RACHEL ANISH	7.5	7.5	15
15	ANN MARIA JOHNEY	7.5	7.5	15
16	ANN MARIYA SHAJI	7.5	7.5	15
17	ANU SOMARAJAN K	7.5	7.5	15
18	ARJUN KRISHNA C V	7.5	7.5	15
19	ARJUN T S	7.5	7.5	15
20	ASHLEY ELIZABETH	7.5	7.5	15
21	ATHUL KRISHNA SURESH	7.5	7.5	15
22	DIYA SEBASTIAN	7.5	7.5	15
23	ELVIN ALOSIOUS	7.5	7.5	15
24	GLADWIN BIJU	7.5	7.5	15
25	ISAAC GEORGE	7.5	7.5	15
25	GOKUL V	7.5	7.5	15
26	JACOB MONCY	7.5	7.5	15
27	JEEVANJOSEPH P B	7.5	7.5	15
28	JESS JOSEPH	7.5	7.5	15
29	JOEL MATHEW	7.5	7.5	15
30	JOHNS PATTARA	7.5	7.5	15
31	JOSE ABRAHAM	7.5	7.5	15
32	JOSIL N D	7.5	7.5	15
33	JOYAL SIBY	7.5	7.5	15
34	JULIT SHAJI	7.5	7.5	15
35	KARTHIKA N	7.5	7.5	15
36	MELWIN SHAJU	7.5	7.5	15
37	NAYANA MANOJ	7.5	7.5	15
38	NAYANA MOL JOSEPH	7.5	7.5	15
39	PRITHVIN A NAIR	7.5	7.5	15
40	SACHIN JOSHY	7.5	7.5	15
41	SARATH P V	7.5	7.5	15
42	SHARON JUDY	7.5	7.5	15
43	SHARON MERIN SABU	7.5	7.5	15
44	SONU JACOB	7.5	7.5	15
45	THEJAS KRISHNA J	7.5	7.5	15

Sample page Assignment CO wise split-up

Roll No	Name	Series Exam 1 (50)
1	AARYA SARA ALEX	49
2	ABHISREE B	28
3	ADITHYAN S PHILIP	19
4	AISWARYA JOSE	22
5	AJIL PAVITHRAN	37
6	AKSHAI P S	29
7	ALAN GEORGE	38
8	ALAN THOMAS	44
9	ALBIN JOSE	8.5
10	ALEENA MARGARET	46
11	ALEX BABU JOSEPH	32
12	ANET SUNNY	19
13	ANISIA S PULICKAN	47
14	ANJALI RACHEL ANISH	32
15	ANN MARIA JOHNEY	24
16	ANN MARIYA SHAJI	41
17	ANU SOMARAJAN K	37
18	ARJUN KRISHNA C V	15
19	ARJUN T S	18
20	ASHLEY ELEZABETH	43
21	ATHUL KRISHNA SURESH	34
22	DIYA SEBASTIAN	45
23	ELVIN ALOSIOUS	29
24	GLADWIN BIJU	37
25	ISAAC GEORGE	36
25	GOKUL V	20
26	JACOB MONCY	36
27	JEEVANJOSEPH P B	44
28	JISS JOSEPH	26
29	JOEL MATHEW	50
30	JOHNS PATTARA	29
31	JOSE ABRAHAM	35
32	JOSIL N D	48
33	JOYAL SIBY	26
34	JULIT SHAJI	45
35	KARTHIKA N	44
36	MELWIN SHAJU	25
37	NAYANA MANOJ	49
38	NAYANA MOL JOSEPH	48
39	PRITHVIN A NAIR	18
40	SACHIN JOSHY	39.5
41	SARATH P V	48
42	SHARON JUDY	41
43	SHARON MERIN SABU	44
44	SONU JACOB	14
45	THEJAS KRISHNA.J	11

Sample – Continuous Internal Evaluation

SERIES EXAM CO SPLIT-UP MARKS

Name of evaluation: Series Exam 1			Total Mark: 50					Series exam name: Series Exam 1						
Roll No	Name	1 (3.00) CO1	2 (3.00) CO1	3 (3.00) CO2	4 (3.00) CO2	5 (3.00) CO3	6 (7.00) CO1	7 (7.00) CO1	8 (7.00) CO1	9 (7.00) CO2	10 (7.00) CO2	11 (7.00) CO2	12 (7.00) CO3	Total
1	AARYA SARA ALEX	3	3	3	3	2.5	7	7		7	7		6.5	49
2	ABHISREE B	0	2	0	3	0	5		6		7	2.5	2.5	28
3	ADITHYAN S PHILIP	2	2	0	0	2.5	3.5		4	0	2		3	19
4	AISWARYA JOSE	3	2.5	0	2.5	1.5	2		2.5		3	2.5	2.5	22
5	AJIL PAVITHRAN	1.5	2.5	2	3	3	3.5		4.5	6	6		5	37
6	AKSHAI P S	2	1.5	2	2.5	2	3		4		4.5	2.5	5	29
7	ALAN GEORGE	3	2	2	3	2	7	2		5	7		5	38
8	ALAN THOMAS	1.5	2.5	2	2	2	6		7	7	7		7	44
9	ALBIN JOSE	0	1.5	0	1	0.5	0	0		1		3.5	1	8.5
10	ALEENA MARGARET	3	3	3	3	3	7	4		7	7		6	46
11	ALEX BABU JOSEPH	3	2	2.5	1.5	2	4.5	2.5		4	5		5	32
12	ANET SUNNY	0	3	0	1.5	0	6.5		7	0	1		0	19
13	ANISIA S PULICKAN	2.5	2	3	3	3	7		5.5	7	7		7	47
14	ANJALI RACHEL ANISH	3	2.5	3	0.5	3	6		5	7	2		0	32
15	ANN MARIA JOHNEY	0.5	2	2	1.5	0	6		5	2		3	2	24
16	ANN MARIYA SHAJI	0	3	3	3	2	6		5	6	7		6	41
17	ANU SOMARAJAN K	3	2.5	3	2	2	5		6.5	5	2		6	37
18	ARJUN KRISHNA C V	0	2	3	1.5	2	2.5	0		0	0		4	15
19	ARJUN T S	0	1.5	3	1.5	0	2		3	0	7		0	18
20	ASHLEY ELIZABETH	3	3	3	3	2	7		6		7	4	5	43
21	ATHUL KRISHNA SURESH	0	2.5	2.5	3	2	7		4	6	7		0	34
22	DIYA SEBASTIAN	3	3	3	3	2.5	7	5		5.5	7		6	45
23	ELVIN ALOSIOUS	0.5	1	1	3	2.5	5	2			6.5	3	4.5	29
24	GLADWIN BIJU	0.5	1	3	3	3	7	1.5			6	7	5	37
25	ISAAC GEORGE	3	2	0	2	3	6.5	3.5			5	4	7	36
25	GOKUL V	0.5	2	0	0	0	6		5.5	0	0		6	20
26	JACOB MONCY	3	3	3	0	2	7		3		7	3	5	36
27	JEEVANJOSEPH P B	3	3	3	2.5	1.5	7		5.5	7	5.5		6	44
28	JISS JOSEPH	2.5	0	2.5	3	0	1.5	1.5		5.5		5.5	4	26
29	JOEL MATHEW	3	3	3	3	3	7		7		7	7	7	50
30	JOHNS PATTARA	1	2	3	3	3	2		5	4	0		6	29
31	JOSE ABRAHAM	0.5	2	3	3	2	6		4		6	4.5	4	35
32	JOSIL N D	3	3	3	3	2.5	7	6.5			7	6.5	6.5	48
33	JOYAL SIBY	3	3	0			6	4			5	5		26
34	JULIT SHAJI	3	3	3	3	2.5	7		5.5		7	4.5	6.5	45
35	KARTHIKA N	3	2.5	2.5	1.5	3	7		6	7	6		5.5	44
36	MELWIN SHAJU	0	1	3	1	2.5	6.5		3	3	0		5	25
37	NAYANA MANOJ	3	3	3	3	3	7		6	7	7		7	49
38	NAYANA MOL JOSEPH	3	3	3	3	2.5	7		6		7	7	6.5	48
39	PRITHVIN A NAIR	0	0	3	2	1.5	0	0		5	4.5		2	18
40	SACHIN JOSHY	1.5	2	3	2.5	2	5		5.5	7	6		5	39.5
41	SARATH P V	2.5	2.5	3	3	3	7		6.5	7	7		6.5	48
42	SHARON JUDY	3	1	3	3	3	7		3	7	4		7	41
43	SHARON MERIN SABU	1	3	3	2.5	3	7	4			7	6.5	7	44
44	SONU JACOB	0	2	0	0	3	1	2		0	0		6	14

Sample – Continuous Internal Evaluation CO wise split-up

Roll No	Name	Attendance (20.00 %)		series (50.00 %) Method: Average of Selected			assignment (30.00 %) Method: Average of Selected			Total	
			(out of 10)	Series Exam 1(out of 50)	Series Exam 2(out of 50)	(out of 25)	Assignment 1(out of 15)	Assignment 2(out of 15)	(out of 15)	Grace Marks	Final Score (out of 50)
1	AARYA SARA ALEX	47/62 (76)	7.6	49	0	12.3	15	12	13.5	0	43
2	ABHISREE B	56/62 (90)	10	28	0	7	15	14	14.5	0	38
3	ADITHYAN S PHILIP	56/62 (90)	10	19	0	4.8	15	15	15	0	36
4	AISWARYA JOSE	52/62 (84)	8.4	22	0	5.5	15	14.5	14.8	1	35
5	AJIL PAVITHRAN	54/62 (87)	8.7	37	0	9.3	15	15	15	0	40
6	AKSHAI PS	61/62 (98)	10	29	0	7.3	15	15	15	0	36
7	ALAN GEORGE	55/62 (89)	8.9	38	0	9.5	15	14.5	14.8	0	41
8	ALAN THOMAS	55/62 (89)	8.9	44	0	11	15	15	15	0	47
9	ALBIN JOSE	60/62 (97)	10	8.5	0	2.1	15	15	15	1	35
10	ALEENA MARGARET	59/62 (95)	10	46	0	11.5	15	15	15	0	46
11	ALEX BABU JOSEPH	59/62 (95)	10	32	0	8	15	15	15	0	42
12	ANET SUNNY	53/62 (85)	8.5	19	0	4.8	15	15	15	0	38
13	ANISIA S PULICKAN	59/62 (95)	10	47	0	11.8	15	14.5	14.8	0	48
14	ANJALI RACHEL ANISH	47/62 (76)	7.6	32	0	8	15	14.5	14.8	0	35
15	ANN MARIA JOHNEY	55/62 (89)	8.9	24	0	6	15	15	15	0	35
16	ANN MARIYA SHAJI	58/62 (94)	10	41	0	10.3	15	15	15	0	44
17	ANU SOMARAJAN K	55/62 (89)	8.9	37	0	9.3	15	15	15	0	43
18	ARJUN KRISHNA CV	50/62 (81)	8.1	15	0	3.8	15	14.5	14.8	0	31
19	ARJUN TS	53/62 (85)	8.5	18	0	4.5	15	14.5	14.8	1	35
20	ASHLEY ELIZABETH	59/62 (95)	10	43	0	10.8	15	15	15	0	45
21	ATHUL KRISHNA SURESH	55/62 (89)	8.9	34	0	8.5	15	14.5	14.8	0	39
22	DIYA SEBASTIAN	56/62 (90)	10	45	0	11.3	15	15	15	0	47
23	ELVIN ALOSIOUS	55/62 (89)	8.9	29	0	7.3	15	14.5	14.8	0	38
24	GLADWIN BIJU	57/62 (92)	10	37	0	9.3	15	14.5	14.8	0	43
25	ISAAC GEORGE	61/62 (98)	10	36	0	9	15	15	15	0	42
25	GOKUL V	44/62 (71)	7.1	20	0	5	15	12	13.5	0	31
26	JACOB MONCY	49/62 (79)	7.9	36	0	9	15	15	15	0	32
27	JEEVANKO SEPH P B	59/62 (95)	10	44	0	11	15	15	15	0	44
28	JISS JOSEPH	53/62 (85)	8.5	26	0	6.5	15	14.5	14.8	0	35
29	JOEL MATHEW	58/62 (94)	10	50	0	12.5	15	15	15	0	49
30	JOHNS PATTARA	55/62 (89)	8.9	29	0	7.3	15	15	15	0	37
31	JOSE ABRAHAM	51/62 (82)	8.2	35	0	8.8	15	14.5	14.8	0	39
32	JOSIEL N D	55/62 (89)	8.9	48	0	12	15	15	15	0	43
33	JOYAL SIBY	47/62 (76)	7.6	26	0	6.5	15	15	15	0	33
34	JULFT SHAJI	60/62 (97)	10	45	0	11.3	15	15	15	0	46
35	KARTHIK AN	62/62 (100)	10	44	0	11	15	15	15	0	46
36	MELWIN SHAJU	56/62 (90)	10	25	0	6.3	15	15	15	0	35

Sample Internal Mark calculation

ECE 2020-2024 A (S5)
CO BASED EVALUATIONS

ECT301 - LINEAR INTEGRATED CIRCUITS

	CO1	CO2	CO3	CO4	CO5
CO attainment- Series Exam 1	3	3	3	-	-
Weightage	50	33.33	33.33	-	-
CO attainment- Assignment 1	3	3	-	-	-
Weightage	50	33.33	-	-	-
CO attainment- SERIES EXAM 2	-	-	1	1	-
Weightage	-	-	33.33	50	50
CO attainment- Assignment 2	-	3	3	3	3
Weightage	-	33.33	33.33	50	50

CO - ATTAINMENT

CO	Internal(70.00%)	External(30.00%)	Direct Attainment(80.00%)	Indirect Assessment(20.00%)	Overall Attainment
CO1	3	0.00	2.10	3.00	2.28
CO2	2.9997	0.00	2.10	3.00	2.28
CO3	2.3331	0.00	1.63	3.00	1.91
CO4	2	0.00	1.40	3.00	1.72
CO5	1.5	0.00	1.05	3.00	1.44
Overall CO			1.926		

CO - PO MAPPING

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2
CO1	3	3	1	2								1	3	2
CO2	3	3	2	2	2							1	3	2
CO3	3	3	2	2	2							1	3	2
CO4	3	3	1	2	2							1	3	2
CO5	3	3	2	2	2							1	3	2
Direct Attainment	1.93	1.93	1.02	1.28	1.22							0.64	1.93	1.28

CO - ATTAINMENT

CO	Internal(70.00%)	External(30.00%)	Direct Attainment(80.00%)	Indirect Assessment(20.00%)	Overall Attainment
CO1	3	3.00	3.00	3.00	3.00
CO2	3	3.00	3.00	3.00	3.00
CO3	3	3.00	3.00	3.00	3.00
CO4	3	3.00	3.00	3.00	3.00
CO5	3	3.00	3.00	3.00	3.00
CO6	3	3.00	3.00	3.00	3.00
Overall CO			3		

CO - PO MAPPING

CO	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
CO1	1	1	1									1	1		
CO2	2	3	3	2								3	3	3	3
CO3	2	3	3	2								3	3	3	3
CO4	2	3	3							1		3	3	3	3
CO5	2	3	3							1		3	3	3	3
CO6	1	1	1		2					1		3	1	3	1
Direct Attainment	1.67	2.33	2.33	2.00	2.00					1.00		2.67	2.33	3.00	2.60

Sample CO Attainment and PO Contribution Calculation

Survey Session: CE-ECT301-ECE 2020-2024 A (S5)		Users Completed: 46			
Section	Question	Option	Mark	Users Opted	Opted %
1	1. Understand Op-amp fundamentals and differential amplifier configurations	excellent	5.00	22	47.83 %
		very good	4.00	16	34.78 %
		good	3.00	7	15.22 %
		fair	2.00	1	2.17 %
		poor	1.00	0	0 %
	2. Design operational amplifier circuits for various applications	excellent	5.00	24	52.17 %
		very good	4.00	15	32.61 %
		good	3.00	6	13.04 %
		fair	2.00	1	2.17 %
		poor	1.00	0	0 %
	3. Design oscillators and active filters using op-amps	excellent	5.00	20	43.48 %
		very good	4.00	15	32.61 %
		good	3.00	10	21.74 %
		fair	2.00	1	2.17 %
		poor	1.00	0	0 %
	4. Explain the working and applications of timer, VCO and PLL ICs	excellent	5.00	22	47.83 %
		very good	4.00	14	30.43 %
		good	3.00	9	19.57 %
		fair	2.00	1	2.17 %
		poor	1.00	0	0 %
5. Outline the working of voltage regulator ICs and Data converters	excellent	5.00	24	52.17 %	
	very good	4.00	12	26.09 %	
	good	3.00	8	17.39 %	
	fair	2.00	2	4.35 %	
	poor	1.00	0	0 %	
Remarks: No remarks found.					

CO-Wise Result				
CO.1	CO.2	CO.3	CO.4	CO.5
3.00	3.00	3.00	3.00	3.00

Course Exit survey

ECE 2020-2024 A (S5)
ATTENDANCE SUMMARY

ECT301 - LINEAR INTEGRATED CIRCUITS

Roll No	Student	19/09	20/09	23/09	24/09	29/09	30/09	06/10	14/10	15/10	17/10	18/10	20/10	21/10	22/10	28/10	03/11	Total				
		7	1	6	1	4	7	6	4	3	6	4	7	1	4	1	6		6	2	4	6
1	ANRVA SARA ALEX	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	47/62 (76)
2	ANHSREE B	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	A	58/62 (93)
3	ANITHYAN S PHILIP	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
4	ANWARAYA JOSE	P	P	P	P	A	A	P	P	P	P	A	P	P	P	P	P	P	P	P	P	52/62 (84)
5	ARL PAVITHRAN	P	P	P	P	P	P	P	P	P	P	A	P	D	P	P	P	P	P	P	P	54/62 (87)
6	ARHUL PS	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
7	ALAN GEORGE	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
8	ALAN THOMAS	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	59/62 (95)
9	ALBIN JOSE	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
10	ALEENA MARGARET	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	58/62 (93)
11	ALEX BABU JOSEPH	P	P	P	P	P	P	P	P	P	P	P	P	D	P	P	P	P	P	P	P	58/62 (93)
12	ANET SUNNY	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
13	ANBHA S PULICKAN	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	58/62 (93)
14	ANALI RACHEL ANISE	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	47/62 (76)
15	ANN MARIA JOSEPH	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	60/62 (96)
16	ANN MARIYA SHAJ	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	58/62 (93)
17	ANU SOMARAJAN K	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	59/62 (95)
18	ARJUN KEBHINA C V	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
19	ARJUN T S	P	P	P	P	A	A	P	P	P	P	A	A	P	P	P	P	P	P	A	P	53/62 (85)
20	ASHLEY ELIZABETH	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
21	ATHUL KRISHNA SURISH	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	A	P	60/62 (96)
22	DIVA SEBASTIAN	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	58/62 (93)
23	ELVIN ALDOBUS	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	60/62 (96)
24	ELADWIN HIRU	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	57/62 (92)
25	ISAAC GEORGE	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
26	GORIL V	A	P	P	P	A	A	P	P	P	P	A	P	A	P	P	A	A	P	A	P	44/62 (73)
26	JACOB MONCY	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	A	P	48/62 (77)
27	JEEVANJOSEPH B	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	58/62 (93)
28	JER JOSEPH	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	60/62 (96)
29	JOEL MATHEW	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	58/62 (93)
30	JOHNS PATTARA	P	P	P	P	P	P	P	P	P	P	P	P	D	P	P	P	P	P	P	P	55/62 (89)
31	JOSE ABRAHAM	P	P	P	P	A	P	P	P	P	P	A	A	A	P	P	P	P	P	P	P	60/62 (96)
32	JOSE N D	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	55/62 (89)
33	JOYAL BIRU	P	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	A	P	47/62 (76)
34	JULIT SHAJ	P	P	P	P	A	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	48/62 (77)
35	KARTHIKA N	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	62/62 (100)
36	MELVIN SHAJI	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
37	NAYANA MAMU	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
38	NAYANA SHIL JOSEPH	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
39	PRITHVIN S NAR	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	60/62 (96)
40	RACHIN JOSHY	P	P	P	P	P	P	P	P	P	P	A	P	P	P	P	P	P	P	P	P	57/62 (92)
41	SARATH PV	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	57/62 (92)
42	SHARON LUDY	A	A	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
43	SHARON MERIN SABU	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	P	60/62 (96)
44	SONU JACOD	P	P	P	P	P	P	P	P	P	P	P	P	D	P	P	P	P	P	P	P	58/62 (93)
45	THEJAS KRISHNA J	P	P	P	P	A	A	P	P	P	P	P	P	P	P	P	P	P	P	P	P	64/62 (103)

Sample Attendance Entry

Audit Report

To verify the adherence of activities with the academic calendar and the stipulated regulations, regular audits are conducted in the institute. The internal audit is conducted under the purview of the IQAC, who deposes trained auditors to check proper course planning, adherence to academic calendars, content delivery, evaluation processes and completeness along with other metrics. The University also conducts Audits to verify the same. A sample of the academic audit report is attached herewith.



HoD EC SJCET Palai <hodec@sjcetpalai.ac.in>

External Audit Report

Dean UG <deanug@sjcetpalai.ac.in>

Sat, Dec 3, 2022 at 3:50 PM

To: HoD EC SJCET Palai <hodec@sjcetpalai.ac.in>

Respected Arun Sir,

Please find the attachment. Also provide HoD's remarks in the Principal's response column and resend it to me on or before 7th December 2022. Once again congratulations to you as well as your department for securing a good rating in the external audit.

--

WITH REGARDS,

Dr. GEORGE TOM VARGHESE

DEAN(UG) & HoD (EIE)

ST. JOSEPH'S COLLEGE OF ENGINEERING AND TECHNOLOGY, PALAI

MOBILE NUMBER: 9437777895 / 9497322385

 **ECE.docx**
26K

Sample of mail received with academic audit report as attachment



APJ Abdul Kalam Technological University
CET Campus, Thiruvananthapuram
Kerala -695016
India

Academic Audit Report
2022 - 2023

Basic Details

Institution	ST JOSEPHS COLLEGE OF ENGINEERING AND TECHNOLOGY PALAI
First Auditor Name	Dr Amith Aravind
Second Auditor Name	Dr Rajesh J
Visit	First
Semester Type	Odd

College Specific Assessments

Key Aspects	Rating	Auditor Remarks	Principal's Response
Compliance to the Academic Calendar of KTU (A)	Excellent(5)	College Calendar prepared in line with the university academic calendar	Noted
Functioning of students grievances and appeal committee (A)	Excellent(5)		Noted
Functioning of Academic Discipline Welfare committee (A)	Excellent(5)		Noted
Average student to faculty ratio (A)	Excellent(5)	1.07	Noted
Faculty Retention (A)	Excellent(5)		Noted
Faculty Qualification Index (A)	Good(4)	5.75	52 faculty members are currently pursuing Ph.D.
Number of qualified technical staff (A)	Excellent(5)	36 nos	Noted

Sample Page 1 of the Audit report

Result Analysis

After the University End Semester examination results are published, an indept analysis of the result is performed to give insight on the performance of students, the methodology of content delivery and a quality loop is maintained with recommendations from the course handling faculty and stream coordinator on suggestions to modify course planning and content delivery in the next semester. A sample of the Result Analysis is attached herewith.



ST. JOSEPH'S COLLEGE OF ENGINEERING AND TECHNOLOGY, PALAI
 Choondacherry P.O, Palai, Kottayam 686 579, Kerala, India.
 Phone: 04822239700, principal@sjcetpalai.ac.in

Batch Academics Report

Sl No	Course	Batch	Student	Is Passed	Internal Mark	CGPA	Current Backlogs	Total Backlogs
1	Electronics and Communication Engineering	ECE 2019-2023 (S8)	AGNES TERESA MALIACKAL	No	0	7.75	1	1
2	Electronics and Communication Engineering	ECE 2019-2023 (S8)	AISWARYA SUKUMARAN	No	0	2.60	17	17
3	Electronics and Communication Engineering	ECE 2019-2023 (S8)	AKSHAY A	No	0	7.57	1	1
4	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ALBIN SEBASTIAN	Yes	0	9.21	0	0
5	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ALEENA ANIL	No	0	6.84	2	2
6	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ALLEN TOM VINCENT	Yes	0	7.16	0	0
7	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ANANDHU ANIL KUMAR	Yes	0	8.66	0	0
8	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ANIE GEORGE	No	0	5.75	4	4
9	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ANJU MARIA RAJU	No	0	2.63	18	19
10	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ANNS MARIA SAJI	No	0	6.30	4	4
11	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ANUPAMA MONCY THOMAS	Yes	0	8.39	0	0
12	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ARCHANA VISWAPPAN	No	0	5.65	5	5
13	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ASHIL GEORGE	Yes	0	7.87	0	0
14	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ASWIN M.R	Yes	0	8.09	0	0
15	Electronics and Communication Engineering	ECE 2019-2023 (S8)	BRINTO JAMES	Yes	0	7.95	0	0
16	Electronics and Communication Engineering	ECE 2019-2023 (S8)	DANA FATHIMA	No	0	7.10	1	1
17	Electronics and Communication Engineering	ECE 2019-2023 (S8)	DEVIKA G	No	0	7.46	2	2
18	Electronics and Communication Engineering	ECE 2019-2023 (S8)	DONALD M JOSE	No	0	5.26	6	6
19	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ELBIN THOMAS	Yes	0	8.14	0	0
20	Electronics and Communication Engineering	ECE 2019-2023 (S8)	ELIAS K PHILIP	No	0	5.27	5	6

1

Sample page 1 of result analysis

More documents supporting the activities in the curriculum planning and implementation are added in the subsequent link.