



ST. JOSEPH'S
COLLEGE OF ENGINEERING
AND TECHNOLOGY.
- PALAI -
AUTONOMOUS

Choondacherry P.O., Pala, Kottayam - 686579
Kerala, India



SYLLABUS

B.Tech.

ELECTRICAL AND ELECTRONICS ENGINEERING

2024 SCHEME

Semester V & VI

COURSES

SEMESTER – V

FIFTH SEMESTER (July-December)														
Sl. No:	Slot	Course Code	Course Type	Course Category	Course Title (Course Name)	Credit Structure				SS	Total Marks		Credits	Hrs./ Week
						L	T	P	R		CIE	ESE		
1	A	24SJPCCEET501	PC	PC	Power Generation, Transmission and Protection	3	1	0	0	5	40	60	4	4
2	B	24SJPCCEET502	PC	PC	Electromagnetic Theory	3	1	0	0	5	40	60	4	4
3	C	24SJPCCEET503	PC	PC	Signals and Systems	3	0	0	0	4.5	40	60	3	3
4	D	24SJPBEET504	PC-PBL	PB	Microprocessor and Embedded Systems	3	0	0	1	5.5	60	40	4	4
5	E	24SJPEEET52N	PE	PE	PE-2	3	0	0	0	4.5	40	60	3	3
6	I*	24SJICHUM506	HMC	IC	Constitution of India (MOOC)	-	-	-	-	2	-	-	1	-
7	L	24SJPCCEEL507	PCL	PC	AC Machines Lab	0	0	3	0	1.5	50	50	2	3
8	Q	24SJPCCEEL508	PCL	PC	Microprocessor and Embedded Systems Lab	0	0	3	0	1.5	50	50	2	3
9	R/M/H		VAC		Remedial/Minor/Honours Course	3	1	0	0	5			4*	4*
	S ₅ /S ₆	Industrial Visit (Maximum 06 Days are permitted, Not exceeding more than 4 working days) /Industrial Training												
Total									30/35			23/27*	24/28*	

*No Grade Points will be awarded for the MOOC course and I slot course.

Industrial Training: Students who are not participating in the industrial visit must attend industrial training during that period.

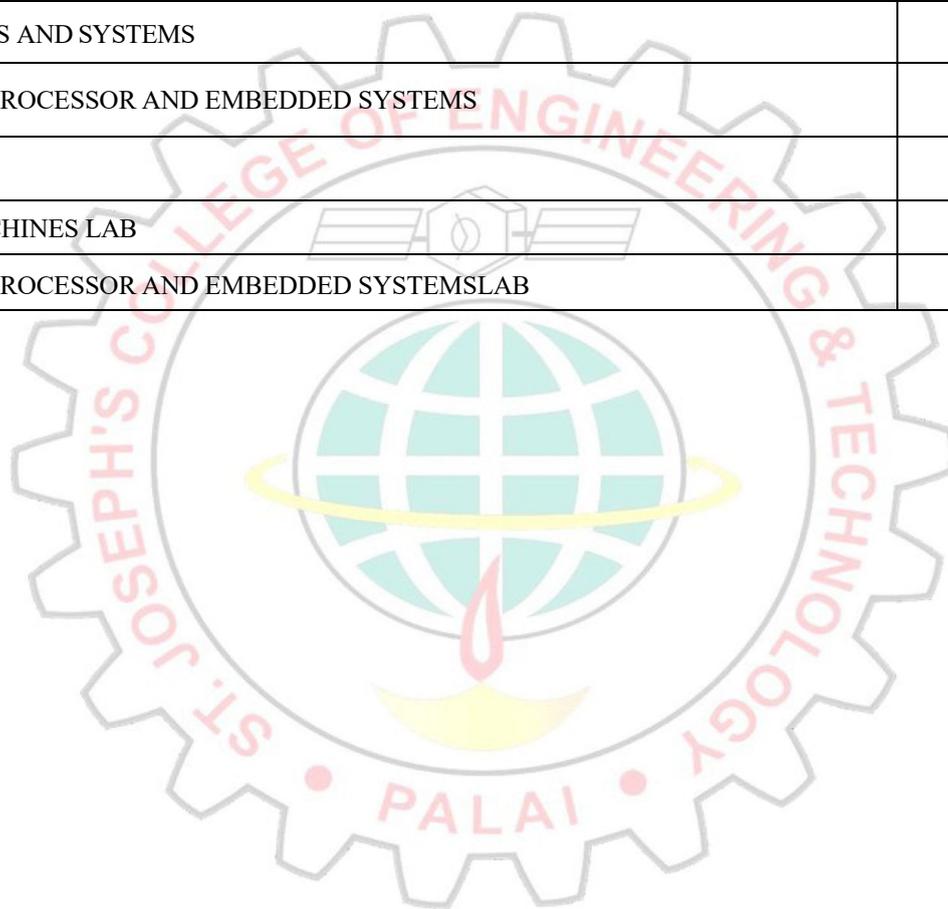
PROGRAM ELECTIVE 2: PEEET52N					
SLOT	COURSE CODE	COURSES	L-T-P-R	HOURS	CREDIT
E	24SJPEEET521	Energy Storage Systems	3-0-0-0	3	3
	24SJPEEET522	Electric Vehicles	3-0-0-0		3
	24SJPEEET523	Digital System Design	3-0-0-0		3
	24SJPEEET524	Software Engineering	3-0-0-0		3
	24SJPEEET526	Data Structures	3-0-0-0		3
	24SJPEEET527	Introduction to Machine Learning	3-0-0-0		3
	24SJPEEET528	Computer Network Systems	3-0-0-0		3

COURSES

COURSES

SEMESTER – V

Course Name	Page No.
POWER GENERATION, TRANSMISSION AND PROTECTION	1
ELECTROMAGNETIC THEORY	5
SIGNALS AND SYSTEMS	8
MICROPROCESSOR AND EMBEDDED SYSTEMS	12
PE-2	16
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MICROPROCESSOR AND EMBEDDED SYSTEMSLAB	51



SEMESTER S5**POWER GENERATION, TRANSMISSION AND PROTECTION**

Course Code	24SJPCEET501	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:1:0:0	ESE Marks	60
Credits	4	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJPCEET302	Course Type	Theory

Course Objectives:

1. To deliver fundamental concepts in power system components.
2. To deliver basic idea of power generation, transmission and protection.
3. To introduce new topics to students like energy storage systems and deregulated systems.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Generation from renewable and non-renewable sources – Hydro, thermal, nuclear- (block schematic details, environmental and ethical factors, advantages, disadvantages)</p> <p>Solar and wind - (block schematic details, environmental factors, regulations, advantages, disadvantages).</p> <p>Energy storage systems as alternative energy sources – BESS, CESS, thermal storage systems.</p> <p>Load curve – Load duration curve, Load factor, diversity factor, demand factor, Plant capacity factor, plant use factor - Numerical Problems.</p>	11
2	<p>Power Transmission System - (Electrical Model)- Line parameters – resistance - inductance and capacitance (Derivation of three phase double circuit)</p> <p>Transmission line modelling - classifications (concept only) – transmission line as two port network - derivation and calculation of ABCD parameters. (derivation and numerical problems)</p>	10

3	<p>Skin Effect & Ferranti Effect – Corona (qualitative study only) – Surge Impedance Loading. Insulators: string efficiency – grading (numerical problems).</p> <p>Calculation of Sag and tension in transmission lines (numerical problems).</p> <p>Introduction to EHVAC and HVDC: Principle, advantages/disadvantage.</p> <p>Underground cables – ratings - classification - Capacitance of cables – grading – 2 types .</p> <p>AC Distribution systems – connection schemes – radial and ring main systems – single phase only (numerical problems)</p> <p>Method of power factor improvement using capacitors (numerical problems)</p> <p>Tariff: different types. Introduction to energy markets (regulated and deregulated systems)</p>	13
4	<p>Need for protection- Types of protection schemes – primary and back-up Protective relays – Basics of typical electromechanical relay – induction type only. Static (block diagrams of o/c and instantaneous o/c relays). Microprocessor (block diagram and flow chart of o/c relay). Fundamentals of Numerical relay.</p> <p>Principles of overcurrent, directional, distance and differential relays.</p> <p>Circuit breakers – operating principle – arc phenomenon – arc extinction – principle & methods – Important terms in arc extinction.</p> <p>Problems of circuit interruption – capacitive current chopping – ratings of CBs - Circuit breaker classification based on medium of arc extinction – SF6 & VCB Introduction to GIS</p>	11

Additional topics:

- 1) Introduction to Machine Learning in Power System Protection – Insulation co-ordination
- 2) Overview of Communication: PLCC - Fibre Optic - Introduction to IEC61850
- 3) Visit to a nearby substation, identify the components and prepare a report.

Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 3 marks <p>(8x3 =24marks)</p>	<ul style="list-style-type: none"> Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. <p>(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Learn different types of power generating systems and schedule generation appropriate for a given area.	K3
CO2	Evaluate the electrical performance of any transmission line.	K3
CO3	Compute various physical characteristics of overhead and underground transmission systems.	K3
CO4	Demonstrate the working of relays and switch gear for protection schemes.	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	2	1	-		2					2
CO2	3	2	1	-							2
CO3	3	2	1	-							2
CO4	3	2	1	-							2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Electrical Power Systems	Wadhwa C. L.	New Age International	8 th edition 2023
2	Principles of Power System	V. K. Mehta and Rohit Mehta	S. Chand	4 th edition reprint 2020
3	Power System Protection and Switchgear	Badri Ramand D.N.Viswakarma	Tata McGraw Hill	2 nd edition, 2011
4	Non-conventional energy sources	B. H. Khan	Tata McGraw Hill	3 rd edition, 2017

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Engineering and Chemical Thermodynamics	Milo D. Koretsky	Wiley	2 nd Edn, 2012
2	Chemical and Process Thermodynamics	Kyle B.G.	Pearson	3 rd Edn, 2015

Video Links (NPTEL, SWAYAM...)

Module No.	Link ID
1	https://archive.nptel.ac.in/courses/103/103/103103144/
2	https://archive.nptel.ac.in/courses/103/103/103103144/
3	https://archive.nptel.ac.in/courses/103/103/103103144/
4	https://archive.nptel.ac.in/courses/103/103/103103144/

SEMESTER S5

ELECTROMAGNETIC THEORY

Course Code	24SJPCEET502	CIE Marks	40
Teaching Hours/Week (L:T:P: R)	3:1:0:0	ESE Marks	60
Credits	4	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJGYMAT201	Course Type	Theory

Course Objectives:

- To familiarize the students with the fundamentals of electrostatics, magnetostatics, timevarying fields and electromagnetic waves.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Mathematical Preliminaries : Rectangular, Cylindrical and Spherical Coordinate Systems - Representation of Point, Unit vector, Vector, Constant surfaces, Transformation of points, unit vectors and vectors among the three coordinate systems - Transformation matrices, Del operator - Representation in the three coordinate systems, Gradient of scalar field - Physical meaning of gradient, Divergence of a vector field - Physical significance of divergence - Divergence Theorem, Curl of a vector field - Physical significance of curl - Stoke's Theorem.</p> <p>Electrostatic Fields : Coulomb's Law, Electric Field Intensity, Force and Field due to system of charges, Gauss's Law - integral form, Electric Flux Density, Field due to line of charge, surface and volume charge distributions.</p>	11
2	<p>Electrostatic Fields in material media : Gauss's law - point form, Electric potential, Relation between E and V, Field due to electric dipole, Energy density in static electric fields, Conduction and Convection Current, Ohm's law in point form, Resistance, Capacitance of parallel plate capacitor, Coaxial and Spherical capacitors, Continuity equation, Boundary conditions, Poisson's and Laplace's Equations (solution not required)</p> <p>Magnetostatics : Biot Savart's Law, Ampere's Circuital Law in integral and point form, Magnetic field due to infinite line current, infinite sheet of current, Coaxial cable, Non conservativeness of magnetic field, Magnetic scalar potential, Magnetic vector potential.</p>	11

3	<p>Magnetostatics in Material Media : Force on a charged particle due to a magnetic field, Force between two current carrying conductors, Magnetic Torque and Moment, Magnetization in materials, Magnetic boundary conditions, Inductance, Energy stored in magnetostatic fields.</p> <p>Electromagnetic Induction and Maxwell- Heaviside Equations: Faraday's law, Transformer emf and Motional emf, Displacement Current, Maxwell-Heaviside equations.</p>	10
4	<p>Electromagnetic Waves : Time varying potentials, Waves in general, Electromagnetic waves, Wave propagation in lossy dielectrics, Plane waves in free space, conductors, skin effect, Power, Poynting theorem, Reflection of plane wave at normal incidence.</p> <p>Transmission Lines: Transmission line equations, Characteristic impedance, Input impedance, Standing wave ratio.</p>	10

Course Assessment Method (CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> • 2 Questions from each module. • Total of 8 Questions, each carrying 3 marks <p style="text-align: center;">(8x3 =24marks)</p>	<ul style="list-style-type: none"> • Each question carries 9 marks. • Two questions will be given from each module, out of which 1 question should be answered. • Each question can have a maximum of 3 sub divisions. <p style="text-align: center;">(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Apply vector calculus in Electricity and Magnetism.	K3
CO2	Compute electric and magnetic fields in different media	K3
CO3	Deduce the Maxwell-Heaviside Equations from the basic laws of electricity and magnetism	K3
CO4	Compare the propagation of Electromagnetic waves in different media.	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	3			1						
CO2	3	3			1						
CO3	3	3			1						
CO4	3	3			1						

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Elements of Electromagnetics	Mathew N O Sadiku	Oxford University Press	7th Edition, 2018
2	Engineering Electromagnetics	William H Hayt Jr, John A Buck	Tata McGraw Hill	9th Edition, 2018

Reference Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Introduction to Electrodynamics	David J Griffiths	Cambridge University Press	4th Edition, 2017

SEMESTER S5

SIGNALS AND SYSTEMS

Course Code	24SJPCEET503	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	Mathematics for Electrical Science	Course Type	Theory

Course Objectives:

1. To introduce time domain and frequency domain representation of continuous and discrete time signals and perform various mathematical operations
2. To introduce various types of signals and systems
3. To introduce time domain and frequency domain representation of continuous and discrete time systems.
4. To familiarize mathematical modelling of dynamic systems.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Introduction to Signals and Systems: Elementary Signals, Classification and representation of continuous time and discrete time signals, Signal operations. (3 hours) Concept of system: Continuous time and discrete time systems; Properties of systems: Time invariance, Linearity, Causality, Systems with and without memory, Stability. (3 hours) Convolution Integral and sum. (2 hours) Impulse and step response. (1 hour)	9

2	<p>Frequency domain characterization of Signals and Systems: <i>Fourier transform:</i> Existence - Properties of Continuous time Fourier transform; Concept of Frequency response; Significance of Fourier transform and difference from Fourier series. (3 hours)</p> <p>Review of Laplace Transforms.</p> <p><i>Characterization of LTI systems:</i> Differential equation representation of continuous time LTI systems. Transfer function representation of differential equation in Laplace domain. (2 hours)</p> <p><i>Modeling of LTI systems:</i> Electrical, translational and rotational mechanical systems, DC servo-motor; Force voltage, Force current analogy. (4 hours)</p>	9
3	<p>Sampled Data Systems and Z-Transform: Sampling process - Impulse train sampling-sampling theorem- Aliasing effect. (2 hour)</p> <p>Zero-order and First-order hold circuits - Signal reconstruction. (2 hours)</p> <p><i>Z-Transform:</i> Region of convergence- Properties of Z-Transform. Inverse Z-Transform. Difference equation representations of LTI systems - Analysis of difference equation of LTI systems (3 hours)</p> <p>Impulse and step response of discrete-time systems. (2 hours)</p>	9
4	<p>Analysis of Sampled Data Systems: Discrete convolution and its properties. (2 hours)</p> <p>Delay operator and block diagram representation- Direct form I & II (2 hours)</p> <p>Discrete Time Fourier Transform: Properties- Frequency response of simple DT systems. (2 hours)</p> <p>The Discrete Fourier Transform - DFT as a linear transformation (Matrix Relation), IDFT, Properties of DFT (proof not necessary). (3 hours)</p>	9

Course Assessment Method (CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 3 marks (8 x 3 = 24 marks) 	<ul style="list-style-type: none"> Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. (4 x 9 = 36 marks) 	60

Course Outcomes (COs)

At the end of the course students should be able:

Course Outcome	Bloom's Knowledge Level (KL)
CO1 To represent continuous and discrete time signals in time domain and perform various mathematical operations	K2
CO2 To represent continuous time signals and systems in frequency domain	K3
CO3 To analyze discrete time signals and systems in Z-domain.	K3
CO4 To analyze the sampled data systems	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyze, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	2	1	2	1	3	3			3	2
CO2	3	3	2	2	2	3	3			3	2
CO3	3	3	2	2	2	3	3			3	2
CO4	3	3	2	2	2	3	3			3	2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), No Correlation

Text Books

SI. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Signals and Systems	Simon Haykin, Barry Van Veen	Wiley	2nd Edition, 2007
2	Discrete Time Control Systems	Katsuhiko Ogata	Pearson	2nd Edition, 2006
3	Control Systems Engineering	Norman S. Nise	Wiley	5th Edition, 2009

Reference Books

SI. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Signals and Systems	Oppenheim A.V., Willsky A.S. & Nawab S.H.	Prentice Hall	2nd Edition, 2015
2	Modern Control Systems	Dorf R. C., Bishop R. H	Pearson Education India	12th Edition, 2013
3	Digital Signal Processing Principles	John G. Proakis & Dimitris G. Manolakis	Prentice Hall	4th Edition, 2007

SEMESTER S5

MICROPROCESSORS AND EMBEDDED SYSTEMS

Course Code	24SJPBEET504	CIE Marks	60
Teaching Hours/Week (L: T:P: R)	3:0:0:1	ESE Marks	40
Credits	4	Exam Hours	2Hrs. 30 Min.
Prerequisites (if any)	24SJPBEET304, 24SJPBEET404, 24SJGXEST204	Course Type	Theory

Course Objectives:

1. This course aims to design and implement Embedded Systems using latest microprocessors / Microcontroller based boards.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Introduction to microprocessors- Features and Architecture of 8085- Registers of 8085 - Flags - 8085 Pin diagram- Pins, Signals and functions- Addressing modes-Classification of instructions. - Assembly language programming- Basic Instruction set to write Simple programs - Arithmetic, Logical, Branching instructions, Opcodes, hand coding, Subtraction, Memory Reading and writing, Sorting</p> <p>Stack and Subroutines – CALL and RETURN instructions–Machine cycles, instruction cycle and T states – fetch and execute cycles –Timing diagram for instructions- Delay subroutines – Interrupts- Interrupt service Routines</p>	12
2	<p>Introduction to Embedded Systems-Application domain, features and characteristics, Microprocessors and Micro controllers– Choice and suitability for applications</p> <p>Introduction to Arduino UNO(8bit)- Hardware fundamentals of ATmega328Pmicrocontroller based Board. Arduino Architecture, Pin diagram and functions of Pins- Overview of main features such as I/O Ports, Timers, interrupts, PWM, ADC (Introduction only). Introduction to Arduino IDE- Arduino Libraries, Steps for creating an Arduino program- Arduino Sketch Structure and Flow- Setup and loop functions</p>	10
3	<p>Programming in Embedded C. Data types- operators, conditional statements- Loops, Arrays and functions- Built in functions in Arduino - Program to blink an LED and its control., Interfacing LCD, Seven Segment LED, switch Interface, Binary counter Working with LED Controlled by Switch/ Potentiometer, Interfacing with Relays, Buzzer, Working with Basic sensors and actuators using Arduino.</p>	12

4	<p>ARM (Advanced RISC Machines) based Embedded System Design: Classification of Microprocessors based on the word length, architecture and Instruction Set- Reduced Instruction Set Computer (RISC) and Complex Instruction Set Computer (CISC). Features and characteristics</p> <p>Introduction to Arduino due(32bit)- micro controller board (based on the atmel sam3x-- arm cortex- m3 cpu)- Features, General Specifications Overview, General architecture- Features OF Microcontroller, INPUTS,OUTPUTS, Ratings, Functional Overview, Pinout- familiarization of the ports of the board. Programming Basics- Arduino IDE-Use of Timer, Interfacing of ADC and DAC -PWM implementation – Introduction to Arduino Cloud Editor</p>
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Course Assessment Method (CIE: 60 marks, ESE: 40 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	30	12.5	12.5	60

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> • 2 Questions from each module. • Total of 8 Questions, each carrying 3 marks <p style="text-align: center;">(8x2 =16 marks)</p>	<ul style="list-style-type: none"> • Each question carries 9 marks. • Two questions will be given from each module, out of which 1 question should be answered. • Each question can have a maximum of 3 sub divisions. <p style="text-align: center;">(4x6 = 24 marks)</p>	40

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Describe the architecture of 8085 microprocessor and 8085 Assembly language programming.	K2
CO2	Understand the need for interrupts, Subroutines, timing diagram of 8085 microprocessor and interfacing	K2
CO3	Understand and gain the basic idea about the embedded system and selection of processors.	K2
CO4	Able to gain working level knowledge about a Arduino Uno based system architecture and Arduino IDE	K2
CO5	Write Programs using Embedded C and implement an application using Arduino UNO board.	K3
CO6	Understand the RISC Architecture and Apply the knowledge for solving the real life problems using ARM - Arduino DUE board based embedded system.	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	2									
CO2	3	2	3	3	2	1					
CO3	3	2	2	2	2						
CO4	3	2									1
CO5	3	2	3	2	1	1					1
CO6	3	2	3	2	1	1					1

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Fundamentals of <i>Microprocessor</i> and <i>Micro controllers</i>	Ram, B.DHANPAT	Rai Publications (P) Ltd.- New Delhi	7th Edition, 2018

2	Microprocessor, Architecture, Programming and Applications	Ramesh Gaonkar	Penram International Publishing;	Sixth edition, 2014.
3	Arduino Cookbook”	Michael Margolis,	O’Reilly Media, Inc.	1st Edition
4	Microprocessor Theory and Application	Rafiquzzaman	PHI Learning	First Edition

Reference Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Arduino-Based Embedded Systems	Rajesh Singh, Anita Gehlot, Bhupendra Singh, and Sushabhan Choudhury		
2	Arduino for beginners: Essential Skills Every Maker Needs”	John Baichtal	Person Education	
3	Arduino Made Simple	Ashwin Pajankar		
4	Embedded C, Pont	Michael J		
5	Programming Arduino Next Steps: Going Further with Sketches	Simon Monk		
6	Arduino: A Technical Reference by	J.M. Hughes	O’Reilly Media, Inc. ISBN: 9781491934494	
7	Arduino Workshop: A Hands-On Introduction with 65 Projects	John <i>Boxall</i>		
8	Exploring Arduino: Tools and Techniques for Engineering Wizardry	<u>Jeremy Blum</u>		First Edition 2019
Video Links (NPTEL, SWAYAM...)				
Module No.	Link ID			
1	https://onlinecourses.nptel.ac.in/noc20_ee42/preview			
2	https://onlinecourses.nptel.ac.in/noc20_ee42/preview			
3	https://onlinecourses.nptel.ac.in/noc20_ee42/preview https://www.arduino.cc/en/Tutorial/HomePage			
4	https://onlinecourses.nptel.ac.in/noc20_ee42/preview https://docs.arduino.cc/hardware/duemilino/			

SEMESTER S5

ENERGY STORAGE SYSTEMS

Course Code	24SJPEEET521	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	NIL	Course Type	Theory

Course Objectives:

1. To introduce the importance and application of energy storage systems.
2. To familiarize with different energy storage technologies.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Need and role of energy storage systems in power system, General considerations, Energy and power balance in a storage unit, Mathematical model of storage system: modelling of power transformation system (PTS)-Central store (CS) and charge-discharge control system (CDCS), Econometric model of storage system. Thermal energy: General considerations -Storage media Containment-Thermal energy storage in a power plant, Potential energy: Pumped hydro-Compressed Air.	
2	Kinetic energy: Mechanical- Flywheel, Power to Gas: HydrogenSynthetic methane. Electro chemical energy: Batteries-Battery parameters: C-rating- SoC - DoD - Specific Energy- Specific power(numerical examples), Fuel cells, Electrostatic energy (Super Capacitors), Electromagnetic energy (Superconducting Magnetic Energy Storage), Comparative analysis, Environmental impacts of different technologies.	
3	Types of renewable energy sources: Wave - Wind - Tidal - Hydroelectric - Solar thermal technologies and photovoltaic, Storage role in isolated power systems with renewable power sources, Storage role in an integrated power system with grid-connected renewable power sources.	
4	Smart grid, Smart micro grid, Smart house, Mobile storage system: Electric vehicles - Grid to Vehicle (G2V)-Vehicle to Grid (V2G), Management and control hierarchy of storage systems.Aggregating energy storage systems and distributed generation (Virtual Power Plant Energy Management with storage systems), Battery SCADA, Hybrid energy storage systems: configurations and applications.	

Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 3 marks <p align="center">(8x3 =24marks)</p>	<ul style="list-style-type: none"> Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. <p align="center">(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Identify the role of energy storage in power systems.	K3
CO2	Classify and compare various energy storage systems along with their applications.	K3
CO3	Illustrate energy storage technology in renewable energy integration.	K2
CO4	Summarise energy storage technology applications for smart grids.	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

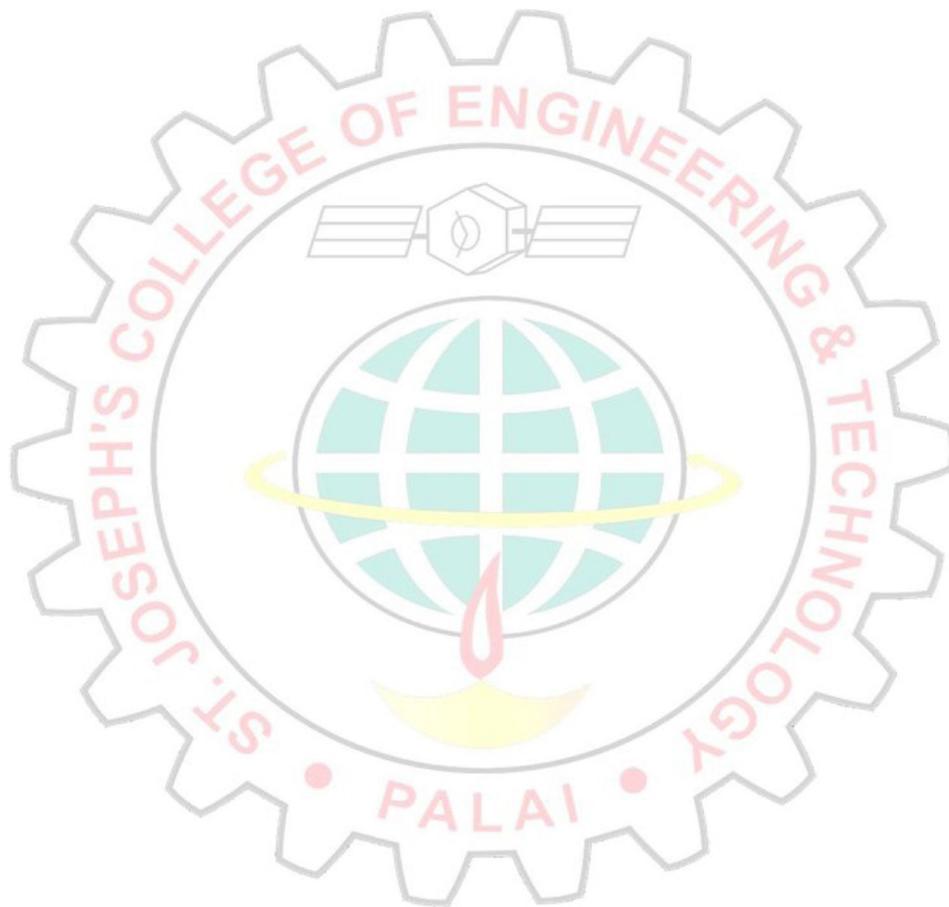
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	1									
CO2	3	1									
CO3	3	1									
CO4	3	1									

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Energy Storage for Power Systems	A.G.Ter Gazarian	The Institution of Engineering and Technology (IET)Publication, UK,	Second Edition, 2011
2	Energy Storage in Power Systems	Francisco Diaz- González, Andreas Sumper, Oriol Gomis-Bellmunt	Wiley Publication	2016

Reference Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Electricity Energy Storage Technology Options: A White Paper Primer on Applications, Costs, and Benefits	D. Rastler	Electric Power Research Institute (USA)	Technical Update, December 2010
2	The Role of Energy Storage with Renewable Electricity Generation	Paul Denholm, Erik Ela, Brendan Kirby and Michael Milligan	National Renewable Energy Laboratory (NREL)	January 2010
3	Electrical energy management of virtual power plants in distribution networks with renewable energy resources and energy storage systems	P. Nezamabadi and G. B. Gharehpetian	IEEE Power Distribution Conference	2011

Video Links (NPTEL, SWAYAM...)	
Module No.	Link ID
1	https://www.youtube.com/watch?v=o6AfpMI_tQ&list=PLLy_2iUCG87AjWoOk0A3y4hpGQVTdtl6G&index=12 (NPTEL lecture IIT Roorkee)
2	https://www.youtube.com/watch?v=yar51GJVqgg (NPTEL lecture IIT Guwahati)
3	https://www.youtube.com/watch?v=frWxC5KL8kE (NPTEL lecture IIT Guwahati)
4	https://www.youtube.com/watch?v=AZIS_MCw8Qc (NPTEL lecture IIT Kanpur)



SEMESTER S5

ELECTRIC VEHICLES

Course Code	24SJPEEET522	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	2:1:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJPCEET303, 24SJPBEET304, 24SJPCEET403	Course Type	Theory

Course Objectives:

1. Familiarise the various characteristics of conventional vehicles and compare them with electric vehicles
2. Analyse the various drive train topologies for electric vehicles
3. Discuss the propulsion unit for electric vehicles
4. Analyse the various energy storage systems and energy management strategies
5. Selection of drive systems and study of various communication protocols for EV

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Conventional Vehicles: Basics of vehicle performance, Vehicle power source characterization, Transmission characteristics (1hr).</p> <p>Introduction to Electric Vehicles: History of electric vehicles, Classification of electric vehicles. Overview of EV challenges. Overview of EV technologies-motor drive technology , energy source technology , battery charging technology , vehicle-to-grid technology(2hr)</p> <p>Vehicle Dynamics & Load Forces:Mathematical models to describe vehicle performance, vehicle load forces: aerodynamic drag,rolling resistance, grading resistance, vehicle acceleration, Calculation of motor power from traction torque, Numerical problems. (4 hrs)</p>	9

	<p>Electric Drive-trains: Basic concept of electric traction, Introduction to various electric drive-train topologies, Power flow control in electric drive-train topologies, Fuel efficiency analysis.(2 hrs)</p>	
2	<p>DC Drives: Motoring using a PM DC Machine - DC motor electric drive using DC-DC converter - Generating/Braking using a PM DC Machine. (3hrs)</p> <p>PMSM Drives: Review of PMSM motor basics – Independent control of orthogonal flux and torque (concept only)- Field Oriented Control (FOC) – Sensored and sensorless control (block diagram only). (4hrs) SRM Drive.</p> <p>Sizing the drive system: Matching the electric machine and the Internal Combustion Engine (ICE) ,Sizing the propulsion motor, Sizing the power electronics-Switch technology Selection ,Ripple capacitor design, Switching frequency and PWM. (2hrs)</p>	9
3	<p>Battery based energy storage systems: Types of battery-battery parameters-units of battery energy storage - capacity rate, - cell voltage - specific energy - cycle life - self-discharge- static battery equivalent circuit model - series-parallel battery pack equivalent circuits.(3hrs)</p> <p>Other storage topologies: Fuel Cell based energy storage systems-Supercapacitors- Flywheel- Hybridization of different energy storage devices. (2 hrs)</p> <p>Sizing considerations of battery -Time and charge/discharge cycles - Lifetime – Beginning of life (BOL) - End of life (EOL) - DOD - Efficiency of Battery Pack - Determination of pack Voltage, range for EV - Determination of Cell/Pack Voltage for a Given Output/Input Power. Battery management system, Numerical problems.(4hrs)</p>	9
4	<p>Overview of Electric Vehicle Battery Chargers–Types of chargers-On-board chargers, Off- board chargers, Wireless charger. Electric Vehicle Supply Equipment (EVSE) - Grid to EVSE to On-board chargers to battery pack power flow block schematic diagrams – V2G concept(3hrs)</p>	9

Types of charging stations - AC Level 1 & 2, DC - Level 3 -Types of Connectors - CHAdeMO, CCS Type1 and 2, GB/T - PIN diagrams and differences (2hrs)	
Autonomous Vehicles: Levels of automation, significance, functional architecture-sensors, actuators, path planning& effects of automation in vehicles (2hrs)	
Vehicle Communication protocols : Need & requirements - Functions of Control Pilot (CP) and Proximity Pilot (PP) pins, Communication Protocols - CAN, LIN, FLEXRAY (Basics only)- Power line communication (PLC) in EV (2 hrs)	

Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> • 2 Questions from each module. • Total of 8 Questions, each carrying 3 marks <p style="text-align: center;">(8x3 =24marks)</p>	<ul style="list-style-type: none"> • Each question carries 9 marks. • Two questions will be given from each module, out of which 1 question should be answered. • Each question can have a maximum of 3 sub divisions. <p style="text-align: center;">(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Familiarise the performance of conventional vehicles and electric vehicles	K2
CO2	Analyse the various drive train topologies for electric vehicles	K3
CO3	Discuss the propulsion unit for electric vehicles and selection of drive systems	K3
CO4	Analyse the various energy storage systems and energy management strategies	K3
CO5	Study of chargers, charging stations and various communication protocols for EV	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

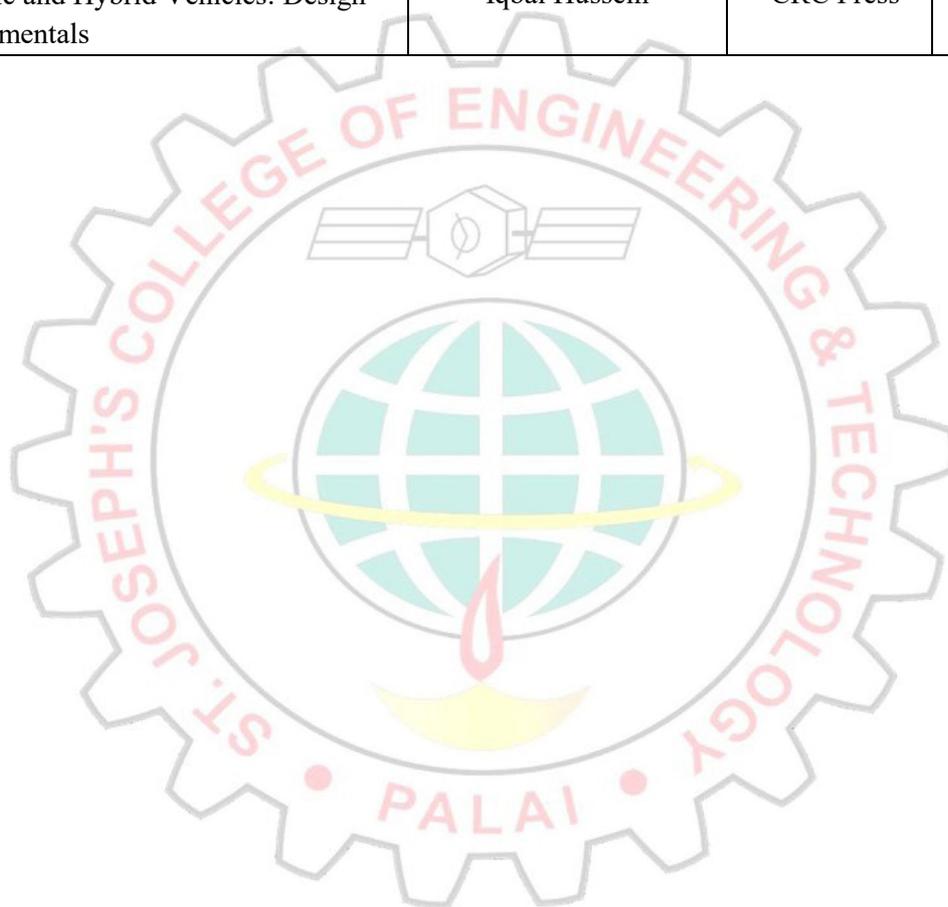
CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3										3
CO2	3		2								3
CO3	3		2								3
CO4	3		2								3
CO5	3										3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Electric Vehicles Machines and Drives- Design, Analysis and Application	K. T. Chau	John Wiley	2015
2	Propulsion Systems for Hybrid Vehicles	John M. Miller	The Institution of Engineering and Technology, London, United Kingdom	2010
3	Hybrid Electric Vehicles – Principles and applications with practical perspectives	Chris Mi, M A Masrur, D W Gao	Wiley	2011

Reference Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Modern Electric, Hybrid and Fuel Cell Vehicles: Fundamentals, Theory and Design	Mehrdad Ehsani, Yimi Gao, Sebastian E. Gay	CRC Press	
2	Permanent Magnet Synchronous and Brushless DC Motors Drives	R. Krishnan	CRC Press	
3	Electric and Hybrid Vehicles: Design Fundamentals	Iqbal Hussein	CRC Press	2003



SEMESTER S5
DIGITAL SYSTEM DESIGN

Course Code	24SJPEEET523	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)		Course Type	PE -Theory

Course Objectives:

- To acquire knowledge about Asynchronous and clocked Synchronous sequential circuit design.
- To detect the faults and hazards in digital circuit design
- To design and implement digital circuits using VHDL.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Clocked Synchronous Networks, Analysis of Clocked Synchronous Sequential Networks (CSSN), Modelling of CSSN, State assignment and reduction, Design of CSSN.	10
2	ASM Chart and its realization. Asynchronous Sequential Circuits, Analysis of Asynchronous Sequential Circuits (ASC), Flow table reduction, Races in ASC, State assignment problem and the transition table.	10
3	Hazards – static and dynamic hazards in combinational networks, Essential Hazards, Design of Hazard free circuits, Data synchronizers, Mixed operating mode asynchronous circuits, Practical issues- clock skew and jitter, Synchronous and asynchronous inputs. Faults: Fault table method – path sensitization method – Boolean difference method.	8
4	VLSI Design flow: Design entry: Schematic, Data types and objects, different modelling styles in VHDL - Dataflow, Behavioural and Structural Modelling. VHDL constructs and codes for combinational and sequential circuits.	8

Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE) :

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> • 2 Questions from each module. • Total of 8 Questions, each carrying 3 marks (8x3 =24 marks) 	<ul style="list-style-type: none"> • Each question carries 9 marks. • Two questions will be given from each module, out of which 1 question should be answered. • Each question can have a maximum of 3 sub divisions. (4x9 = 36 marks) 	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome	Bloom's Knowledge Level (KL)
CO1 Analyze asynchronous and clocked synchronous sequential circuits	K3
CO2 Design hazard-free digital circuits	K3
CO3 Identify faults in digital circuits	K3
CO4 Apply VHDL programming in digital system design	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	3	3	3							3
CO2	3	2	2	2							3
CO3	3	3	2		2						3
CO4	3	3	3	3	3						3

Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Digital Principles & Design	Donald G Givone	Tata McGraw Hill	1/e 2002

2	Digital Design with an introduction to HDL, VHDL and Verilog	M.Morris Mano and Michel.D.Ciletti	Pearson education	6/e, 2018
3	Digital Design	John F Wakerly	Pearson Education	4/e 2008
4	Digital Logic Applications and Design	John M Yarbrough	Cengage India	1/e 2006

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Digital Systems Testing and Testable Design	Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman	John Wiley & Sons Inc	
2	Logic Design Theory	N. N. Biswas	PHI	
3	Introduction to Digital Design Using Digilent FPGA Boards	Richard E. Haskell, Darrin M. Hanna	LBE Books- LLC	
4	Digital Circuits and Logic Design	Samuel C. Lee	PHI	
5	Digital System Design Using VHDL	R. Anand	Khanna Book Publishing Company	
6	Digital System Design using VHDL	Charles Roth	TMH	

SEMESTER S5

SOFTWARE ENGINEERING

Course Code	24SJPEEET524	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

1. Provides fundamental knowledge in the Software Development Process which covers Software Development, and Project Management concepts.
2. Enables the learners to apply state of the art industry practices in Software development.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Introduction to Software Engineering: Introduction to Software Engineering - Professional software development, Software engineering ethics. Software process models - The waterfall model, Incremental development. Process activities - Software specification, Software design and implementation, Software validation, Software evolution. Coping with change - Prototyping, Incremental delivery, Boehm's Spiral Model. Agile software development - Agile methods, agile manifesto - values and principles. Agile development techniques, Agile Project Management. Case studies : An insulin pump control system. Mentcare - a patient information system for mental health care.	8
2	Requirement Analysis and Design: Functional and non-functional requirements, Requirements engineering processes. Requirements elicitation, Requirements validation, Requirements change, Traceability Matrix. Developing use cases, Software Requirements Specification Template, Personas, Scenarios, User stories, Feature identification. Design concepts - Design within the context of software engineering, Design Process, Design concepts, Design Model. Architectural Design - Software Architecture, Architectural Styles, Architectural considerations, Architectural Design Component level design - What is a component?, Designing Class-Based Components, Conducting Component level design, Component level design for web-apps.	10

3	<p>Implementation and Testing</p> <p>Object-oriented design using the UML, Design patterns, Implementation issues, Open-source development - Open-source licensing - GPL, LGPL, BSD. Review Techniques - Cost impact of Software Defects, Code review and statistical analysis. Informal Review, Formal Technical Reviews, Postmortem evaluations. Software testing strategies - Unit Testing, Integration Testing, Validation testing, System testing, Debugging, White box testing, Path testing, Control Structure testing, Black box testing, Testing Documentation and Help facilities. Test automation, Test-driven development, Security testing. Overview of DevOps and Code Management - Code management, DevOps automation, CI/CD/CD. Software Evolution - Evolution processes, Software maintenance.</p>	12
4	<p>Software Project Management: Software Project Management - Risk management, Managing people, Teamwork. Project Planning, Software pricing, Plan-driven development, Project scheduling, Agile planning. Estimation techniques, COCOMO cost modeling. Configuration management, Version management, System building, Change management, Release management, Agile software management - SCRUM framework. Kanban methodology and lean approaches.</p>	8

Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE) :

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> • 2 Questions from each module. • Total of 8 Questions, each carrying 3 marks (8x3 =24 marks) 	<ul style="list-style-type: none"> • Each question carries 9 marks. • Two questions will be given from each module, out of which 1 question should be answered. • Each question can have a maximum of 3 sub divisions. (4x9 = 36 marks) 	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Interpret software process models and core activities, including handling changes with techniques like prototyping and incremental delivery.	K2
CO2	Describe agile methods, including the Agile Manifesto and agile project management practices.	K2
CO3	Prepare Software Requirement Specification and Software Design for a given problem	K3
CO4	Interpret object-oriented design principles, design patterns, software testing methods (including unit testing, integration testing, and test automation), and open-source licensing models (such as GPL, LGPL, and BSD).	K2
CO5	Describe software review techniques, DevOps practices and code management principles, and software evolution processes and maintenance strategies.	K2
CO6	Make use of software project management concepts while planning, estimation, scheduling, tracking and change management of a project, with proper application of SCRUM, Kanban and Lean frameworks.	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	3					3				3
CO2	3	3									3
CO3	3	3	3						3		3
CO4	3	3	3								3
CO5	3	3						3			3
CO6	3	3						3		3	3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Software Engineering	Ian Sommerville	Pearson Education	Tenth edition, 2015
2	Software Engineering : A practitioner's approach	Roger S. Pressman	McGraw Hill publication	Eighth edition, 2014
3	Engineering Software Products: An Introduction to Modern Software Engineering	Ian Sommerville	Pearson Education	First Edition, 2020

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Kanban	David J. Anderson	Blue Hole Press	2010
2	Agile Management for Software Engineering	David J. Anderson	Pearson	2003
3	Software Project Management : A unified framework	Walker Royce	Pearson Education	1998
4	Implementing Lean Software Development: From Concept to Cash	Mary Poppendieck	Addison-Wesley Signature Series	2006

Video Links (NPTEL, SWAYAM...)

Module No.	Link ID
1	https://nptel.ac.in/courses/106105182
2	https://nptel.ac.in/courses/106105182
3	https://nptel.ac.in/courses/106105182
4	https://nptel.ac.in/courses/106105218

SEMESTER S5

DATA STRUCTURES

Course Code	24SJPEEET526	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	UCEST105, GBEST204	Course Type	Theory

Course Objectives:

1. To impart a thorough understanding of linear data structures such as arrays, stacks, queues and linked lists and their applications.
2. To impart a thorough understanding of non-linear data structures such as trees, graphs and their applications.
3. To impart familiarity with various sorting, searching and hashing techniques and their performance comparison.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Basic Concepts of Data Structures: Algorithms, Performance Analysis, Space Complexity, Time Complexity, Asymptotic Notations Arrays: Linear Search and Binary Search, Stacks, Queues-Circular Queues, Priority Queues, Double Ended Queues, Evaluation of Expressions	11
2	Linked List: Self-Referential Structures, Dynamic Memory Allocation, Singly Linked List- Operations on Linked List. Doubly Linked List, Circular Linked List, Stacks and Queues using Linked List, Polynomial representation using Linked List	11
3	Trees and Graphs: Trees, Binary Trees-Tree Operations, Binary Tree Representation, Tree Traversals, Binary Search Trees- Binary Search Tree Operations Graphs, Representation of Graphs, Depth First Search and Breadth First Search on Graphs, Applications of Graphs	11

4	Sorting and Hashing: Sorting Techniques - Selection Sort, Insertion Sort, Quick Sort, Merge Sort and Heap Sort Hashing- Hashing Techniques, Collision Resolution, Overflow handling, Hashing functions - Mid square, Division, Folding, Digit Analysis	11
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Course Assessment Method (CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> • 2 Questions from each module. • Total of 8 Questions, each carrying 3 marks (8x3 =24 marks) 	<ul style="list-style-type: none"> • Each question carries 9 marks. • Two questions will be given from each module, out of which 1 question should be answered. • Each question can have a maximum of 3 sub divisions. (4x9 = 36 marks) 	60

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Compare performance of algorithms using asymptotic notations	K2
CO2	Solve real world problems efficiently using appropriate data structures like arrays, linked list, stacks and queues.	K3
CO3	Make use of nonlinear data structures like trees and graphs to design algorithms for various applications.	K3

CO4	Apply and compare various techniques for searching and sorting.	K3
CO5	Apply appropriate hash function to store and access a given dataset	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	2	2	2	1	-	1	-	-	-	-	-
CO2	3	2	3	1	-	1	-	-	-	-	-
CO3	3	2	3	1	-	1	-	-	-	-	-
CO4	2	2	3	1	-	1	-	-	-	-	-
CO5	3	2	2	1	-	1	-	-	-	-	-

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Fundamentals of Data Structures in C	Ellis Horowitz, Sartaj Sahni and Susan Anderson-Freed	Universities Press	
2	Classic Data Structures	Samanta D	Prentice Hall India	2/e, 2009

Reference Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Data Structures: A Pseudocode Approach with C	Richard F. Gilberg, Behrouz A. Forouzan	Cengage Learning	2/e, 2005
2	Data Structures and Algorithms	Aho A. V., J. E. Hopcroft and J. D. Ullman	Pearson Publication	1983
3	Introduction to Data Structures with Applications	Tremblay J. P. and P. G. Sorenson	Tata McGraw Hill	1995

4	Advanced Data Structures	Peter Brass	Cambridge University Press	2008
5	Theory and Problems of Data Structures	Lipschuts S.	Schaum's Series	1986

Video Links (NPTEL, SWAYAM...)	
Module No.	Link ID
1	https://nptel.ac.in/courses/106102064 https://youtu.be/zWg7U0OEAoE https://youtu.be/g1USSZVWDsY https://youtu.be/PGWZUgzDMYI
2	https://nptel.ac.in/courses/106102064 https://youtu.be/PGWZUgzDMYI
3	https://nptel.ac.in/courses/106102064 https://youtu.be/tORLeHHtazM https://youtu.be/eWeqqVpgNPg https://youtu.be/9zpSs845wf8
4	https://youtu.be/KW0UvOW0XIo https://youtu.be/gtWw_8VvHjk

SEMESTER S5

INTRODUCTION TO MACHINE LEARNING

Course Code	24SJPEEET527	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJGYEST305	Course Type	PE - Theory

Course Objectives:

1. To equip students with overall understanding of the underlying mathematical and algorithmic concepts of machine learning.
2. To understand and perform various data pre-processing and visualization in using various python libraries
3. To implement various machine learning algorithms using python.
4. To evaluate and optimize machine learning models for diverse applications

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Mathematics for Machine Learning. - Association of two variables - Discrete variables, Ordinal and Continuous variable, Probability calculus - Summary Statistics, probability distributions, Inductive statistics - Point estimation, Interval estimation, Hypothesis Testing - Basic definitions, t-test, F-test, ANOVA	9
2	Introduction to machine learning algorithms - supervised vs. unsupervised learning, regression and classification, linear discriminant analysis, decision trees, random forests, and bagging. Unsupervised - Principal Component Analysis, clustering algorithms, SVMs, re-sampling methods: cross-validation and bootstrapping	9
3	Introduction to python for ML - essential python libraries and ML functions (NumPy, pandas, Matplotlib, SciKit-Learn), working with data sets – data	

	cleaning and pre-processing functions, Data visualization- bar, scatter, histogram, heatmaps.	
4	ML algorithm implementation with python - Linear Regression Simple and multiple linear regression, Model evaluation metrics: MSE, RMSE, R^2 , Classification Algorithms - Logistic regression, k-Nearest Neighbours (kNN), Decision Trees, Model evaluation metrics: accuracy, precision, recall, F1-score, Support Vector Machines (SVM), Ensemble methods (Random Forest, Gradient Boosting), Clustering Algorithms -K-means clustering, Hierarchical clustering.	9

**Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)**

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro project	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE) :

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 3 marks <p align="center">(8x3 =24marks)</p>	<ul style="list-style-type: none"> Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. <p align="center">(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Understand the relationships between different types of variables (discrete, ordinal, and continuous) using summary statistics and probability distributions, and perform hypothesis testing including tests and F-tests.	K2
CO2	Apply different supervised and unsupervised machine learning algorithms (such as regression, classification, clustering, and dimensionality reduction) and their appropriate applications in solving real-world problems.	K3
CO3	Apply essential Python libraries (NumPy, Pandas, Matplotlib) to clean, pre-process, and visualize data sets, preparing data for machine learning applications.	K3
CO4	Implement machine learning algorithms (such as linear regression, logistic regression, k-Nearest Neighbours, Decision Trees, SVM, Random Forest, Gradient Boosting, and clustering) in Python and evaluate their performance using relevant metrics.	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	2									2
CO2	3	3	2	2							2
CO3	3	3	2								2
CO4	3	3	2	3							2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Mathematics for Machine Learning	Marc Peter Deisenroth, A. Aldo Faisal, Cheng Soon Ong	Cambridge University Press	1st Edition, 2020
2	Pattern Recognition and Machine Learning	Christopher M. Bishop	Springer	1st Edition, 2006
3	Python Data Science Handbook: Essential Tools for Working with Data	Jake Vander Plas	O'Reilly Media	1st Edition, 2016

4	Hands-On Machine Learning with Scikit-Learn, Keras, and TensorFlow	Aurélien Géron	O'Reilly Media	2nd Edition, 2019
5	Introduction to Machine Learning with Python: A Guide for Data Scientists	Andreas C. Müller, Sarah Guido	O'Reilly Media	1st Edition, 2016

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	The Elements of Statistical Learning: Data Mining, Inference and Prediction	Trevor Hastie, Robert Tibshirani, Jerome Friedman	Springer	2nd Edition, 2009
2	Data Mining: Concepts and Techniques	Jiawei Han, Micheline Kamber, Jian Pei	Morgan Kaufmann	3rd Edition, 2011
3	Python Machine Learning: Machine Learning and Deep Learning with Python, scikitlearn, and Tensor Flow 2	Sebastian Raschka, Vahid Mirjalili	Packt Publishing	3rd Edition, 2019
4	Applied Predictive Modelling	Max Kuhn, Kjell Johnson	Springer	1st Edition, 2013

Video Links (NPTEL, SWAYAM...)

Module No.	Link ID
Module - I	https://onlinecourses.nptel.ac.in/noc23_cs18/preview
Module - II	https://onlinecourses.nptel.ac.in/noc23_cs18/preview
Module - III	https://nptel.ac.in/courses/106105152
Module - IV	https://nptel.ac.in/courses/106105152

SEMESTER S5
COMPUTER NETWORK SYSTEMS

Course Code	24SJPEEET528	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	Nil	Course Type	Theory

Course Objectives:

1. To familiarize various types of layers in OSI model.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Introduction – Uses of computer networks, Network hardware, Network software - Protocol hierarchies – Design issues for the layers – Connection oriented versus connectionless service. Reference models – The OSI reference model, The TCP/IP reference model, Comparison of OSI and TCP/IP reference models.</p> <p>Physical Layer –Transmission media overview – Twisted pair and fiber optics. Performance indicators – Bandwidth, Throughput, Latency, Bandwidth–Delay product.</p>	8
2	<p>Data link layer - Data link layer design issues, Error detection and correction, Sliding window protocols.</p> <p>Medium Access Control (MAC) sublayer, Channel allocation problem, Multiple access protocols – CSMA, Collision free protocols.</p> <p>Ethernet – Switched Ethernet, fast Ethernet and gigabit Ethernet.</p> <p>Wireless LANs - 802.11 – Architecture and protocol stack, Use of Bridges, Repeaters, Hubs, Switches, Routers and Gateways.</p>	8

3	<p>Network layer design issues. Routing algorithms - The Optimality Principle, Shortest path routing, Flooding, Distance Vector Routing, Link State Routing, Routing for mobile hosts.</p> <p>Congestion control algorithms – Approaches to congestion control (Details not required). Quality of Service (QoS) - Requirements, Techniques for achieving good QoS – Traffic shaping, Packet scheduling.</p> <p>IPv4 protocol, IP addresses, IPv6, Internet Control Protocols - Internet Control Message Protocol (ICMP), Address Resolution Protocol (ARP), Dynamic Host Configuration Protocol (DHCP). Open Shortest Path First (OSPF) Protocol, Border Gateway Protocol (BGP), Internet multicasting.</p>	12
4	<p>Transport service – Services provided to the upper layers, Transport service primitives. User Datagram Protocol (UDP) – Introduction, Remote procedure call. ELECTRICAL AND ELECTRONICS Transmission Control Protocol (TCP) – Introduction, TCP service model, TCP protocol, TCP segment header, Connection establishment & release. Application Layer – Domain Name System (DNS) – overview of DNS name space and Name servers, Electronic mail – Architecture and services- SMTP – IMAP - POP3, World Wide Web (WWW) - Architectural overview, HTTP, File Transfer Protocol (FTP).</p>	8

**Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)**

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> □ 2 Questions from each module. □ Total of 8 Questions, each carrying 3 marks <p>(8x3 =24marks)</p>	<ul style="list-style-type: none"> □ Each question carries 9 marks. □ Two questions will be given from each module, out of which 1 question should be answered. □ Each question can have a maximum of 3 sub divisions. <p>(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

	Course Outcome	Bloom's Knowledge Level (KL)
CO1	Explain the computer networks, layered architecture, protocols and physical media used for setting up a network.	K2
CO2	Identify the role of Data link layer, role of the MAC sub layer and networking devices in Ethernets and wireless LANs	K2
CO3	Explain routing algorithms and congestion control algorithms and ways to achieve good quality of service, IP address classes, ICMP protocols and other external routing protocols.	K2
CO4	Explain the services provided by the transport layer and application layer.	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	2										2
CO2	2	1									2
CO3	2	1									2
CO4	2										2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Computer Networks	Andrew S. Tanenbaum	Pearson Education India.	5 th edition
2	Data Communication and Networking	Behrouz A Forouzan	McGraw Hill Education	5 th edition

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Computer Networks – A Systems Approach	Larry L Peterson and Bruce S Dave	Morgan Kaufmann	5 th edition
2	Computer Networking and the Internet	Fred Halsall		5 th edition
3	Computer Networking: A Top-Down Approach	James F. Kurose, Keith W. Ross		6 th edition
4	An Engineering Approach to Computer Networks	Keshav	Addison Wesley	1998
5	TCP/IP Illustrated Volume 1,	W. Richard Stevens.	Addison-Wesley	2005
6	Computer Networking with Internet Protocols.,	William Stallings	Prentice-Hall	2004

Video Links (NPTEL, SWAYAM...)

Module No.	Link ID
1	https://nptel.ac.in/courses/106105183 https://nptel.ac.in/courses/106106091
2	https://nptel.ac.in/courses/106105183 https://nptel.ac.in/courses/106106091
3	https://nptel.ac.in/courses/106105183 https://nptel.ac.in/courses/106106091
4	https://nptel.ac.in/courses/106105183 https://nptel.ac.in/courses/106106091

SEMESTER: S5
AC MACHINES LAB

Course Code	PCEEL507	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	0:0:3:0	ESE Marks	50
Credits	2	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJPCEET402	Course Type	Lab

Course Objectives:

- Provide practical experience in operation and testing of synchronous and induction machines

Expt. No.	Experiments
PART A – INDUCTION MACHINES	
1	<p>Load test on a 3-phase squirrel-cage induction motor (CO1)</p> <p>Objectives:</p> <p>a) Start the motor using star-delta starter / auto-transformer starter b) Determine the performance characteristics</p>
2	<p>Load test on a 3-phase slip-ring induction motor (CO1)</p> <p>Objectives:</p> <p>a) Start the motor using rotor resistance starter / auto-transformer starter</p> <p>b) Determine the performance characteristics</p>
3	<p>No-load and blocked-rotor tests on a 3-phase squirrel-cage induction motor (CO1)</p> <p>Objectives:</p> <p>a) Determine the equivalent circuit parameters</p> <p>b) Predetermine its performance at rated speed from equivalent circuit</p> <p>c) Predetermine its performance on full-load from circle diagram</p>
4	<p>No-load and blocked-rotor tests on 3-phase pole-changing induction motor (CO1)</p> <p>Objectives:</p> <p>a) Conduct no-load and blocked-rotor tests in two different pole configurations (example 4 pole and 8 pole)</p> <p>b) Predetermine its performance on full-load from circle diagrams in both cases</p>

	OR
	<p>Load test on 3-phase pole-changing induction motor (CO1)</p> <p>Objectives:</p> <ol style="list-style-type: none"> Conduct load tests in two different pole configurations (example 4 pole and 8 pole) Determine the performance characteristics
5	<p>Variation of starting torque with rotor resistance in 3-phase slip-ring induction motor (CO1)</p> <p>Objectives:</p> <ol style="list-style-type: none"> Plot the variation of starting torque against rotor resistance Determine the external rotor resistance for which maximum starting torque is obtained
6	<p>Brake test on 1-phase induction motor (CO6)</p> <p>Objective:</p> <ol style="list-style-type: none"> Plot the performance characteristics
7	<p>No-load and blocked-rotor tests on 1-phase induction motor (CO6)</p> <p>Objectives:</p> <ol style="list-style-type: none"> Determine the equivalent circuit Predetermine the efficiency on full-load from equivalent circuit
8	<p>3-phase induction machine working as motor and generator (CO2)</p> <p>Objective:</p> <p>Determine the performance of 3-phase induction machine working as motor and generator</p>
9	<p>Speed control of 3-phase squirrel-cage induction motor using V/f technique (CO3)</p> <p>Objective:</p> <p>Perform the speed control of a 3-phase squirrel-cage induction motor by varying supply voltage and frequency</p>
PART B –SYNCHRONOUS MACHINES	
10	<p>Voltage regulation of 3-phase synchronous generator by EMF and MMF method (CO4)</p> <p>Objectives:</p> <ol style="list-style-type: none"> Conduct OC and SC tests. Predetermine the full-load voltage regulation at different power factors.

11	<p>Voltage regulation of 3-phase synchronous generator by direct loading (CO4)</p> <p>Objectives:</p> <ol style="list-style-type: none"> Determine the voltage regulation at full-load or half full-load at any power factor. Compare the voltage regulation with emf method.
12	<p>Voltage regulation of 3-phase synchronous generator by Potier method (CO4)</p> <p>Objectives:</p> <ol style="list-style-type: none"> Conduct OC, SC and ZPFC tests. Predetermine the full-load voltage regulation at different power factors.
13	<p>V curves and Inverted V curves of synchronous machines (CO5)</p> <p>Objectives:</p> <ol style="list-style-type: none"> Synchronise the 3-phase alternator using dark lamp or bright lamp method Plot the V curves and inverted V curves of synchronous motor on no-load and half/full load. Plot the V curves and inverted V curves of synchronous generator on half/full load.
14	<p>Slip test on 3-phase salient-pole synchronous machines (CO4)</p> <p>Objectives:</p> <ol style="list-style-type: none"> Determine direct-axis and quadrature-axis synchronous reactances Predetermine the full-load voltage regulation at different power factors Predetermine the excitation and reluctance power with 120% excitation voltage and hence plot the power angle characteristics
<p>NOTE: A minimum of TWELVE experiments are mandatory out of the fourteen listed.</p>	

Course Assessment Method
(CIE: 50 marks, ESE: 50 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Preparation/Pre-Lab Work experiments, Viva and Timely completion of Lab Reports / Record (Continuous Assessment)	Internal Examination	Total
5	25	20	50

End Semester Examination Marks (ESE):

Procedure/ Preparatory work/Design/ Algorithm	Conduct of experiment/ Execution of work/ troubleshooting/ Programming	Result with valid inference/ Quality of Output	Viva voce	Record	Total
10	15	10	10	5	50

- **Submission of Record:** Students shall be allowed for the end semester examination only upon submitting the duly certified record.
- **Endorsement by External Examiner:** The external examiner shall endorse the record

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Analyze the performance of 3-phase squirrel cage and slip ring induction motor at different loads.	K3
CO2	Analyze the performance of line excited induction machine working in motoring and generating modes	K3
CO3	Apply V/f control techniques for the speed control of 3-phase induction motors	K3
CO4	Determine the voltage regulation of 3-phase cylindrical rotor type and salient pole type synchronous generators	K3
CO5	Construct V and inverter V curves of synchronous machines at constant load.	K3
CO6	Compute the efficiency of single-phase induction motor at a specified load.	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO- PO Mapping (Mapping of Course Outcomes with Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	2		2				3	2		3
CO2	3	2		2				3	2		3
CO3	3	2		2				3	2		3
CO4	3	2		2				3	2		3
CO5	3	2		2				3	2		3
CO6	3	2		2				3	2		3

1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Electrical Machinery	P.S. Bimbhra	Khanna Publishers	7 th edition 2021
2	Electric Machines	D P Kothari & I J Nagrath	Tata McGraw Hill	5 th edition 2017

Continuous Assessment (25 Marks)

1. Conduct of Experiments (10 Marks)

- Procedure and Execution: Adherence to correct procedures, accurate execution of experiments, and following safety protocols.
- Skill Proficiency: Proficiency in handling equipment, accuracy in observations, and troubleshooting skills during the experiments.
- Teamwork: Collaboration and participation in group experiments.

2. Lab Reports and Record Keeping (5 Marks)

- Quality of Reports: Clarity, completeness and accuracy of lab reports. Proper documentation of experiments, data analysis and conclusions.
- Timely Submission: Adhering to deadlines for submitting lab reports/rough record and maintaining a well-organized fair record.

3. Viva Voce (10 Marks)

- Ability to explain the experiment, results and underlying principles based on their theoretical knowledge.

Final Marks Averaging: *The final marks for conduct of experiments, viva, and record are the average of experiments conducted in the lab.*

Evaluation Pattern for End Semester Examination (50 Marks)

1. Procedure/Preliminary Work/Design/Algorithm (10 Marks)

- Procedure Understanding and Description: Clarity in explaining the procedure and understanding each step involved.
- Preliminary Work and Planning: Thoroughness in planning and organizing materials/equipment.
- Algorithm Development: Correctness and efficiency of the algorithm related to the experiment.
- Creativity and logic in algorithm or experimental design.

2. Conduct of Experiment/Execution of Work/Programming (15 Marks)

- Setup and Execution: Proper setup and accurate execution of the experiment or programming task.

3. Result with Valid Inference/Quality of Output (10 Marks)

- Accuracy of Results: Precision and correctness of the obtained results.
- Analysis and Interpretation: Validity of inferences drawn from the experiment or quality of program output.

4. Viva Voce (10 Marks)

- Ability to explain the experiment, procedure results and answer related questions
- Proficiency in answering questions related to theoretical and practical aspects of the subject.

5. Record (5 Marks)

- Completeness, clarity, and accuracy of the lab record submitted

SEMESTER S5

MICROPROCESSORS AND EMBEDDED SYSTEMS LAB

Course Code	24SJPCeel508	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	0:0:2:0	ESE Marks	50
Credits	2	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Lab

Course Objectives:

1. Achieve proficiency in 8051 microcontroller assembly language and embedded C programming.
2. Acquire practical experience with Arduino.

Expt. No.	Experiments
1	ALP programming for (a) Data transfer: Block data movement, exchanging data, sorting, finding largest element in an array. (b) Arithmetic operations: Addition, Subtraction, Multiplication and Division. Comparing square and cube of 16 bit numbers.
2	ALP programming for the implementation of counters: Hex up and down counters, BCD up/down counters.
3	(a) ALP programming for implementing Boolean and logical instructions: bit manipulation. (b) ALP programming for implementing conditional call and return instructions: Toggle the bits of port 1 by sending the values of 55H and AAH continuously, Factorial of a number.
4	ALP program for Generation of delay.
5	C program for stepper motor control.

6	C program for DC motor direction and speed control using PWM.
7	C program for alphanumerical LCD panel/keyboard interface.
8	C program for ADC interfacing.
9	Demo experiment using 8051 Microcontroller programming. ALP programming for implementation code conversion- BCD to ASCII , ASCII to BCD, ASCII to Decimal , Decimal to ASCII, Hexadecimal to Decimal and Decimal to Hexadecimal
10	a)Familiarization of Aurdino IDE. b)LED blinking with different ON/OFF delay timings with (i) inbuilt LED (ii) externally interfaced LED.
11	Arduino based voltage measurement of 12 V solar PV module /12 V battery and displaying the measured value using 12C LCD display..
12	Demo experiments on Arduino / Raspberry Pi to upload /retrieve temperature and humidity data to thing speak cloud.
13	Interfacing of the relay with Arduino.

**Course Assessment Method
(CIE: 50 marks, ESE: 50 marks)**

Continuous Internal Evaluation Marks (CIE):

Attendance	Preparation/Pre-Lab Work experiments, Viva and Timely completion of Lab Reports / Record (Continuous Assessment)	Internal Examination	Total
5	25	20	50

End Semester Examination Marks (ESE):

Procedure/ Preparatory work/Design/ Algorithm	Conduct of experiment/ Execution of work/ troubleshooting/ Programming	Result with valid inference/ Quality of Output	Viva voce	Record	Total
10	15	10	10	5	50

- **Submission of Record:** Students shall be allowed for the end semester examination only upon submitting the duly certified record.
- **Endorsement by External Examiner:** The external examiner shall endorse the record

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Develop and execute ALP programs for solving arithmetic and logical problems using microcontroller	K3
CO2	Develop embedded C programming using instruction sets of 8051	K3
CO3	Examine circuits for interfacing processor with various peripheral devices	K3
CO4	Design a microcontroller based system with the help of various interfacing devices	K4
CO5	Design an Arduino based system with the help of various interfacing devices	K4

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO- PO Mapping (Mapping of Course Outcomes with Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	3	3	3	3						3
CO2	3	3	3	3	3						3
CO3	3	3	3	3	3						3
CO4	3	3	3	3	3						3
CO5	3	3	3	3	3						3

1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	The 8051 microcontroller	Kenneth Ayala	Cengage Learning	The 8051 microcontroller
2	Microprocessors and Microcontrollers	R. LylaB.Das	Pearson Education	Microprocessors and Microcontrollers

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	The 8051 Microcontroller	I. ScottMacKenzie,Raphael C.- W.Phan		
2	The 8051 microcontroller and embedded systems	Muhammad Ali Mazidi	Pearson Education	

Continuous Assessment (25 Marks)**1. Preparation and Pre-Lab Work (7 Marks)**

- Pre-Lab Assignments: Assessment of pre-lab assignments or quizzes that test understanding of the upcoming experiment.
- Understanding of Theory: Evaluation based on students' preparation and understanding of the theoretical background related to the experiments.

2. Conduct of Experiments (7 Marks)

- Procedure and Execution: Adherence to correct procedures, accurate execution of experiments, and following safety protocols.
- Skill Proficiency: Proficiency in handling equipment, accuracy in observations, and troubleshooting skills during the experiments.
- Teamwork: Collaboration and participation in group experiments.

3. Lab Reports and Record Keeping (6 Marks)

- Quality of Reports: Clarity, completeness and accuracy of lab reports. Proper documentation of experiments, data analysis and conclusions.
- Timely Submission: Adhering to deadlines for submitting lab reports/rough record and

maintaining a well-organized fair record.

4. Viva Voce (5 Marks)

- Oral Examination: Ability to explain the experiment, results and underlying principles during a viva voce session.

Final Marks Averaging: The final marks for preparation, conduct of experiments, viva, and record are the average of all the specified experiments in the syllabus.

Evaluation Pattern for End Semester Examination (50 Marks)

1. Procedure/Preliminary Work/Design/Algorithm (10 Marks)

- Procedure Understanding and Description: Clarity in explaining the procedure and understanding each step involved.
- Preliminary Work and Planning: Thoroughness in planning and organizing materials/equipment.
- Algorithm Development: Correctness and efficiency of the algorithm related to the experiment.
- Creativity and logic in algorithm or experimental design.

2. Conduct of Experiment/Execution of Work/Programming (15 Marks)

- Setup and Execution: Proper setup and accurate execution of the experiment or programming task.

3. Result with Valid Inference/Quality of Output (10 Marks)

- Accuracy of Results: Precision and correctness of the obtained results.
- Analysis and Interpretation: Validity of inferences drawn from the experiment or quality of program output.

4. Viva Voce (10 Marks)

- Ability to explain the experiment, procedure results and answer related questions
- Proficiency in answering questions related to theoretical and practical aspects of the subject.

5. Record (5 Marks)

- Completeness, clarity, and accuracy of the lab record submitted

Programme Outcomes (POs)

PO1: Engineering Knowledge: Apply knowledge of mathematics, natural science, computing, engineering fundamentals and an engineering specialization as specified in WK1 to WK4 respectively to develop to the solution of complex engineering problems.

PO2: Problem Analysis: Identify, formulate, review research literature and analyse complex engineering problems reaching substantiated conclusions with consideration for sustainable development. (WK1 to WK4)

PO3: Design/Development of Solutions: Design creative solutions for complex engineering problems and design/develop systems/components/processes to meet identified needs with consideration for the public health and safety, whole-life cost, net zero carbon, culture, society and environment as required. (WK5)

PO4: Conduct Investigations of Complex Problems: Conduct investigations of complex engineering problems using research-based knowledge including design of experiments, modelling, analysis & interpretation of data to provide valid conclusions. (WK8).

PO5: Engineering Tool Usage: Create, select and apply appropriate techniques, resources and modern engineering & IT tools, including prediction and modelling recognizing their limitations to solve complex engineering problems. (WK2 and WK6)

PO6: The Engineer and The World: Analyse and evaluate societal and environmental aspects while solving complex engineering problems for its impact on sustainability with reference to economy, health, safety, legal framework, culture and environment. (WK1, WK5, and WK7).

PO7: Ethics: Apply ethical principles and commit to professional ethics, human values, diversity and inclusion; adhere to national & international laws. (WK9)

PO8: Individual and Collaborative Team work: Function effectively as an individual, and as a member or leader in diverse/multi-disciplinary teams.

PO9: Communication: Communicate effectively and inclusively within the engineering community and society at large, such as being able to comprehend and write effective reports and design documentation, make effective presentation considering cultural, language, and learning differences.

PO10: Project Management and Finance: Apply knowledge and understanding of engineering management principles and economic decision-making and apply these to one's own work, as a member and leader in a team, and to manage projects and in multidisciplinary environments.

PO11: Life-Long Learning: Recognize the need for, and have the preparation and ability for i) independent and life-long learning ii) adaptability to new and emerging technologies and iii) critical thinking in the broadest context of technological change. (WK8)

Knowledge and Attitude Profile (WK)

WK1: A systematic, theory-based understanding of the natural sciences applicable to the discipline and awareness of relevant social sciences.

WK2: Conceptually-based mathematics, numerical analysis, data analysis, statistics and formal aspects of computer and information science to support detailed analysis and modelling applicable to the discipline.

WK3: A systematic, theory-based formulation of engineering fundamentals required in the engineering discipline.

WK4: Engineering specialist knowledge that provides theoretical frameworks and bodies of knowledge for the accepted practice areas in the engineering discipline; much is at the forefront of the discipline.

WK5: Knowledge, including efficient resource use, environmental impacts, whole-life cost, re-use of resources, net zero carbon, and similar concepts, that supports engineering design and operations in a practice area.

WK6: Knowledge of engineering practice (technology) in the practice areas in the engineering discipline.

WK7: Knowledge of the role of engineering in society and identified issues in engineering practice in the discipline, such as the professional responsibility of an engineer to public safety and sustainable development.

WK8: Engagement with selected knowledge in the current research literature of the discipline, awareness of the power of critical thinking and creative approaches to evaluate emerging issues.

WK9: Ethics, inclusive behaviour and conduct. Knowledge of professional ethics, responsibilities, and norms of engineering practice. Awareness of the need for diversity by reason of ethnicity, gender, age, physical ability etc. with mutual understanding and respect, and of inclusive attitudes.

Department of Electrical and Electronics Engineering

● — Vision — ●

Develop technically competent, emotionally strong and socially committed Electrical & Electronics Engineering professionals of international excellence.

● — Mission — ●

- To develop and maintain a conducive infrastructure and learning-environment, to bring out good quality Electrical & Electronics Engineering graduates.
- To appoint and retain a team of competent, dedicated and research-oriented faculty.
- To inculcate ethical & moral values among students and faculty.

● — Program Specific Outcomes (PSOs): — ●

Graduates of the program will be able to:

- Apply knowledge of mathematics, science and engineering to design, commission and maintain various types of electrical systems and address challenges in the field.
- Derive sustainable solutions to complex electrical engineering problems that meet the specified needs with ethical, social and environmental considerations.
- Empower the students for lifelong learning so as to adapt to dynamic changes in Electrical Engineering.



ST. JOSEPH'S

COLLEGE OF ENGINEERING
AND TECHNOLOGY,
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AUTONOMOUS

Vision

Developing into a world class, pace setting institute of Engineering and Technology with distinct identity and character, meeting the goals and aspirations of the society.

Mission

- To maintain a conducive infrastructure and learning environment for world class education.
- To nurture a team of dedicated, competent and research-oriented faculty.
- To develop students with moral and ethical values, for their successful careers, by offering variety of programs and services.



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Kerala, India



SYLLABUS

B.Tech.

ELECTRICAL AND ELECTRONICS ENGINEERING

2024 SCHEME

Semester VI

SEMESTER - VI

SIXTH SEMESTER (January-June)														
Sl. No:	Slot	Course Code	Course Type	Course Category	Course Title (Course Name)	Credit Structure				SS	Total Marks		Credits	Hrs/Week
						L	T	P	R		CIE	ESE		
1	A	24SJPCCEET601	PC	PC	Control Systems	3	1	0	0	4.5	40	60	4	4
2	B	24SJPCCEET602	PC	PC	Electrical System Design and Estimation	3	0	0	0	4.5	40	60	3	3
3	C	24SJPEEET63N	PE	PE	PE-3	3	0	0	0	4.5	40	60	3	3
4	D	24SJPBEET604	PC-PBL	PB	Power System Analysis	3	0	0	1	5.5	60	40	4	4
5	F	24SJGXEST605	ESC	GC	Design Thinking and Product Development	2	0	0	0	3	40	60	2	2
6	#O	24SJOE--T61N /24SJIE--T61N	OE/ILE	OE/IE	OE/ILE-1	3	0	0	0	4.5	40	60	3	3
7	L	24SJPCCEEL607	PCL	PC	Control Systems Lab	0	0	3	0	1.5	50	50	2	3
8	P	24SJPCCEEP608	PWS	PC	Mini Project: Socially Relevant Project	0	0	0	3	3	50	50	2	3
9	Q	24SJPCCEEL609	PCL	PC	Power Systems Lab	0	0	2	0	1	50	50	1	2
10	R/ M/ H		VAC		Remedial/Minor/Honours Course	3	0	0	0	4.5			3*	3*
	S5/ S6	Industrial Visit (Maximum of 06 Days are permitted, Not Exceeding more than 4 Working Days) /Industrial Training												
Total										32/ 36		23/26*	26/29*	

#Open Electives (OE)/ Industry Linked Elective (ILE) applicable to Electrical and Electronics Engineering students.

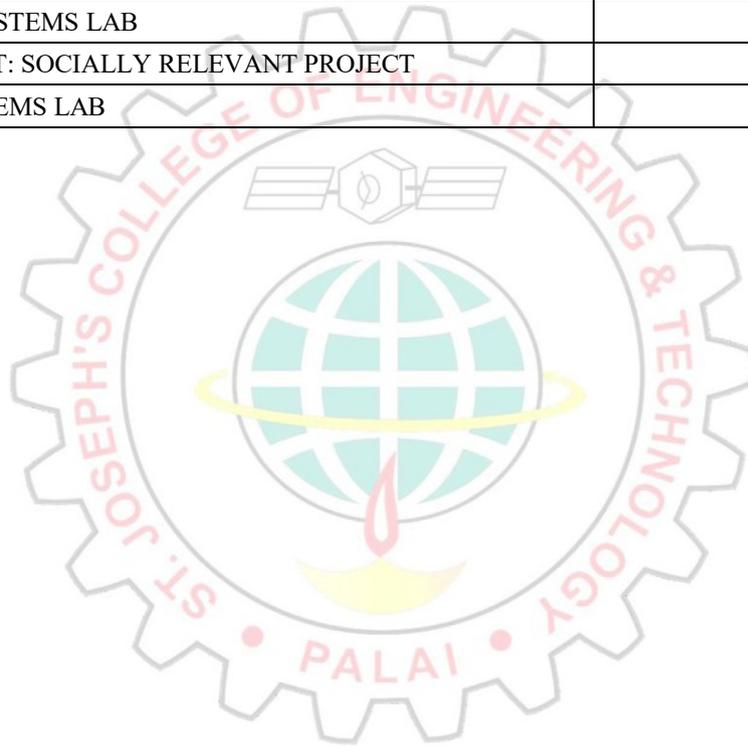
Note: Open Electives are such courses which will be offered by other departments. Like CS department students have to opt open electives from EC/ME/EE etc. departments.

Industrial Training: Students who are not participating in the industrial visit must attend industrial training during that period

PROGRAM ELECTIVE 3: 24SJPEEET63N					
SLOT	COURSE CODE	COURSES	L-T-P-R	HOURS	CREDIT
C	24SJPEEET631	Digital Protection of Power Systems	3-0-0-0	3	3
	24SJPEEET632	Operating Systems	3-0-0-0		3
	24SJPEEET633	High Voltage Engineering	3-0-0-0		3
	24SJPEEET634	Internet of Things	3-0-0-0		3
	24SJPEEET636	Digital Signal Processing	3-0-0-0		3
	24SJPEEET637	Cloud Computing	3-0-0-0		3
	24SJPEEET638	Optimization Techniques	3-0-0-0		3

COURSES**SEMESTER - VI**

Course Name	Page No.
CONTROL SYSTEMS	1
ELECTRICAL SYSTEM DESIGN AND ESTIMATION	5
PE-3	9
POWER SYSTEM ANALYSIS	20
DESIGN THINKING AND PRODUCT DEVELOPMENT	26
CONTROL SYSTEMS LAB	31
MINI PROJECT: SOCIALLY RELEVANT PROJECT	37
POWER SYSTEMS LAB	40



SEMESTER S6

CONTROL SYSTEMS

Course Code	24SJPCCEET601	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJPCCEET503	Course Type	Theory

Course Objectives:

1. To introduce various classical tools for analysis of linear control system in time and frequency domain.
2. To provide a fundamental knowledge of modern control system.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Introduction to Control Systems and its time domain analysis</p> <p><i>Review of Open loop and Closed loop control systems; Automatic control systems; Necessity and significance. (Not for evaluation)</i></p> <p><i>(1 hour)</i></p> <p>Block diagram representation - Signal flow graph - Marsels gain function (Concept only)</p> <p><i>Time domain analysis of control systems: Impulse and Step responses of first and second order systems -Types - poles and zeros, order - Pole dominance for higher order systems. Time domain specifications. (4 hours)</i></p> <p><i>Error analysis: Steady state error analysis and static error constants. (2 hours)</i></p>	7
2	<p>Root Locus Analysis and Controllers:</p> <p>Concept of stability - Routh Hurwits criterion</p> <p><i>Root locus technique: Construction of Root locus - stability analysis- effect of addition of poles and zeros; Effect of positive feedback systems on Root locus. (5 hours)</i></p> <p><i>Controller design: Types of controllers and their control action- proportional (P), integral (I), derivative (D), PID control. PID tuning using Ziegler-Nichols method. (2 hours)</i></p>	7

3	<p>Frequency domain analysis:</p> <p><i>Bode Plot:</i> Construction, Concept of gain margin and phase margin-stability analysis. (4 hours)</p> <p>Frequency domain specifications - correlation between time domain and frequency domain responses (Resonant peak and resonant frequency).</p> <p>Introduction to compensators. (Concept only). (2 hours)</p> <p>Polar plot: Gain margin and phase margin, Stability analysis. (2 hours)</p> <p>Nyquist stability criterion. Concept of Nichols Chart. (3 hours)</p>	11
4	<p>State space representation of systems:</p> <p><i>Introduction to state-space modelling:</i> State variables, state equations. State variable representation of electrical systems. (2 hours)</p> <p><i>Relationship between State space and Transfer function models:</i> Derivation of transfer functions from state equations. Controllable, Observable and Diagonal/Jordan canonical forms.</p> <p>Introduction to similarity transformations (concept only). (4 hours)</p> <p><i>Solution of time invariant systems:</i> Solution of time response of autonomous systems and forced systems. State transition matrix - computation using Method of Laplace Transform and Cayley Hamilton theorem. (4 hours)</p> <p><i>Controllability & Observability:</i> Definition, Kalman's test. (1 hour)</p>	11

Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination- 1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 3 marks <p>(8x3 =24marks)</p>	<ul style="list-style-type: none"> Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. <p>(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Understand the time domain responses of linear systems and predict and diagnose transient response parameters of the system for standard input functions.	K2
CO2	Analyse dynamics systems for their performance and stability using Root locus	K3
CO3	Apply frequency domain tools to analyse the performance of linear dynamic systems	K3
CO4	Represent and analyse dynamic systems using state-space.	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

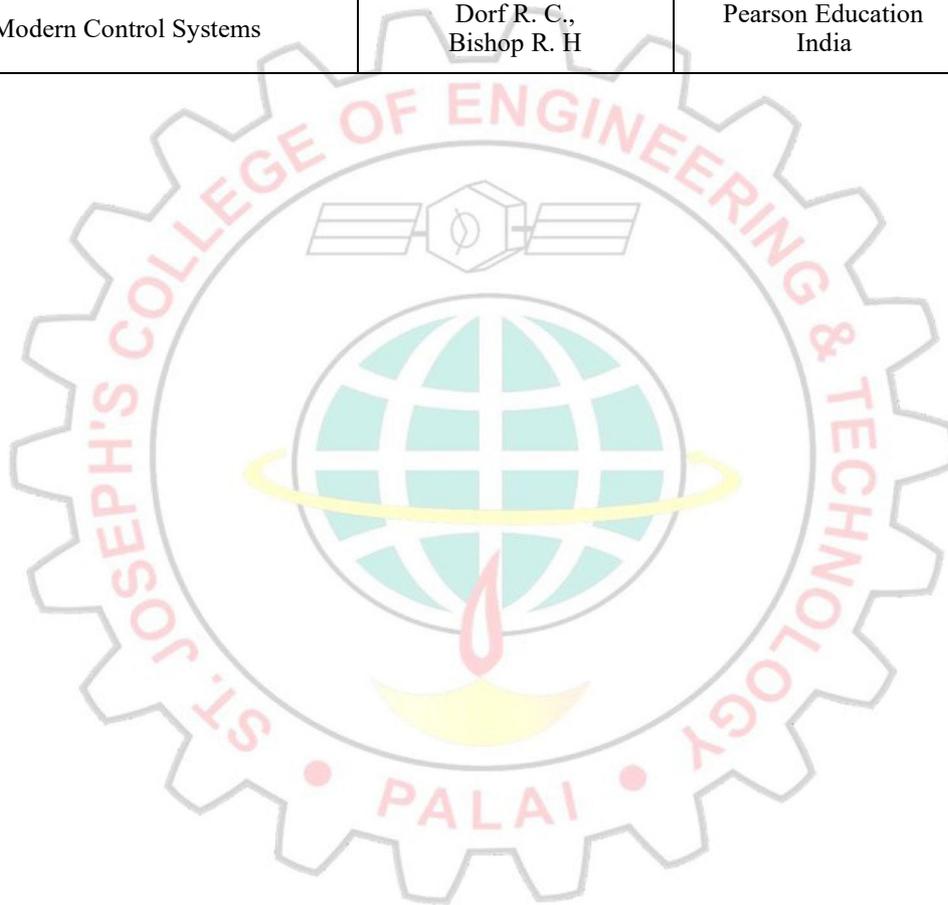
CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	2	1			1					1
CO2	3	3	2	1		1					1
CO3	3	3	2			1					1
CO4	3	2	1	1		1					1

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Modern Control Engineering	Katsuhiko Ogata	Pearson	5th edition, 2009
2	Control Systems Engineering	Norman S. Nise	Wiley	5th edition, 2009
3	Control Systems Engineering	I. J. Nagrath, M. Gopal	New Age	5th edition, 2009

Reference Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Automatic Control Systems,	Kuo B. C.	Prentice Hall of India	9th edition, 2014
2	Control Systems Principles and Design	Gopal M.	Tata McGraw Hill.	4th edition, 2012
3	Modern Control Systems	Dorf R. C., Bishop R. H	Pearson Education India	12th edition, 2013



SEMESTER - S6
ELECTRICAL SYSTEM DESIGN AND ESTIMATION

Course Code	24SJPC EET602	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

1. To create awareness regarding electrical symbols, Indian Standard codes, Indian Electricity acts and NEC norms
2. To enable students to design the various electrical installations with necessary precautions to ensure life safety, risk prevention and continuous operation of the system
3. To help in energy-efficient electrical design in compliance with codes and regulations.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Awareness on IS Codes - IS 732, IS 3043, IS 2026- IS 3646-part 1 & 2 - IS 5216 part 1 & 2. Electricity supply code-2014, IE Act 1910, 2003, NEC LT system wiring components, selection of cables, wires, switches, distribution box, metering system, basics of star rating and labelling .Principle of operation of Fuse, MCB, MCCB, ELCB/RCCB, isolator.	7
2	General requirements for electrical installations- Residential/ Commercial/ High rise building, method of load survey for electrical installation, Diversity factor. Sizing and selection of wires, MSB, SSB, DB and protection devices. Design steps in electrical wiring, material estimation and development of single line diagrams. Electrical CAD (optional). Pre-commissioning test applicable to domestic installation. Lighting design calculations - Definitions of Luminous flux, Luminous intensity, Illuminance. Illumination calculation, factors affecting Coefficients of Utilisation (CoU) - Light Loss Factor (LLF). Design and Estimation the quantity of material required in Electrical Installation for - Small residential building/Flat/Factory (Micro-Project).	12

3	<p>Indoor and Outdoor substation- selection of transformer, switch gears and protective devices, Procedure for HT connection, design and estimation the quantity of material required for substations, Pre-commissioning tests for transformers.</p> <p>Industrial loads, selection of starters, cable and switchgears, Power factor improvement – kVAr calculation, correction methods.</p> <p>Design of MSB & SSB including Motor Control Centre (MCC) - Selection of bus bars (CU & Al) and Switchgears.</p> <p>Specifications of LT Breakers and other LT panel components (Basics only)</p> <p>Selection of industrial UG cables - Calculation of ampacity, voltage drop, short circuit withstand capacity.</p>	10
4	<p>Standby DG Systems with AMF panel – Essential protections. UPS system and its design for residential application.</p> <p>Selection and installation of elevators and lifts.</p> <p>Earthing and Soil Resistivity calculation– Earth electrodes. Methods of earthing - Plate earthing - Pipe earthing - Rod earthing. Methods of improving earth resistance - Size of earth continuity conductor.</p> <p>Substation earthing and design (Theory only), substation lightning protection (Theory only).</p> <p>Solar PV Power generation – Design and installation of standalone and grid interactive Solar PV system -Smart meter/Net meter.</p>	7

Course Assessment Method (CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> • 2 Questions from each module. • Total of 8 Questions, each carrying 3 marks <p style="text-align: center;">(8x3 =24marks)</p>	<ul style="list-style-type: none"> • Each question carries 9 marks. • Two questions will be given from each module, out of which 1 question should be answered. • Each question can have a maximum of 3 sub divisions. <p style="text-align: center;">(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Understand the Indian standards and code of practice for efficient and effective energy usage with various electrical system design components.	K2
CO2	Design electrical wiring for residential and commercial consumers as per IS codes and NEC and integration of PV systems	K3
CO3	Design electrical installation for industrial consumers and high rise buildings.	K3
CO4	Analyse electrical system conditioning equipment and power backups.	K4
CO5	Design various earthing methods and protection	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	2	1			2	2	2			2
CO2	3	3	3	1		2		2	1		2
CO3	3	3	3	1		2		2	1		2
CO4	3	3	3	1		2		2	1		2
CO5	3	3	3	1		2		2	1		2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

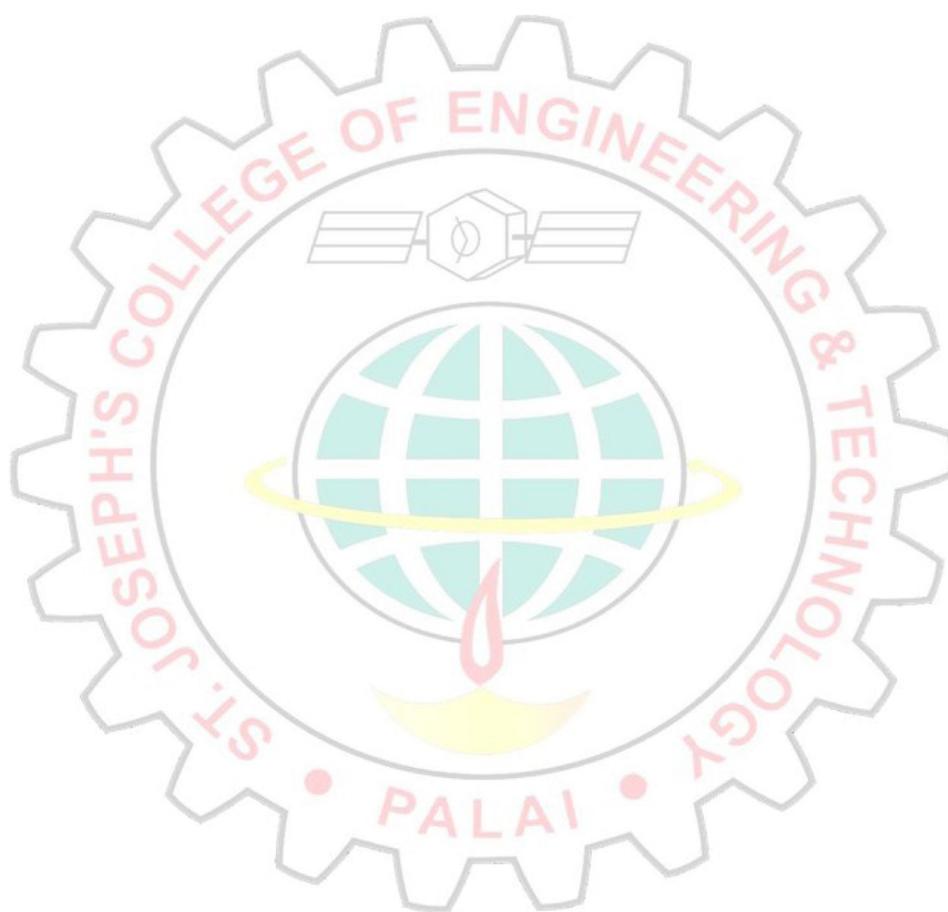
Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	National Electrical Code, Bureau of Indian Standards.		Bureau of Indian Standards.	
2	Electrical Systems Design	M. K. Giridharan	IK International Publishers, New Delhi	
3	Electrical Design Estimating Costing	K. B. Raina, S. K. Bhattacharya	NEW AGE; Reprint edition	
4	Residential Commercial and Industrial Systems	H. Joshi	McGraw Hill Education	

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	National Lighting Code 2010, Bureau of Indian Standards.			

2	National Building Code of INDIA 2016 - Bureau of Indian Standards.			
3	A Course in Electrical Installation Estimating and Costing.	J. B. Gupta	S.K. Kataria & Sons	Reprint 2013 edition
4	Electrical estimating and costing	S. Singh, and R. D. Singh	Dhanpat Rai and Co.	1997



SEMESTER S6
DIGITAL PROTECTION OF POWER SYSTEMS

Course Code	24SJPEEET631	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJPCEET501, 24SJPBEET604	Course Type	Theory

Course Objectives:

1. To deliver fundamental concepts to design various electronic circuits to implement various relaying functions.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Introduction: Need for protective systems, Zones of protection, Current transformers and voltage transformers (Electromagnetic and Capacitive voltage transformers), Principle of operation of magneto optic CT/ PT, effect on relaying philosophy.</p> <p>Relays: Over current relays - time-current characteristics of over current relays: definite time over current relays, inverse Definite Minimum time - directional over current relays, current setting and time setting - Numerical Problems - Differential relays: Operating and restraining characteristics, types of differential relays, Distance relays: impedance relays, reactance relays, mho relays (basic principles and characteristics only)</p>	9
2	<p>Protection of Transmission Lines: Schemes of distance protection, Differential line protection, Phase comparison line protection.</p> <p>Protection of Bus-bar, Transformer and Generator & Motor: Types of faults, differential protection: High impedance and low impedance differential protection schemes, harmonic restraint relay, Restricted Earth Fault Protection, frame leakage protection, stator and rotor protection against various types of faults.</p>	9

3	<p>Digital (Numerical) Relays: Basic Components of numerical Relays with block diagram, Processing Unit, Human machine Interface, Principle of operation, Comparison of numerical relays with electromechanical and static relays, Advantages of numerical relays - communication in protective relays (IEC 61850), Information handling with substation automation system (SAS) Signal Conditioning Subsystems: Surge Protection Circuits, Anti-aliasing filter, Conversion Subsystem, The Sampling Theorem, aliasing, Sample and Hold Circuit, Concept of analog to digital and digital to analog conversion, Idea of sliding window concept, Fourier, Discrete and fast Fourier transforms</p>	9
4	<p>Signal processing techniques: Sinusoidal wave based algorithms, Fourier Analysis based algorithms (half cycle and full cycle), Least squares based algorithm. Digital filters - Fundamentals of Infinite Impulse Response Filters, Finite Impulse Response filters, Filters with sine and cosine windows.</p> <p>Wide Area Protection and Measurement: Phasor Measurement Units, concept of synchronized sampling, Definition of wide-area protection, Architectures of wide-area protection, concept of Adaptive relaying, advantages of adaptive relaying and its application, Adaptive Differential protective scheme.</p>	9

Course Assessment Method (CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Micro-project	Internal Examination-1 (Written)	Internal Examination- 2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	PartB	Tota 1
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 3 marks <p>(8x3 =24marks)</p>	<ul style="list-style-type: none"> Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. <p>(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Identify the relay protection scheme suitable for overcurrent, differential and distance protection.	K3
CO2	Develop the protection scheme for bus bars, transformers, generators, motors and distribution systems using appropriate protective relays	K3
CO3	Illustrate the operation of a numerical relay.	K2
CO4	Explain signal processing methods and algorithms in digital protection	K2
CO5	Infer emerging protection schemes in power systems	K3

Note; K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3		2								
CO2	3		2								
CO3	3		2								
CO4	3		2								
CO5	3		2								

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), No Correlation

Text Books				
SI. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Digital Protection of Power System	A. T. Johns and S. K. Salman	Peter Peregrinus Ltd, UK	1995
2	Computer Relaying for Power Systems	A. G. Phadke and James S. Thorpe	Research study press Ltd, John Wiley & Sons, Taunton, UK	1988
3	Power System Protection and Switchgear	Badri Ram and D. N. Viswakarma	Tata McGraw Hill Education, Pvt Edition	2011
4	Digital Signal Processing in Power System Protection and Control	Waldemar Rebizant	Springer Publication	2008

Video Links (NPTEL, SWAY AM...)	
SI No	Link ID
1	https://archive.nptel.ac.in/courses/117/107/117107148/ (NPTEL lecture IIT Roorkee)

HIGH VOLTAGE ENGINEERING

Course Code	24SJPEEET633	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	NIL	Course Type	Theory

Course Objectives:

1. To introduce basic terms and techniques applicable to high voltage ac and dc networks.
2. To learn about generation of different type of High voltage waveforms, their measurement and analysis.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Generation of High DC Voltages- half-wave rectifier circuit- Cockroft-Walton voltage multiplier circuit- Electrostatic generator</p> <p>Generation of high AC voltages-Cascaded Transformers- Series resonant circuit.</p> <p>Generation of Impulse Voltages and Currents- Impulse voltage- Impulse generator circuits- Multistage impulse generator circuit- Construction of impulse generator- Triggering of impulse generator- Impulse current generation.</p>	9
2	<p>High Voltage Measurement Techniques -Measuring Spark Gaps - Sphere- to-sphere Spark Gap -Rod-to-rod Spark Gap - Electrostatic Voltmeter- Field Sensors - Electrically Short Sensors, Electrically Long Sensors, Potential-free Probes, Generator-mode Sensors, Electro- optical and Magneto-optical Field Sensors - Voltage Dividers - Instrument Transformers - Measurements of R.M.S. Value, Peak Value and Harmonics</p> <p>Current Measurement: Measurement of high currents in HV systems</p> <p>Dielectric measurements- Dissipation Factor and Capacitance, Insulation Resistance, Conductivity, Dielectric System Response-Partial discharge measuring technique- Requirements on a partial discharge measuring</p>	9

	system - Measuring systems for apparent charge – Partial discharge measurements on high-voltage transformers, high-voltage cables, high-voltage gas-insulated substations.	
3	<p>Classification of Voltages and Overvoltages-Origin of Overvoltages – Representative Overvoltages- Performance Criterion –Withstand voltage.</p> <p>Insulation Coordination Procedure- Determination of Representative Voltages and Overvoltages- Continuous Power Frequency Voltage, Temporary Overvoltages, Slow-Front Overvoltages, Fast-Front Overvoltages.</p> <p>Determination of Coordination Withstand Voltage (U_{cw})-Deterministic Approach, Statistical Approach: Risk of Failure - Determination of Required Withstand Voltage (U_{rw})-Altitude Correction Factor, Safety Factor (K_s)- Selection of Standard Withstand Voltage (U_w)</p> <p>Surge Arresters- Rated Voltage- Discharge Current- Impulse Current Tests-Residual Voltages- Arrester Durability Requirements.</p>	9
4	<p>High voltage Testing of equipment: insulators, bushings, isolators, circuit breakers, transformers, surge diverters, cables.</p> <p>Insulation Systems:</p> <p>AC Voltages -Cables, bushings and transformers</p> <p>DC Voltages- Capacitors, HVDC bushings and Cables</p> <p>Impulse Voltages -Electrical Stress and Strength -Energy Storage -Impulse Capacitors (Energy Storage or Surge Capacitors)</p> <p>Lightning Protection Technologies- Light and Laser Technology- X-ray Technology-Electrostatic Particle Precipitation, Ionization- Spark plugs.</p>	9

Course Assessment Method

(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination-2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 3 marks <p>(8x3 =24marks)</p>	<ul style="list-style-type: none"> Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. <p>(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Identify different high voltage and current waveform generation circuits.	K2
CO2	Implement different sensing & measurement techniques for high voltage and current measurement.	K3
CO3	Describe insulation coordination and surge arrestor design.	K2
CO4	Implement different testing methods for equipments and applications of HV systems and explain lightning protection technologies.	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3										2
CO2	3										2
CO3	3					2					2
CO4	3	2				2					2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	High Voltage Engineering	C. L. Wadhwa	New Age International	2011
2	High Voltage Engineering Fundamentals – Technology Applications	Andreas Kuchler	Springer	2018
3	High Voltage Engineering	Naidu M. S. and Kamaraju V.	Tata Mc Graw Hill	2004
4	High Voltage Engineering Fundamentals	Kuffel E. Zaengl S. and Kuffel J.	Elsevier India P Ltd	2005

SEMESTER S6

INTERNET OF THINGS

Course Code	24SJPEEET634	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	3:0:0:0	ESE Marks	60
Credits	3	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	Nil	Course Type	Theory

Course Objectives:

1. This course aims to introduce IOT fundamentals.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Introduction to IoT technology: Definitions of IoT, Characteristics of IoT devices – power, computational constraints, IoT Architectural view – Middleware based architecture, Service oriented architecture, M2M Communication and IoT, Typical application areas of IoT technology (case studies of at least four domains) - Energy management and Smart grid, IoT for Home, Cities, Environment monitoring, Agriculture, Supply chain and customer monitoring	9
2	Components of IoT technology: Identification/Addressing - Electronic Product Codes, RFID, ubiquitous code, IPv4, IPv6. Sensors and Actuators*. IoT Hardware**, IoT Software – overview of Operating systems, Firmware, Middle ware, Application software used in IoT. Connectivity for IoT devices – characteristics.	9
3	Communication technologies for IoT : Zigbee - key features, architecture, limitations, Bluetooth technology - bluetooth stack, piconet, scatternet, limitations, Bluetooth Low Energy (key features, architecture, limitations), Wifi (IEEE 802.11) technology – key features, limitations, Cellular technology – GSM, 3G, 4GLTE (overview), features, limitations, LoRa technology – features, LoRaWAN architecture, 6LoWPAN – features, protocol stack, Narrow Band (NB- IoT) – features, applications, Sigfox – features, applications	9

4	<p>IoT Data Management : Storage technologies for IoT hardware – Volatile, Non-volatile, Embedded (MTP/OTP), external flash (NAND/NOR), DRAM, eflash, UFS, eMMC (overview of technologies). Cloud and IoT, Cloud computing – architecture, advantages of cloud computing, Software as a Service (SaaS), Platform as a Service (PaaS), Infrastructure as a Service (IaaS). Case study of commercial cloud computing platforms like - Microsoft Azure IoT Suite, Google Cloud's IoT Platform, IBM Watson IoT Platform. IoT analytics</p>	9
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Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignment/ Microproject	Internal Examination-1 (Written)	Internal Examination-2 (Written)	Total
5	15	10	10	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> • 2 Questions from each module. • Total of 8 Questions, each carrying 3 marks <p style="text-align: center;">(8x3 =24marks)</p>	<ul style="list-style-type: none"> • Each question carries 9 marks. • Two questions will be given from each module, out of which 1 question should be answered. • Each question can have a maximum of 3 sub divisions. <p style="text-align: center;">(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Explain in a concise manner the architecture of IoT	K2
CO2	Identify various hardware and software components used in IoT	K3
CO3	Discuss the various communication technologies and interfaces in IoT	K2
CO4	Describe the usage of modern technologies like cloud computing for data management in IoT	K2

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	2	2	2							2
CO2	3	2	2	2							2
CO3	3	2	2	1							2
CO4	3	2	2	1							2

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Internet of Things : Architecture and Design Principles”	Rajkamal	McGraw Hill (India) Private Limited.	2nd edition, 2022
2	“Internet of Things (A Hands-on- Approach)”	Vijay Madiseti and Arshdeep Bahga	Orient Blackswan Private Limited - New Delhi	1st Edition, 2015

Reference Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Internet of things: A survey on enabling technologies, protocols, and applications	Al-Fuqaha	IEEE Communications Surveys & Tutorials	2015
2	The Internet of Things	Samuel Greengard	The MIT Press Essential Knowledge series Paperback	March 20, 2015
3	The Internet of Things: Converging Technologies for Smart Environments and Integrated Ecosystems	Ovidu Vermesan and Peter Friess	River Publishers	1st Edition, 2013
4	. Internet of Things - From Research and Innovation to Market Deployment	Peter Friess, Ovidiu Vermesan	River Publishers	1 st Edition, 2014

Video Links (NPTEL, SWAYAM...)	
Module No.	Link ID
1	https://youtu.be/WUYAjsxnwjU4?si=s58W-NKMrEQMaJ8m https://youtu.be/BXDxYh1EV2w?si=8oFtQB9vycC_c-t2
2	https://youtu.be/z3VEZPwl5gA?si=tNuzG_By-KBU3ks_ https://youtu.be/SXz0XR68dwE?si=1tVN1g9FQcGp87li https://youtu.be/TvzgzO6xKrY?si=gYzJstW51MTNsgKj
3	https://youtu.be/qko-flVDhCM?si=0tWM_OHS395ESV_w https://youtu.be/d9QfVpCG00Y?si=qeHk8tPg_torr2yX https://youtu.be/1zQ8wbBozqI?si=7vOSHMt8OT3nQINO
4	https://youtube.com/playlist?list=PLE7VH8RC_N3bpVn-e8QzOAHziEgmjQ2qE&si=rr5Fpuew5q9_Y4qg

SEMESTER S6

POWER SYSTEM ANALYSIS

Course Code	24SJPBEET604	CIE Marks	60
Teaching Hours/Week (L: T:P: R)	3:0:0:1	ESE Marks	40
Credits	4	Exam Hours	1 Hrs. 40 Min.
Prerequisites (if any)	Power Generation, Transmission and Protection	Course Type	Theory

Course Objectives:

- To enable the students to analyse power systems under normal and abnormal conditions.
- To understand the need for load flow analysis and different methods
- To understand power system modeling
- To understand the need for stability studies and their analysis

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	<p>Per unit quantities-single phase and three phase-selection of base quantities -advantages of per unit system –changing the base of per unit quantities-Simple problems.</p> <p>Modelling of power system components - single line diagram – per unit quantities. Symmetrical components- sequence impedances and sequence networks of generators, transformers and transmission lines.</p> <p>Fault calculations-Symmetrical and unsymmetrical- Fault level of installations- Limiters.</p>	12
2	<p>Load flow studies – Introduction-types-network model formulation and admittance matrix, Gauss-Siedel (two iterations), Newton-Raphson (Qualitative analysis only) and Fast Decoupled method (two iterations) - principle of DC load flow - Introduction to distribution flow.</p>	12
3	<p>Turbines and speed governors-Inertia-Automatic Generation Control: Load frequency control: single area and two area systems - Subsynchronous Resonance - Automatic voltage control -Exciter Control- SCADA System</p>	11

4	<p>Power system stability - steady state, dynamic and transient stability-power angle curve- Mechanics of angular motion-Swing equation Equal area criterion application - Methods of improving stability limits.</p> <p>Economic Operation - Distribution of load between units within a plant - transmission loss as a function of plant generation - distribution of load between plants.</p> <p>Unit commitment: Introduction — constraints on unit commitments: spinning reserve, thermal unit constraints- hydro constraints.</p>	11
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Suggestion on Project Topics

Project-Based Learning (PBL) is a student-centered teaching approach where the teacher serves as a facilitator and advisor.

Students are encouraged to think the need of the society and industry. Select a project topic relevant to the present society as well as covers topics in the syllabus.

In the first step they start defining problem statement with requirements and specifications.

In the second step, students work in groups to discover optimal and creative solutions by sharing their unique and inventive ideas for solutions.

They begin designing and developing components using contemporary tools and technology in the third level. Design the circuit and simulate it using available simulation tools. Also perform the hardware implementation to make it a product.

Project Topic Suggestions:

1. Software based Mini Projects
(Ideal for MATLAB, Simulink, Python, PSCAD, ETAP, or OpenDSS users)
2. Per Unit Converter and Display using Arduino
3. Miniature Transmission Line Model
4. Fault Detection in Transmission Line
5. Load Flow Indicator System

Course Assessment Method (CIE: 60 marks, ESE: 40 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Project	Internal Ex-1	Internal Ex-2	Total
5	30	12.5	12.5	60

End Semester Examination Marks (ESE):

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 2 marks (8x2 =16 marks) 	<ul style="list-style-type: none"> 2 questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 2 sub divisions. Each question carries 6 marks. (4x6 = 24 marks) 	40

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome	Bloom's Knowledge Level (KL)
CO 1 Apply the per unit scheme for any power system network and model the system	K3
CO 2 Analyse the voltage profile of any given power system network using iterative methods and compute the fault analysis	K3
CO3 Model the control scheme of power systems	K3
CO4 Analyse the steady state and transient stability of power system networks and Schedule optimal generation scheme	K3
CO5 Implement a software / hardware circuit for the solution of a real time Problem.	K4

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO 1	3	3									2
CO 2	3	3	2								2
CO 3	3	3	2								1
CO 4	3	2									
CO 5	3	3	1			3				3	1

References

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Power System Analysis and Design	B. R. Gupta	S. Chand, New Delhi	6th ed., 2011
2	Transmission & Distribution of Electrical Power	J. B. Gupta	S.K. Kataria & Sons	2006
3	Power System Analysis	Hadi Saadat	McGraw Hill	2nd ed., 2002
4	Modern Power System Analysis	D. P. Kothari & I. J. Nagrath	McGraw Hill (India)	5th ed., 2022
5	Power System Stability and Control	P. Kundur	McGraw Hill	2006
6	Electrical Power Systems	S. L. Uppal & S. Rao	Khanna Publishers	2009
7	Electrical Power Systems	C. L. Wadhwa	New Age International	33rd ed., 2004
8	Electric Power System	B. M. Weedy, B. J. Cory, N. Jenkins, J. B. Ekanayake, G. Strbac	John Wiley & Sons	5th ed., 2012

Video Links (NPTEL, SWAYAM...)

Module No.	Link ID
1	https://onlinecourses.nptel.ac.in/noc19_ee62/preview
2	https://nptel.ac.in/courses/108105133
3	https://archive.nptel.ac.in/courses/117/105/117105140/
4	https://archive.nptel.ac.in/courses/108/107/108107127/

PBL Course Elements

L: Lecture (3 Hrs.)	R: Project (1 Hr.), 2 Faculty Members		
	Tutorial	Practical	Presentation
Lecture delivery	Project identification	Simulation/ Laboratory Work/ Workshops	Presentation (Progress and Final Presentations)
Group discussion	Project Analysis	Data Collection	Evaluation
Question answer Sessions/ Brainstorming Sessions	Analytical thinking and self-learning	Testing	Project Milestone Reviews, Feedback, Project reformation (If required)
Guest Speakers (Industry Experts)	Case Study/ Survey Report	Field Prototyping	Poster Presentation/ Video Presentation: Students present their results in a 2 to 5 minutes video

Assessment and Evaluation for Project Activity

Sl. No	Evaluation for	Allotted Marks
1	Project Planning and Proposal	5
2	Contribution in Progress Presentations and Question Answer Sessions	4
3	Involvement in the project work and Team Work	3
4	Execution and Implementation	10
5	Final Presentations	5
6	Project Quality, Innovation and Creativity	3
Total		30

1. Project Planning and Proposal (5 Marks)

- Clarity and feasibility of the project plan
- Research and background understanding
- Defined objectives and methodology

2. Contribution in Progress Presentation and Question Answer Sessions (4 Marks)

- Individual contribution to the presentation
- Effectiveness in answering questions and handling feedback

3. Involvement in the Project Work and Team Work (3 Marks)

- Active participation and individual contribution
- Teamwork and collaboration

4. Execution and Implementation (10 Marks)

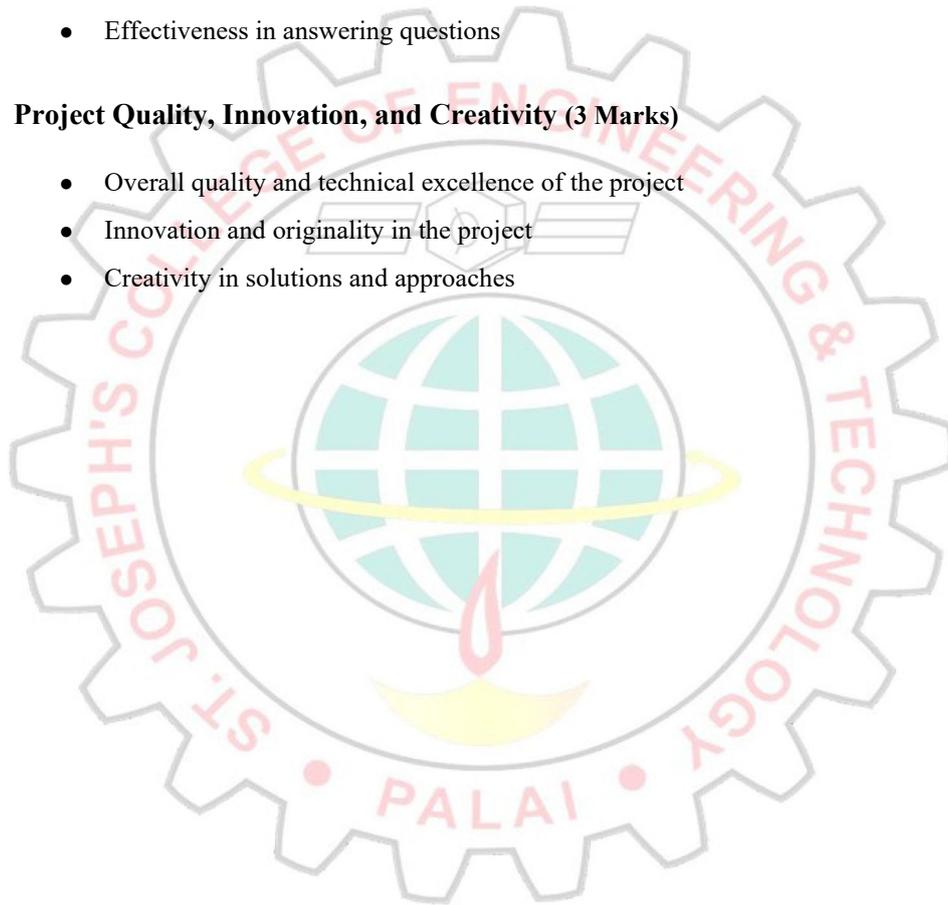
- Adherence to the project timeline and milestones
- Application of theoretical knowledge and problem-solving
- Final Result

5. Final Presentation (5 Marks)

- Quality and clarity of the overall presentation
- Individual contribution to the presentation
- Effectiveness in answering questions

6. Project Quality, Innovation, and Creativity (3 Marks)

- Overall quality and technical excellence of the project
- Innovation and originality in the project
- Creativity in solutions and approaches



SEMESTER S6

DESIGN THINKING AND PRODUCT DEVELOPMENT

Course Code	24SJGBEST605	CIE Marks	40
Teaching Hours/Week (L: T:P: R)	2:0:0:0	ESE Marks	60
Credits	2	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	None	Course Type	Theory

Course Objectives:

1. To guide students through the iterative stages of design thinking, including empathizing with users, defining problems, ideating solutions and developing Proof of Concepts (PoC) and technical feasibility studies.
2. To promote the development of critical thinking skills by engaging students in integrative inquiry, where they ask meaningful questions that connect classroom knowledge with real-world applications.
3. To equip students with the ability to involve in product design considering the sustainability, inclusivity, diversity and equity aspects.

SYLLABUS

Module No.	Syllabus Description	Contact Hours
1	Fundamentals of design thinking and product development: Overview of stages of product development lifecycle; Design thinking -Definition-Design thinking for product innovation; Bringing social impact in ideation-Identifying societal needs-understanding multi-faceted issues-community engagement and empathetic design- technological innovation meeting societal needs; Understanding and Bridging the divide using Human Centered Design (HCD); Designing for inclusivity in product development-embracing user diversity - Long term impact - sustainability encompassing environmental, economic and social dimensions; Technology Readiness Level in the Innovation Life-cycle; Performing a self-check on innovative ideas - Originality of idea-understanding innovation landscape - patentability - understanding the economic landscape - Unique Selling Proposition (USP) - Repeatability and Manufacturability - Sustainability - Leveraging business models for comprehensive analysis	6

2	Empathize: Design thinking phases; Role of empathy in design thinking; Methods of empathize phase - Ask 5 Why/ 5 W+H questions; Empathy maps - Things to be done prior to empathy mapping - Activities during and after the	6
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	<p>session; Understanding empathy tools - Customer Journey Map - Personas.</p> <p>Define: Methods of Define Phase: Storytelling, Critical items diagrams, Define success.</p>	
3	<p>Ideation : Stages of ideation; Techniques and tools - Divergent thinking tools - Convergent thinking tools - Idea capturing tools; Cross-industry inspiration; Role of research in ideation - Market research - consumer research - leveraging research for informed ideation; Technological trends - navigating the technological landscape - Integrating emerging technologies; Feasibility studies - technical, economic, market, operational, legal, and ethical feasibility; Ideation session- techniques and tips.</p> <p>Proof of Concept (PoC): Setting objectives; Risk assessment; Technology scouting; Document and process management; Change management; Knowledge Capture; Validating PoC; Story telling in PoC presentation</p>	6
4	<p>Design: Navigating from PoC to detailed design; Developing Specification Requirement Document (SRD)/Software Requirement Specification (SRS); Design for manufacturability; Industrial standards and readability of code; Design to cost; Pre-compliance; Optimized code; Design Failure Mode and Effects Analysis (DFMEA); Forecasting future design changes.</p> <p>Prototyping: Alpha prototypes; Beta prototypes; Transition from design to prototype; Goals and expectations for Alpha and Beta prototypes; Effective strategies for maintaining timeline in prototyping; Testing and refining Alpha prototypes; Transitioning to Beta prototypes.</p> <p>Pilot build: Definition and purpose of a pilot build; setting objectives; Identification and selection of manufacturing partner for pilot build; Testing procedures in pilot build; Scaling from pilot build to full-scale production / implementation.</p>	6

Course Assessment Method
(CIE: 40 marks, ESE: 60 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Assignments	Internal Examination	Reflective Journal and Portfolio	Total
5	20	10	5	40

End Semester Examination Marks (ESE)

In Part A, all questions need to be answered and in Part B, each student can choose any one full question out of two questions

Part A	Part B	Total
<ul style="list-style-type: none"> 2 Questions from each module. Total of 8 Questions, each carrying 3 marks <p align="center">(8x3 =24marks)</p>	<ul style="list-style-type: none"> Each question carries 9 marks. Two questions will be given from each module, out of which 1 question should be answered. Each question can have a maximum of 3 sub divisions. <p align="center">(4x9 = 36 marks)</p>	60

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Empathize to capture the user needs and define the objectives with due consideration of various aspects including inclusivity, diversity and equity	K5
CO2	Ideate using divergent and convergent thinking to arrive at innovative ideas keeping in mind the sustainability, inclusivity, diversity and equity aspects.	K6
CO3	Engage in Human Centric Design of innovative products meeting the specifications	K5
CO4	Develop Proof of Concepts (PoC), prototypes & pilot build of products and test their performance with respect to the Specification Requirement Document.	K4
CO5	Reflect on professional and personal growth through the learnings in the course, identifying areas for further development	K4

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table (Mapping of Course Outcomes to Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
CO1	3	2	2		2	3	3	3	2	2		3
CO2	3	2	3		2	3	3	3	2	2		3
CO3	3	2	3		2	3	3	2	2	2		3
CO4	3	2	2		3	3	3	2	2	2		3
CO5	3					3	3	2	2	2		3

Note: 1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books				
Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Product Sense: Engineering your ideas into reality	Dr. K R Suresh Nair	NotionPress.com	2024
2	Change by Design: How Design Thinking Transforms Organizations and Inspires Innovation	Tim Brown	HarperCollins Publishers Ltd.	2009
3	Design Thinking for Strategic Innovation	Idris Mootee	John Wiley & Sons Inc.	2013

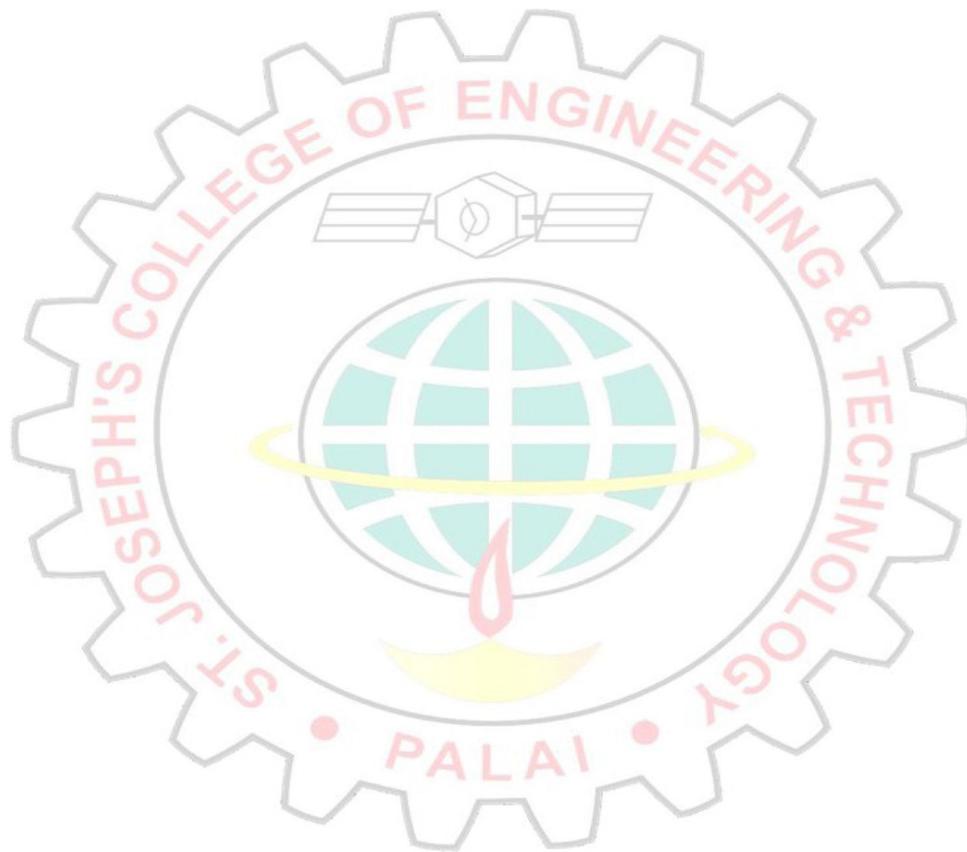
Sample Assignments:

1. Evaluate and prepare a report on how the aspects including inclusivity, diversity and equity are taken into consideration during the empathize and define phases of the Mini project course.
2. Evaluate and prepare a report on how the aspects including sustainability, inclusivity, diversity and equity are taken into consideration during the ideate phase of the Mini project course.
3. Evaluate and prepare a report on how User-Centric Design (UCD) is used in the design and development of PoC of the product being developed in the Mini project course.
4. Prepare a plan for the prototype building of the product being developed in the Mini project course.
5. Report on the activities during the empathize phase including the maps & other materials created during the sessions.
6. Report on the activities during the define phase including the maps & other materials created during the sessions.
7. Report of all the ideas created during the ideation phase of the Mini project course through the tools including SCAMPER technique, SWOT analysis, Decision matrix analysis, six thinking hats

exercise

8. Prepare a full scale production plan for the product being developed in the Mini project course.
9. Create a Stanford Business Model Canvas related to the Mini project.

An industrial visit of at least a day for experiential learning and submit a report on the learnings, for example industry standards and procedures.



SEMESTER S6
CONTROL SYSTEM LAB

Course Code	24SJPCeel607	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	0:0:3:0	ESE Marks	50
Credits	2	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJPCeet302/ 24SJPCeet601	Course Type	Lab

Course Objectives:

1. To make the students learn how to determine the parameters experimentally and model the given system.
2. To make the students learn the experimental determination of responses of dynamic systems and analyse its behaviour.
3. To make the students learn the different analysis and controller design tools using appropriate simulation software

Expt. No.	Experiments
1	Transfer Function and State Space Modelling of Armature Controlled DC Motor. Objective: Obtain the transfer function and state space model of the armature and field-controlled DC motor by experiment.
3	Synchro Transmitter and Receiver for open loop position control. Objective: Plot the characteristics of synchro. Error study of the synchro transmitter and receiver pair as a simple open loop position control in Direct mode and Differential mode.
4	Realisation of lead compensator. Objective: Design, set up and analyse the gain and phase plots of a lead compensator by hardware experimentation using i) passive elements and ii) active components
5	Realisation of lag compensator. Objective: Design, set up and analyse the gain and phase plots of a lag compensator by hardware experimentation using: i) passive elements and ii) active components

6	<p>Performance of a typical process control system</p> <p>Objective: Study of performance characteristics and response analysis of a typical temperature/ Flow/ Level control system.</p>
7	<p>Step response and frequency response of a second order system using simulation</p> <p>Objective: To analyse the response of a second order RLC System (using (MATLAB/SCILAB/similar softwares)</p> <p>a. The effect of damping factor ($0 < \xi < 1$, $\xi = 1$, $\xi > 1$) for a step input .</p> <p>b. Comparison of the delay time, rise time, peak overshoot and settling time with the experimental values for $0 < \xi < 1$.</p> <p>c. The effect of damping ratio on frequency response.</p> <p>d. Comparison of resonant peak, resonant frequency and bandwidth with the experimental values for $0 < \xi < 1$.</p>
8	<p>Performance Analysis using Root-Locus and frequency Response Methods in MATLAB/SCILAB/similar softwares.</p> <p>Objective:</p> <p>1. Plot the i) root locus ii) Bode plot and iii) Nyquist plot and iv) Nichols chart for the given transfer functions and analyse the following:</p> <p>Root Locus:</p> <p>a. Determine the critical gain, frequency of oscillation at critical gain.</p> <p>b. The effect of gain, K on the stability.</p> <p>c. Determine the gain corresponding to a given damping ratio and obtain the step response of the system for the corresponding gain.</p> <p>d. The effect of the addition of poles and zeros on the given system.</p> <p>Frequency response:</p> <p>e. Determination of Gain Margin and Phase Margin (stable and unstable, minimum/non-minimum phase system)</p> <p>f. The effect of controller gain K on the stability margin</p> <p>g. The effect of the addition of poles and zeros on the given system (especially the poles at origin).</p> <p>h. Determine the stability of a given minimum and non-minimum phase system using Nyquist stability criterion.</p> <p>i. Determine the bandwidth of a given system from open loop frequency response using Nichols chart.</p>

9	<p>Design of lag, lead compensator using root locus. Objective: Design a suitable compensator for the given system to satisfy the given time domain specifications using MATLAB/SCILAB/ similar software.</p>
10	<p>Design of lag, lead compensator using frequency response. Objective: Design a suitable compensator for the given system to satisfy the given frequency domain specifications using MATLAB/SCILAB/ similar software.</p>
11	<p>State Space Model, Analysis and Controller Design</p> <p>Objective: Analyse the given system (eg. Armature controlled DC motor) in state space and design a controller by pole-placement technique using MATLAB/SCILAB/ similar software.</p> <p>a. Determine the open loop stability, controllability and observability b. Design a state-feedback controller by pole-placement technique for a given specification.</p>
12	<p>PID Controller Design</p> <p>Objective: Design a PID controller for the given system (eg. Armature controlled DC motor) using SIMULINK/ MATLAB based tool boxes.</p> <p>a. Design of a suitable controller (P/PI/PD/PID) to meet the desired specifications using root locus/frequency response.</p>

Note:

1. A minimum of **10 experiments** are compulsory.

Course Assessment Method (CIE: 50 marks, ESE: 50 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Preparation/Pre-Lab Work experiments, Viva and Timely completion of Lab Reports / Record (Continuous Assessment)	Internal Examination	Total
5	25	20	50

End Semester Examination Marks (ESE):

Procedure/ Preparatory work/Design / Algorithm	Conduct of experiment/ Execution of work/ troubleshooting/ Programming	Result with valid inference/ Quality of Output	Viva voce	Record	Total
10	15	10	10	5	50

- **Submission of Record:** Students shall be allowed for the end semester examination only upon submitting the duly certified record.
- **Endorsement by External Examiner:** The external examiner shall endorse the record

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Identify and conduct suitable experiments to determine the parameters to model a physical system.	K3
CO2	Conduct suitable experiments and determine the performance specifications.	K3
CO3	Analyse a linear continuous time system model using simulation tools.	K3
CO4	Design suitable controllers/compensators to meet the performance requirements using simulation tools.	K5

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO- PO Mapping (Mapping of Course Outcomes with Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	3	2	2	2	1		1			1
CO2	3	3	2	2	2	1		1			1
CO3	3	3	2	2	2	1		1			1
CO4	3	3	2	2	2	1		1			1

1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), -: No Correlation

Text Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Modern Control Engineering	Katsuhiko Ogata	Pearson	5th edition, 2009
2	Control Systems Engineering	Norman S. Nise	Wiley	5th edition, 2009
3	Control Systems Engineering	I. J. Nagrath, M. Gopal	New Age	5th edition, 2009

Reference Books

Sl. No	Title of the Book	Name of the Author/s	Name of the Publisher	Edition and Year
1	Automatic Control Systems,	Kuo B. C.	Prentice Hall of India	

2	Control Systems Principles and Design	Gopal M.	Tata McGraw Hill.	
3	Modern Control Systems	Dorf R. C., Bishop R. H	Pearson Education India	

Continuous Assessment (25 Marks)

1. Preparation and Pre-Lab Work (7 Marks)

- Pre-Lab Assignments: Assessment of pre-lab assignments or quizzes that test understanding of the upcoming experiment.
- Understanding of Theory: Evaluation based on students' preparation and understanding of the theoretical background related to the experiments.

2. Conduct of Experiments (7 Marks)

- Procedure and Execution: Adherence to correct procedures, accurate execution of experiments, and following safety protocols.
- Skill Proficiency: Proficiency in handling equipment, accuracy in observations, and troubleshooting skills during the experiments.
- Teamwork: Collaboration and participation in group experiments.

3. Lab Reports and Record Keeping (6 Marks)

- Quality of Reports: Clarity, completeness and accuracy of lab reports. Proper documentation of experiments, data analysis and conclusions.
- Timely Submission: Adhering to deadlines for submitting lab reports/rough record and maintaining a well-organized fair record.

4. Viva Voce (5 Marks)

- Oral Examination: Ability to explain the experiment, results and underlying principles during a viva voce session.

Final Marks Averaging: *The final marks for preparation, conduct of experiments, viva, and record are the average of all the specified experiments in the syllabus.*

Evaluation Pattern for End Semester Examination (50 Marks)

1. Procedure/Preliminary Work/Design/Algorithm (10 Marks)

- Procedure Understanding and Description: Clarity in explaining the procedure and understanding each step involved.
- Preliminary Work and Planning: Thoroughness in planning and organizing materials/equipment.

- Algorithm Development: Correctness and efficiency of the algorithm related to the experiment.
- Creativity and logic in algorithm or experimental design.

2. Conduct of Experiment/Execution of Work/Programming (15 Marks)

- Setup and Execution: Proper setup and accurate execution of the experiment or programming task.

3. Result with Valid Inference/Quality of Output (10 Marks)

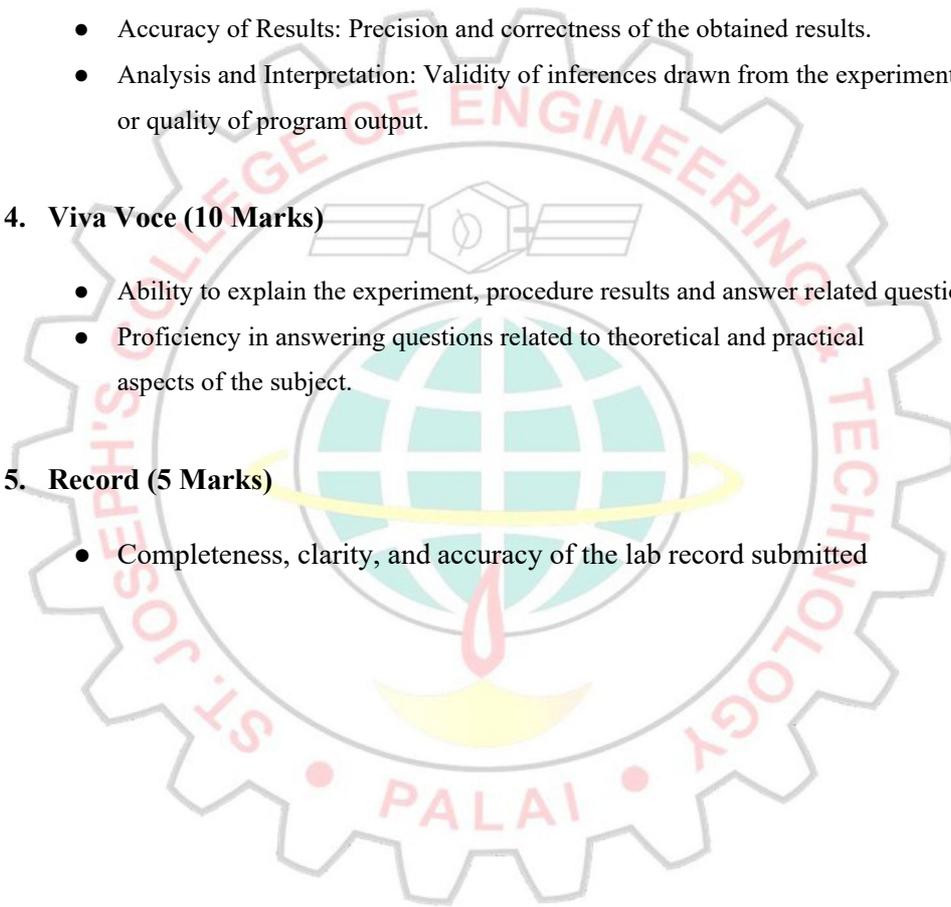
- Accuracy of Results: Precision and correctness of the obtained results.
- Analysis and Interpretation: Validity of inferences drawn from the experiment or quality of program output.

4. Viva Voce (10 Marks)

- Ability to explain the experiment, procedure results and answer related questions
- Proficiency in answering questions related to theoretical and practical aspects of the subject.

5. Record (5 Marks)

- Completeness, clarity, and accuracy of the lab record submitted



SEMESTER S6

MINI PROJECT: Socially Relevant Project

Course Code	24SJPCEEP608	CIE Marks	50
Teaching Hours/Week (L:T:P:R)	0:0:0:3	ESE Marks	50
	2	Exam Hours	NA
Prerequisites (if any)	None	Course Type	Project

Preamble: The objective of this course is to apply the fundamental concepts of Engineering discipline principles for the effective development of an application/research project. Mini project enables the students to boost their skills, widen the horizon of thinking and their ability to resolve real life problems. The students are expected to design and develop a software/hardware project to innovatively solve a real-world problem.

Course Assessment Method (CIE: 50 marks, ESE: 50 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Project Guide	Evaluation by the Committee (will be evaluating the level of completion and demonstration of functionality/specifications, presentation, oral examination, work knowledge and involvement)	Project Report	Total
5	15	20	10	50

End Semester Examination Marks (ESE)

Presentation	Demonstration	Viva	Total
20	20	10	50

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Identify technically and economically feasible problems of social relevance	K3
CO2	Identify and survey the relevant literature for getting exposed to related solutions	K3
CO3	Perform requirement analysis and identify design methodologies and develop adaptable and reusable solutions of minimal complexity by using modern tools and advanced programming techniques	K3
CO4	Prepare technical report and deliver presentation	K3
CO5	Apply engineering and management principles to achieve the goal of the project	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO-PO Mapping Table:

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	2	2	2			2					
CO2	2		2								2
CO3	2	2	2	3	3		2				
CO4	2							2	2		2
CO5	2							2		3	

Course Plan

Student Groups with 3 or 4 members should identify a topic of interest in consultation with Faculty/Advisor. Review the literature and gather information pertaining to the chosen topic. State the objectives and develop a methodology to achieve the objectives. Carryout the design/fabrication or develop codes/programs to achieve the objectives. Innovative design concepts, performance, scalability, reliability considerations, aesthetics/ergonomic, user experience and security aspects taken care of in the project shall be given due weight.

The progress of the mini project is evaluated based on a minimum of two reviews. The review committee may be constituted with the Head of the Department or a senior faculty, Mini Project coordinator and project guide as the members. Innovative design concepts, reliability considerations, aesthetics/ergonomic aspects taken care of in the project shall be given due weight. The internal evaluation shall be made based on the progress/outcome of the project, reports and a viva-voce examination, conducted internally by a 3-member committee. A project report is required at the end of the semester. The product/application has to be demonstrated for its full design specifications.

Guidelines for the Report preparation

A bonafide report on mini project shall be submitted within one week after the final presentation. Minimum number of pages should be 40.

- Use Times New Roman font for the entire Report – Chapter / Section Title –Times New Roman 18, Bold; Heading 2 – Times New Roman 16, Bold; Heading 3 – Times New Roman 14, Bold; Body- Times New Roman12, Normal.
- Line Spacing – Between Heading 2 – 3 lines, between lines in paragraph 1.5 lines.
- Alignments – Chapter / Section Title – Center, Heading 2 & 3 should be Left Aligned. Ensure that all body text is paragraph justified.
- Figures & Tables – Ensure that all Figures and Tables are suitably numbered and given proper names/headings. Write figure title under the figure and table title above the table
- Suggestive order of documentation:
 - i. Top Cover
 - ii. Title page
 - iii. Certification page
 - iv. Acknowledgement
 - v. Abstract
 - vi. Table of Contents
 - vii. List of Figures and Tables
 - viii. Chapters
 - ix. Appendices, if any
 - x. References/Bibliography

SEMESTER S6
POWER SYSTEM LAB

Course Code	24SJPCEEL609	CIE Marks	50
Teaching Hours/Week (L: T:P: R)	0:0:2:0	ESE Marks	50
Credits	1	Exam Hours	2 Hrs. 30 Min.
Prerequisites (if any)	24SJPCEET501	Course Type	Lab

Course Objectives:

1. To encourage students learn through analytical problem solving and practical implementation.
2. To motivate the students for self-learning
3. To make them ready for practical implementation of the knowledge that they have gained from theory.

Expt. No.	Experiments
	Software
1	Y-Bus formulation: Aim: (i) To formulate the bus admittance matrix of the given power system from its single line diagram, using basic MATLAB programming. (ii) To incorporate changes in basic topology.
2	Transmission Line Modelling: ABCD constants Aim: (i) To model the given medium transmission line using nominal T and nominal pi representation and to derive the ABCD constants using basic MATLAB programming.
3	Load Flow Analysis - Gauss-Siedel Method, Newton - Raphson Method, Fast Decoupled Method - Aim: (i) To conduct load flow analysis using Gauss-Siedel method, Newton- Raphson method, Fast Decoupled method and to study the effect of change in load/generation schedule.
4	Load Flow Analysis - Gauss-Siedel Method, Newton - Raphson Method, Fast Decoupled Method Aim: (i) To conduct load flow analysis using Gauss-Siedel method, Newton-Raphson method, Fast Decoupled method and to study the effect of change in real power/reactive power limits.
5	Short Circuit Analysis - Symmetrical Faults and Unsymmetrical Faults Aim: (i) To conduct short circuit analysis for symmetrical and unsymmetrical faults.
6	Transient Stability Analysis Aim: To conduct transient stability analysis of a given system and plot suitable graphs using MATLAB Simulink or dedicated software (if available)

7	Automatic Generation Control - Single Area, Two Area Aim: To implement Automatic Generation Control in MATLAB Simulink.
8	Automatic Voltage Regulator Aim: To implement Automatic Voltage Regulator in MATLAB Simulink.
9	Ferranti Effect and Reactive Power Compensation Aim: (i) To exhibit Ferranti effect in a lightly loaded long transmission line in MATLAB Simulink and to show the effect of reactive power compensation. (ii) To calculate Surge Impedance Loading of the line
10	Plot the IV characteristics of a PV module and determine Maximum Power Point Aim: To plot the IV characteristics of a PV module in MATLAB Simulink and determine the Maximum Power Point
Hardware	
11	High Voltage Testing - Power frequency /impulse
12	High Voltage Testing - DC
13	Relay Testing - Over current Relay / Earth Fault (Electromechanical / Static /Numerical) Aim: To draw the characteristics of the given relay.
14	Relay Testing -Voltage relay/ Impedance Relay (Electromechanical/Static/Numerical) Aim: To draw the characteristics of the given relay.
15	Insulation Testing - LT & HT Cable Aim: To determine the insulation resistance of the given LT & HT cable.
16	Testing of CT and PT Aim: To conduct ratio test of the given CT and PT.
17	Testing of transformer oil Aim: To determine the dielectric strength of the given sample of transformer oil.
18	Testing of dielectric strength of solid insulating materials Aim: To determine the dielectric strength of the solid insulating material given.
19	Testing of dielectric strength of air Aim: To determine the dielectric strength of air.
20	Power factor improvement Aim: To calculate the power factor of the given RL series circuit (transmission line) and design the capacitance required to improve the power factor to the desired value.

Course Assessment Method
(CIE: 50 marks, ESE: 50 marks)

Continuous Internal Evaluation Marks (CIE):

Attendance	Preparation/Pre-Lab Work experiments, Viva and Timely completion of Lab Reports / Record (Continuous Assessment)	Internal Examination	Total
5	25	20	50

End Semester Examination Marks (ESE):

Procedure/ Preparatory work/Design/ Algorithm	Conduct of experiment/ Execution of work/ troubleshooting/ Programming	Result with valid inference/ Quality of Output	Viva voce	Record	Total
10	15	10	10	5	50

- **Submission of Record:** Students shall be allowed for the end semester examination only upon submitting the duly certified record.
- **Endorsement by External Examiner:** The external examiner shall endorse the record

Course Outcomes (COs)

At the end of the course students should be able to:

Course Outcome		Bloom's Knowledge Level (KL)
CO1	Develop mathematical models and conduct steady state and transient analysis of power system networks using standard / dedicated software.	K3
CO2	Conduct appropriate tests for any power system component as per standards to analyse their performance.	K3

Note: K1- Remember, K2- Understand, K3- Apply, K4- Analyse, K5- Evaluate, K6- Create

CO- PO Mapping (Mapping of Course Outcomes with Program Outcomes)

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	3	3	3	3	3			3	3	3	3
CO2	3	3	3	3	3			3	3	3	3

1: Slight (Low), 2: Moderate (Medium), 3: Substantial (High), No Correlation

Continuous Assessment (25 Marks)

1. Preparation and Pre-Lab Work (7 Marks)

- Pre-Lab Assignments: Assessment of pre-lab assignments or quizzes that test understanding of the upcoming experiment.
- Understanding of Theory: Evaluation based on students' preparation and understanding of the theoretical background related to the experiments.

2. Conduct of Experiments (7 Marks)

- Procedure and Execution: Adherence to correct procedures, accurate execution of experiments, and following safety protocols.
- Skill Proficiency: Proficiency in handling equipment, accuracy in observations, and troubleshooting skills during the experiments.
- Teamwork: Collaboration and participation in group experiments.

3. Lab Reports and Record Keeping (6 Marks)

- Quality of Reports: Clarity, completeness and accuracy of lab reports. Proper documentation of experiments, data analysis and conclusions.
- Timely Submission: Adhering to deadlines for submitting lab reports/rough record and maintaining a well-organized fair record.

4. Viva Voce (5 Marks)

- Oral Examination: Ability to explain the experiment, results and underlying principles during a viva voce session.

Final Marks Averaging: The final marks for preparation, conduct of experiments, viva, and record are the average of all the specified experiments in the syllabus.

Evaluation Pattern for End Semester Examination (50 Marks)

1. Procedure/Preliminary Work/Design/Algorithm (10 Marks)

Procedure Understanding and Description: Clarity in explaining the procedure and understanding each step involved.

- Preliminary Work and Planning: Thoroughness in planning and organizing materials/equipment.
- Algorithm Development: Correctness and efficiency of the algorithm related to the experiment.
- Creativity and logic in algorithm or experimental design.

2. Conduct of Experiment/Execution of Work/Programming (15 Marks)

- Setup and Execution: Proper setup and accurate execution of the experiment or programming task.

3. Result with Valid Inference/Quality of Output (10 Marks)

- Accuracy of Results: Precision and correctness of the obtained results.
- Analysis and Interpretation: Validity of inferences drawn from the experiment or quality of program output.

4. Viva Voce (10 Marks)

- Ability to explain the experiment, procedure results and answer related questions
- Proficiency in answering questions related to theoretical and practical aspects of the subject.

5. Record (5 Marks)

Completeness, clarity, and accuracy of the lab record submitted

Programme Outcomes (POs)

PO1: Engineering Knowledge: Apply knowledge of mathematics, natural science, computing, engineering fundamentals and an engineering specialization as specified in WK1 to WK4 respectively to develop to the solution of complex engineering problems.

PO2: Problem Analysis: Identify, formulate, review research literature and analyse complex engineering problems reaching substantiated conclusions with consideration for sustainable development. (WK1 to WK4)

PO3: Design/Development of Solutions: Design creative solutions for complex engineering problems and design/develop systems/components/processes to meet identified needs with consideration for the public health and safety, whole-life cost, net zero carbon, culture, society and environment as required. (WK5)

PO4: Conduct Investigations of Complex Problems: Conduct investigations of complex engineering problems using research-based knowledge including design of experiments, modelling, analysis & interpretation of data to provide valid conclusions. (WK8).

PO5: Engineering Tool Usage: Create, select and apply appropriate techniques, resources and modern engineering & IT tools, including prediction and modelling recognizing their limitations to solve complex engineering problems. (WK2 and WK6)

PO6: The Engineer and The World: Analyse and evaluate societal and environmental aspects while solving complex engineering problems for its impact on sustainability with reference to economy, health, safety, legal framework, culture and environment. (WK1, WK5, and WK7).

PO7: Ethics: Apply ethical principles and commit to professional ethics, human values, diversity and inclusion; adhere to national & international laws. (WK9)

PO8: Individual and Collaborative Team work: Function effectively as an individual, and as a member or leader in diverse/multi-disciplinary teams.

PO9: Communication: Communicate effectively and inclusively within the engineering community and society at large, such as being able to comprehend and write effective reports and design documentation, make effective presentation considering cultural, language, and learning differences.

PO10: Project Management and Finance: Apply knowledge and understanding of engineering management principles and economic decision-making and apply these to one's own work, as a member and leader in a team, and to manage projects and in multidisciplinary environments.

PO11: Life-Long Learning: Recognize the need for, and have the preparation and ability for i) independent and life-long learning ii) adaptability to new and emerging technologies and iii) critical thinking in the broadest context of technological change. (WK8)

Knowledge and Attitude Profile (WK)

WK1: A systematic, theory-based understanding of the natural sciences applicable to the discipline and awareness of relevant social sciences.

WK2: Conceptually-based mathematics, numerical analysis, data analysis, statistics and formal aspects of computer and information science to support detailed analysis and modelling applicable to the discipline.

WK3: A systematic, theory-based formulation of engineering fundamentals required in the engineering discipline.

WK4: Engineering specialist knowledge that provides theoretical frameworks and bodies of knowledge for the accepted practice areas in the engineering discipline; much is at the forefront of the discipline.

WK5: Knowledge, including efficient resource use, environmental impacts, whole-life cost, re-use of resources, net zero carbon, and similar concepts, that supports engineering design and operations in a practice area.

WK6: Knowledge of engineering practice (technology) in the practice areas in the engineering discipline.

WK7: Knowledge of the role of engineering in society and identified issues in engineering practice in the discipline, such as the professional responsibility of an engineer to public safety and sustainable development.

WK8: Engagement with selected knowledge in the current research literature of the discipline, awareness of the power of critical thinking and creative approaches to evaluate emerging issues.

WK9: Ethics, inclusive behaviour and conduct. Knowledge of professional ethics, responsibilities, and norms of engineering practice. Awareness of the need for diversity by reason of ethnicity, gender, age, physical ability etc. with mutual understanding and respect, and of inclusive attitudes.

Department of Electrical and Electronics Engineering

● — Vision — ●

Develop technically competent, emotionally strong and socially committed Electrical & Electronics Engineering professionals of international excellence.

● — Mission — ●

- To develop and maintain a conducive infrastructure and learning-environment, to bring out good quality Electrical & Electronics Engineering graduates.
- To appoint and retain a team of competent, dedicated and research-oriented faculty.
- To inculcate ethical & moral values among students and faculty.

● — Program Specific Outcomes (PSOs): — ●

Graduates of the program will be able to:

- Apply knowledge of mathematics, science and engineering to design, commission and maintain various types of electrical systems and address challenges in the field.
- Derive sustainable solutions to complex electrical engineering problems that meet the specified needs with ethical, social and environmental considerations.
- Empower the students for lifelong learning so as to adapt to dynamic changes in Electrical Engineering.



ST. JOSEPH'S

COLLEGE OF ENGINEERING
AND TECHNOLOGY,
- PALAI -

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Vision

Developing into a world class, pace setting institute of Engineering and Technology with distinct identity and character, meeting the goals and aspirations of the society.

Mission

- To maintain a conducive infrastructure and learning environment for world class education.
- To nurture a team of dedicated, competent and research-oriented faculty.
- To develop students with moral and ethical values, for their successful careers, by offering variety of programs and services.