



ST. JOSEPH'S

COLLEGE OF ENGINEERING
AND TECHNOLOGY.
- PALAI -
AUTONOMOUS

SJCET/OCOE/IE/2026/167

Date: 16-10-2026

Notification

The Second Internal Retest Examination for the Third and Fifth Semester B.Tech. programmes, Third Semester MCA programme, and Third and Fifth Semester Integrated MCA programmes (2024 Scheme) is scheduled to be conducted from 22nd October 2026 (Thursday) to 26th October 2026 (Monday).

Only students who have a valid reason for missing the examination will be permitted to appear. They must produce supporting documents along with permission from the Head of the Department at the time of entry to the examination hall.

The detailed examination schedule is as follows:

Examination Timings

For B.Tech. Programmes

- Forenoon Session (FN): 9:30 AM – 10:45 AM
- Afternoon Session (AN): 2:45 PM – 4:00 PM

For MCA and Integrated MCA Programmes

- Forenoon Session (FN): 9:30 AM – 11:00 AM
- Afternoon Session (AN): 2:45 PM – 4:15 PM

Students are instructed to report to the examination hall at least 10 minutes before the commencement of the examination and carry their College ID Card. They shall strictly adhere to the examination regulations and maintain proper discipline throughout the examination period.

Dr. Nidhish Mathew Nidhiry
Controller of Examinations
SJCET Palai

Copy to:

1. Director – for kind information
2. Principal
3. Vice-Principal
4. Bursar
5. Lab Manager (Soft Copy)
6. All HoDs – for informing the concerned staff members and students
7. Website Administrator
8. Circulated among students
9. College Notice Board
10. Notice File / File Copy / e-Records





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Second Internal Retest Exam October 2026- S5 BTech (2024 Scheme)

Date	Session	Course Code	Course Name	Branch	Slot
22/10/2026	AN		MINOR		
23/10/2026	FN	24SJPCCEET501	HYDROLOGY AND WATER RESOURCES ENGINEERING	CE	A
		24SJPCMET501	DYNAMICS OF MACHINERY	ME	A
		24SJPCCEST501	COMPUTER NETWORKS	AD, CA, CS	A
		24SJPCCECT501	APPLIED CRYPTOGRAPHY	CA	A
		24SJPCCEET501	POWER GENERATION, TRANSMISSION AND PROTECTION	EE	A
		24SJPCCECT501	ELECTROMAGNETICS	EC	A
		24SJPCERT501	DIGITAL SIGNAL PROCESSING	ER	A
23/10/2026	AN		HONOURS		
24/10/2026	FN	24SJPCCEET502	TRANSPORTATION ENGINEERING	CE	B
		24SJPCMET502	ADVANCED MANUFACTURING ENGINEERING	ME	B
		24SJPCADT502	ROBOTICS AND INTELLIGENT SYSTEMS	AD	B
		24SJPCCEST502	DESIGN AND ANALYSIS OF ALGORITHMS	CS	B
		24SJPCCECT502	NETWORK AND SYSTEM SECURITY	CC	B
		24SJPCCEET502	ELECTROMAGNETIC THEORY	EE	B
		24SJPCCECT502	ANALOG AND DIGITAL COMMUNICATION	EC	B
		24SJPCERT502	THEORY OF COMPUTATION	ER	B
		24SJPCCAT502	THEORY OF COMPUTATION	CA	B
24/10/2026	AN	24SJPCCEET503	ENVIRONMENTAL ENGINEERING	CE	C
		24SJPCMET503	HEAT AND MASS TRANSFER	ME	C
		24SJPCCEST503	MACHINE LEARNING	AD, CA, CC, CS	C
		24SJPCCEET503	SIGNALS AND SYSTEMS	EE	C
		24SJPCCECT503	CONTROL SYSTEMS	EC	C
		24SJPCERT503	MICROCONTROLLERS AND INTERFACING	ER	C



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26/10/2026		24SJBCET504	FOUNDATION ENGINEERING	CE	D
		24SJPBMET504	MANAGEMENT FOR ENGINEERS	ME	D
		24SJPBADT504	BIG DATA ANALYTICS	AD	D
		24SJBCST504	MICROCONTROLLERS	CS, CC	D
		24SJBCAT504	ADVANCED GRAPH ALGORITHMS	CA	D
A	FN	24SJBEET504	MICROPROCESSOR AND EMBEDDED SYSTEMS	EE	D
B		24SJBECT504	DIGITAL SIGNAL PROCESSING	EC	D
C		24SJBERT504	DATABASE MANAGEMENT SYSTEMS	ER	D
D		24SJPEADT522	BUSINESS ANALYTICS	AD	E
E		24SJPEADT525	FUNDAMENTALS OF DIGITAL IMAGE PROCESSING	AD	E
26/10/2026	AN	24SJPECST521	SOFTWARE PROJECT MANAGEMENT	CS	E
		24SJPECST522	ARTIFICIAL INTELLIGENCE	CS	E
		24SJPECCT521	NETWORK FUNDAMENTALS FOR CLOUD	CC	E
		24SJPECCT522	BLOCK CHAIN AND CRYPTO CURRENCY	CC	E
		24SJPECCT524	ADVANCED INDUSTRIAL CYBER SECURITY	CC	E
		24SJPECAT522	ARTIFICIAL NEURAL NETWORKS TECHNIQUES	CA	E
		24SJPECET524	DISASTER MANAGEMENT	CE	E
		24SJPEET522	ELECTRIC VEHICLES	EE	E
		24SJPEECT523	SENSORS AND ACTUATORS	EC	E
		24SJPEECT521	BIOMEDICAL ENGINEERING	EC	E
		24SJPEERT523	SENSORS AND ACTUATORS	ER	E
		24SJPEERT524	CLOUD COMPUTING	ER	E
		24SJPEMET524	ADDITIVE MANUFACTURING	ME	E
		24SJPEMET527	HUMAN RESOURCES MANAGEMENT	ME	E



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Second Internal Retest Exam October 2026- S5 MCA Integrated (2024 Scheme)

Date	Slot	Course Code	Subject Name	Slot
22/10/2026	AN	24SJINMCA301	Numerical Methods	A
23/10/2026	FN	24SJINMCA303	User Interface Design	B
23/10/2026	AN	24SJINMCA305	Introduction to RDBMS and SQL	C
24/10/2026	FN	24SJINMCA307	Fundamentals of Information Systems Security	D
24/10/2026	AN	24SJINMCA309	Introduction to Operations Research	E



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Second Internal Retest Exam October 2026- S3 BTech (2024 Scheme)

DATE	SESSION	COURSE CODE & NAME	SLOT	PROGRAMME(S)
22/10/2026	AN	MINOR		All Branches
23/10/2026	FN	24SJGAMAT301 – MATHEMATICS FOR COMPUTER AND INFORMATION SCIENCE-3	A	AD, CA, CC, CS
		24SJGYMAT301 – MATHEMATICS FOR ELECTRICAL SCIENCE AND PHYSICAL SCIENCE-3	A	CE, EE, EC, ER, ME
23/10/2026	AN	24SJPCAIT302 – FOUNDATIONS OF ARTIFICIAL INTELLIGENCE	B	AD
		24SJPC CET302 – FLUID MECHANICS	B	CE
		24SJPC CST302 – THEORY OF COMPUTATION	B	CC, CS
		24SJPC ECT302 – SOLID STATE DEVICES	B	EC
		24SJPC EET302 – CIRCUITS AND NETWORKS	B	EE
		24SJPC MET302 – MECHANICS OF SOLIDS	B	ME
		24SJPCERT302 – DATA STRUCTURES	B	ER
		24SJPCCAT302 – INTRODUCTION TO ARTIFICIAL INTELLIGENCE	B	CA



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24/10/2026	FN	24SJPCST303 – DATA STRUCTURES AND ALGORITHMS	C	AD, CA, CC, CS
		24SJPCET303 – STRUCTURAL ANALYSIS – I	C	CE
		24SJPECT303 – ANALOG CIRCUITS	C	EC
		24SJPEET303 – DC MACHINES AND TRANSFORMERS	C	EE
		24SJPCMET303 – FLUID MECHANICS AND MACHINERY	C	ME
		24SJPCERT303 – DIGITAL SYSTEM DESIGN USING VERILOG	C	ER
24/10/2026	AN	24SJPBADT304 – INTRODUCTION TO DATA SCIENCE	D	AD
		24SJBCET304 – SURVEYING AND GEOMATICS	D	CE
		24SJBCST304 – OBJECT ORIENTED PROGRAMMING	D	CA, CS
		24SJPBECT304 – LOGIC CIRCUIT DESIGN	D	EC
		24SJBEET304 – ANALOG ELECTRONICS	D	EE
		24SJBMET304 – MANUFACTURING PROCESSES	D	ME



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		24SJPBERT304 – ELECTRONIC DEVICES AND CIRCUITS	D	ER
		24SJPBCCT304 – BASIC CONCEPTS IN COMPUTER NETWORKS	D	CC
		24SJGAEST305 – DIGITAL ELECTRONICS AND LOGIC DESIGN	F	AD, CA, CC, CS
26/10/2026	FN	24SJGYEST305 – INTRODUCTION TO ARTIFICIAL INTELLIGENCE AND DATA SCIENCE	F	CE, EE, EC, ER, ME
26/10/2026	AN	24SJCHUT346 – ECONOMICS FOR ENGINEERS	G	AD, CE, EE, EC, ER, ME

Second Internal Retest Exam October 2026- S3 MCA (2024 Scheme)

DATE	SESSION	COURSE CODE & NAME	SLOT
23/10/2026	FN	24SJMCA201-DATA SCIENCE & MACHINE LEARNING	A
23/10/2026	AN	24SJMCA203-DESIGN & ANALYSIS OF ALGORITHMS	B
24/10/2026	FN	24SJMCA263-CYBER SECURITY & CRYPTOGRAPHY	C
		24SJMCA265-CLOUD COMPUTING	
24/10/2026	AN	24SJMCA281-INTERNET OF THINGS	D
		24SJMCA283-DEEP LEARNING	



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Second Internal Retest Exam October 2026- S3 MCA Integrated (2024 SCHEME)

DATE	SESSION	COURSE CODE & NAME	SLOT
22/10/2026	AN	24SJINMCA201-COMPUTER ORGANIZATION	A
23/10/2026	FN	24SJINMCA203-PROBABILITY AND STATISTICS	B
23/10/2026	AN	24SJINMCA205-INTRODUCTION TO OBJECT ORIENTED PROGRAMMING	C
24/10/2026	FN	24SJINMCA207-BASICS OF DATA ANALYTICS	D
24/10/2026	AN	24SJINMCA209-DATA STRUCTURES	E

Second Internal Retest Exam October 2026- S3 MCA (2024 Scheme)

DATE	SESSION	COURSE CODE & NAME	SLOT
23/10/2026	FN	24SJINMCA201-DATA SCIENCE & MACHINE LEARNING	A
23/10/2026	AN	24SJINMCA203-DESIGN & ANALYSIS OF ALGORITHMS	B
24/10/2026	FN	24SJINMCA205-CYBER SECURITY & CRYPTOGRAPHY	C
24/10/2026	AN	24SJINMCA207-CLOUD COMPUTING	D
24/10/2026	FN	24SJINMCA209-INTERNET OF THINGS	E
24/10/2026	AN	24SJINMCA201-DEEP LEARNING	F