About the Institution

The establishment of St. Joseph's College of Engineering and Technology, Palai, was the fulfilment of a long cherished dream of providing facilities for higher education to the people of the diocese and surrounding regions. The main objective is to develop a college with a distinct identity and character, where education and training are imparted in a truly Christian environment conducive to fostering Christian values such as faith in God, love for their fellow men and devotion to the motherland. Every facility is provided in the campus to create an environment fully conducive to realizing this objective.

Discipline, hard work, positive thinking, commitment to excellence and abiding faith in the Almighty are the guiding principles that propel the college to its vision of emerging as a Centre of Excellence in technical education in the country. Value systems such as eco-friendliness, quality consciousness and work ethics are also being instilled through the special work culture and campus life existing in the college.

VISION

Developing into a world-class, pace-setting Institute of Engineering and Technology with distinct identity and character, meeting the goals and aspirations of the society.

MISSION

- To maintain a conducive infrastructure and learning environment for world class education.
- To nurture a team of dedicated, competent and research oriented faculty.
- To develop students with moral & ethical values, for their successful career by offering variety of programmes and services.

Organizing Committee

CHIEF PATRON

Msgr. Dr. Joseph Thadathil
Chairman, SJCET Palai

PATRONS

Rev. Prof. Dr. James John Mangalathu
Director, SJCET Palai

Dr. V. P. Devassia
Principal, SJCET Palai

Rev. Dr. Joseph Purayidathil
Vice-Principal, SJCET Palai

CONVENER

Dr. Giby Jose
Head of the Department, Electronics and Computer Engineering

ORGANIZING COMMITTEE

Ms. Soya Treesa Jose, DQAC Coordinator
Ms. Tinu Thomas, Coordinator
Ms. Sreelakshmi K R, Co-coordinator

CONTACT

Ms. Tinu Thomas – 9526725379
Ms. Sreelakshmi K R – 8281030360

ST. JOSEPH'S COLLEGE OF ENGINEERING AND TECHNOLOGY
- PALAI -
AUTONOMOUS

SJCET PALAI
2002 - 2027
FROM STRONG FOUNDATIONS TO BOUNDLESS HORIZONS

DEPARTMENT OF ELECTRONICS AND COMPUTER ENGINEERING

SIX-DAY HYBRID FACULTY DEVELOPMENT PROGRAMME

MACHINE LEARNING:

From Fundamentals to Emerging AI

Python • scikit-learn • PyTorch • Transformers • LLMs & RAG • Hands-on Labs

30 NOV – 5 DEC 2026

Days 1–5 Online · 7:00 – 9:00 PM

Day 6 Offline · 9:30 AM – 4:00 PM

Organised by

Department of Electronics and Computer Engineering

www.sjcetpalai.ac.in

About the Department

B. Tech in Electronics and Computer Engineering (ES) is a four-year professional undergraduate program established in the year 2021 with an annual intake of 60 students.

Electronics and Computer Engineering is an integrated program which focuses on imparting skills in electronics hardware, software and IT, as required by the industry. The curriculum builds a solid foundation in Electronic Circuits, Digital System Design, VLSI Design, Embedded Systems & IoT, Data Structures, Object Oriented Programming, Computer Networks, Database Management Systems and more.

Merging the two disciplines offers a strong foundation in both subject areas and prepares aspirants for a wide range of careers in either or both fields, moulding multi-skilled engineers with a meticulous understanding of fundamental concepts and techniques.

DEPARTMENT VISION

Developing into a centre of excellence in Electronics and Computer Engineering for training technically competent professionals to cater to the needs of Industry, Academia and Society.

DEPARTMENT MISSION

- To pursue continuous improvement in learning, creativity and innovation among both faculty and students by enhanced infrastructure, state-of-the art laboratories and a unique learning environment.
- To inculcate in both faculty and students, technical and entrepreneurial skills by professional activities to create socially relevant and sustainable solutions in the domain of Electronics and Computer Engineering.

PROGRAMME EDUCATIONAL OBJECTIVES (PEO)

PEO1: Graduates will be professionally successful in diverse career paths including supportive and leadership roles or will pursue higher education.

PEO2: Graduates will demonstrate technical competence to provide solutions to societal challenges by collaborating in multidisciplinary teams.

PEO3: Graduates will be receptive to new technologies, lifelong learning and professional ethics to provide sustainable solutions in Electronics and Computer Engineering.

PROGRAMME SPECIFIC OUTCOMES (PSO)

PSO1: Ability to apply fundamental knowledge to analyse and design hardware and software systems so as to understand and solve problems in Electronics and Computer Engineering.

PSO2: Ability to effectively use modern software/hardware tools to design, analyze and communicate technical details in the Electronics and Computer Engineering domain.

Resource Persons

DAY 1 · 30 Nov

Foundations of Machine Learning

Dr. Vipin Venugopal

Assistant Professor (Selection Grade), School of Artificial Intelligence, Amrita Vishwa Vidyapeetham, Coimbatore

DAY 2 · 1 Dec

Advanced Machine Learning

Dr. Malaya Kumar Nath

Assistant Professor, Dept. of ECE, National Institute of Technology Puducherry, Karaikal

DAY 3 · 2 Dec

Deep Learning & Sequence Models

Mr. Muhammed Raees P C

Founder & CEO, REYRA AI – An Initiative of Edutecnicia Pvt. Ltd., Calicut

DAY 4 · 3 Dec

Transformers & Generative Models

Dr. Mithun Kumar Kar

Assistant Professor (Senior Grade), School of Artificial Intelligence, Amrita Vishwa Vidyapeetham, Coimbatore

DAY 5 · 4 Dec

LLMs & Applied Generative AI

Prof. Ameenudeen P E

Assistant Professor, Dept. of ECE, College of Engineering Trivandrum

DAY 6 · 5 Dec

Hands-on Labs: ML, DL & RAG (Offline)

Mr. Siju K S

Associate Professor, Saintgits College of Engineering, Kottayam

About the FDP

Machine Learning, Deep Learning, Transformers, Generative AI and LLMs are now core components of engineering curricula and industry practice. This hands-on programme builds knowledge progressively, from classical machine learning to deep learning, transformers, generative models and LLM-based applications, with emphasis on practical implementation using Python, scikit-learn and PyTorch.

WHO CAN ATTEND

Faculty members of engineering colleges and research scholars.

WHAT YOU WILL GAIN

- The complete ML workflow, from data preparation to evaluation
- Deep learning in PyTorch: CNN, RNN, LSTM and GRU
- Attention, Transformers, VAEs, GANs and diffusion models
- LLM applications with prompt engineering, RAG and LoRA fine-tuning
- Lab exercises and assessments for ML, DL and LLM courses

DAYS 1–5 · ONLINE
7:00 – 9:00 PM
Google Meet · Colab

DAY 6 · OFFLINE
9:30 AM – 4 PM
Software Computing Lab

REGISTRATION FEE: ₹250

SCAN TO REGISTER

SCAN TO PAY

NAAC Accredited with grade A in First cycle

ISO 9001:2015 Certified Institution

ISO 14001:2015 Certified Institution

A+ Graded Green Institution